



100V INPUT, 12V 30mA REGULATOR TRANSISTOR

Description

The ZXTR2012Z monolithically integrates a transistor, Zener diode and resistor to function as a high voltage linear regulator. The device regulates with a 12V nominal output at 15mA. It is designed for use in high voltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT89 package, minimizing PCB area and reducing number of components when compared with a multi-chip discrete solution.

Applications

Supply Voltage Regulation in:

- Startup Switch in DC-DC Converters
- Networking
- Telecommunications
- Power over Ethernet (PoE)

SOT89

Top View

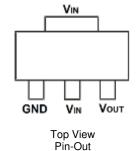
ZXTR2012 V_{IN} V_{OUT} 150kΩ GND Internal Device Schematic

Features

- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 15V to 100V (For Regulated Output Voltage)
- Output Voltage = 12V ± 10%
- 150kΩ Resistor To Limit Quiescent Current
- Fully Integrated into a SOT89 Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 for High Reliability

Mechanical Data

- Case: SOT89
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.052 grams (Approximate)



Pin Name	Pin Function
V _{IN}	Input Supply
GND	Power Ground
V _{OUT}	Voltage Output

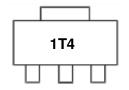
Ordering Information (Note 4)

Product	Package	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXTR2012Z-7	SOT89	1T4	7	12	1,000
ZXTR2012Z-13	SOT89	1T4	13	12	2,500

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



1T4 = Product Type Marking Code



Absolute Maximum Ratings (Voltage relative to GND, @TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Supply Voltage	V _{IN}	-0.3 to 100	V
Continuous Input & Output Current	I _{IN} , I _{OUT}	550	mA
Peak Pulsed Input & Output Current	I _{IM} , I _{OM}	2	Α
Maximum Voltage applied to V _{OUT}	V _{OUT(MAX)}	Smaller of V _{IN+} 12V or 18V	V

Maximum Current at V_{IN} = 48V (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Continuous Output Current	(Note 7)	Іоит	47	mA
Pulsed Output Current	(Note 8)	,	880	mΛ
Pulsed Output Current	(Note 9)	Іом	180	mA

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	В	1.7	W
Power dissipation	(Note 6)	P _D	0.89	
Thermal Resistance, Junction to Ambient	(Note 5)	В	59	
	(Note 6)	R _{0JA}	112	°C/W
Thermal Resistance, Junction to Lead	(Note 10)	$R_{\theta JL}$	20	- C/VV
Thermal Resistance, Junction to Case	(Note 10)	ReJC	15.7	
Recommended Operating Junction Temperature Range		TJ	-40 to +125	°C
Maximum Operating Junction and Storage Temperature Range		T _J , T _{STG}	-65 to +150	

ESD Ratings (Note 11)

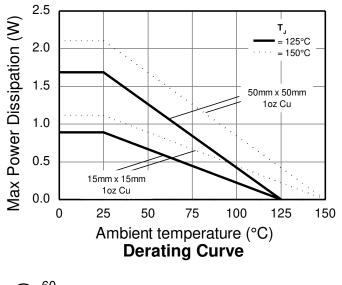
Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	С

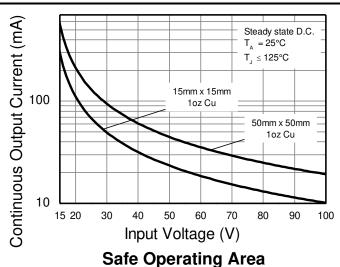
Notes:

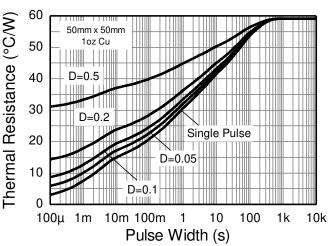
- 5. For a device mounted with the exposed V_{IN} pad on 50mm x 50mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.
- 6. Same as note 5, except mounted on 15mm x 15mm 1oz copper.
- 7. Same as note 5, whilst operating at $V_{IN} = 48V$. Refer to Safe Operating Area for other Input Voltages.
- 8. Same as note 5, except measured with a single pulse width = $100\mu s$ and $V_{IN} = 48V$.
- 9. Same as note 5, except measured with a single pulse width = 10ms and V_{IN} = 48V.
- 10. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (on the exposed V_{IN} pad).
 - $R_{\theta JC}$ = Thermal resistance from junction to the top of case.
- 11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

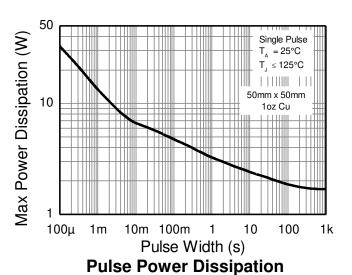


Thermal Characteristics and Derating Information

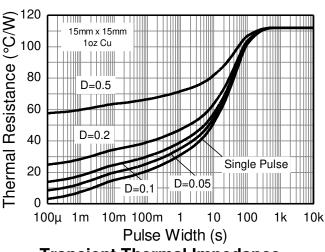












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Transient Thermal Impedance



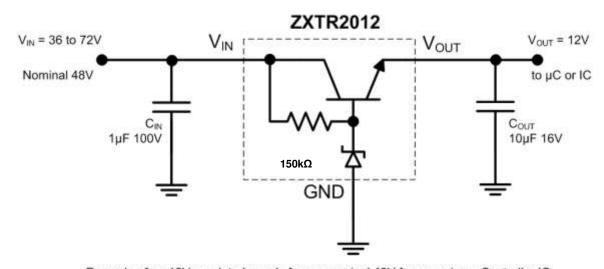
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 12)	V _{OUT}	10.8	12	13.2	V	$V_{IN} = 48V$, $I_{OUT} = 15mA$
Line Regulation (Notes 12 & 13)	ΔV_{OUT}	1	240	750	mV	$V_{IN} = 15 \text{ to } 72V$, $I_{OUT} = 15\text{mA}$
Temperature Coefficient	ΔV _{OUT} /ΔΤ	1	8.0	l	mV/°C	$T_{J} = -40$ °C to +125°C $V_{IN} = 48V$, $I_{OUT} = 15$ mA
Load Regulation (Notes 12 & 14)	ΔV_{OUT}		-450 -600	-600 -750	mV	I _{OUT} = 0.1 to 30mA, V _{IN} = 48V I _{OUT} = 0.1 to 100mA, V _{IN} = 48V
Minimum Value of Input Voltage Required to Maintain Line Regulation	V _{IN(MIN)}	15	_	ı	٧	_
Quiescent Current	ΙQ	_	240 590	400 900	μΑ	$V_{IN} = 48V, I_{OUT} = 10\mu A$ $V_{IN} = 100V, I_{OUT} = 10\mu A$
Power Supply Rejection Ratio	$\Delta V_{IN}/\Delta V_{OUT}$		45		dB	$C_{OUT} = 100nF$, $I_{OUT} = 15mA$, $V_{OUT} = 12V$, $V_{IN} = 15$ to 100V, $f = 100Hz$

Notes: 1

- 12. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%
- 13. Line regulation $\Delta V_{OUT} = V_{OUT}(@V_{IN} = 72V) V_{OUT}(@V_{IN} = 15V)$
- 14. Load regulation $\Delta V_{OUT} = V_{OUT} (@ I_{OUT} = 30mA) V_{OUT} (@ I_{OUT} = 0.1mA)$
 - $\Delta V_{OUT} = V_{OUT}(@ I_{OUT} = 100mA) V_{OUT}(@ I_{OUT} = 0.1mA)$

Typical Application Circuit



Example of an 12V regulated supply from a nominal 48V for powering a Controller IC.

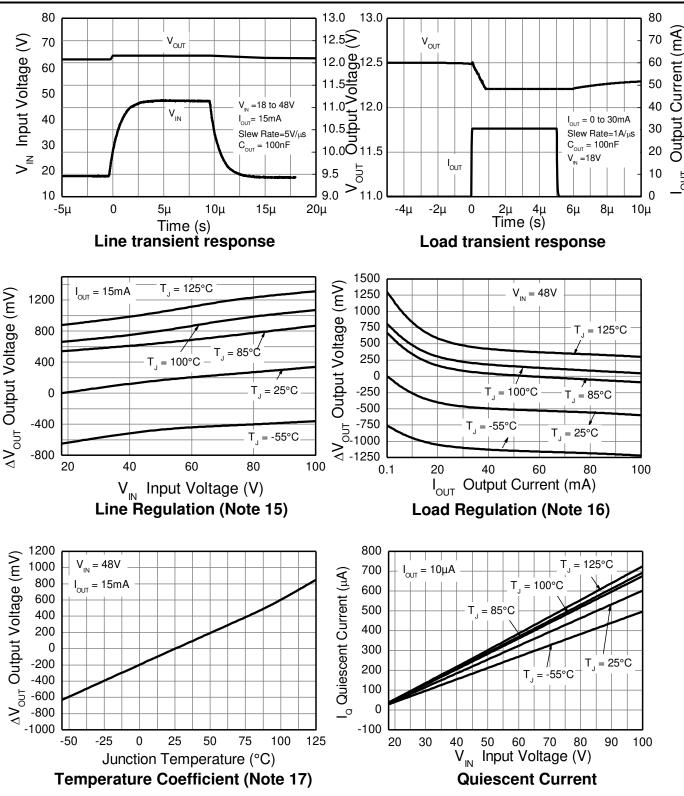
Pin Functions

Pin Name	Pin Function	Notes
V _{IN}	Input Supply	Input voltage can vary from -0.3V to 100V with respect to GND; for V_{OUT} regulated then 15V $\leq V_{IN} \leq$ 100V. It is recommended to connect a 1 μ F capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
V _{OUT}	Voltage Output	Outputs a regulated 12V when $15V \le V_{IN} \le 100V$. When $V_{IN} < 15V$, then VOUT maximum = $V_{IN} - 1.5V$. The pin can be pulled high to a maximum of +18V with respect to GND, or +12V with respect to V_{IN} , whichever is lower. It is recommended to connect a $10\mu F$ capacitor to GND and a minimum of $10\mu A$ to be drawn from V_{OUT} to maintain regulation.

OUT







15. Line regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@V_{IN} = 15V, I_{OUT} = 15mA, T_J = +25^{\circ}C)$ Notes:

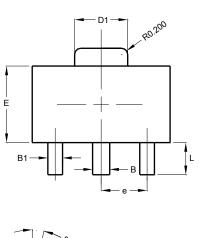
^{16.} Load regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@V_{IN} = 48V, I_{OUT} = 0.1 mA, T_J = +25 °C)$

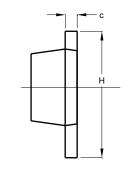
^{17.} Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@V_{IN} = 48V, I_{OUT} = 15mA, T_J = +25^{\circ}C)$



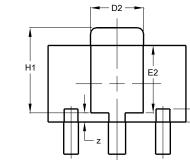
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





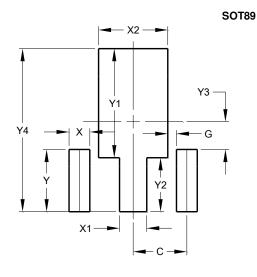
SOT89



SOT89					
Dim	Min	Max	Тур		
Α	1.40	1.60	1.50		
В	0.50	0.62	0.56		
B1	0.42	0.54	0.48		
С	0.35	0.43	0.38		
D	4.40	4.60	4.50		
D1	1.62	1.83	1.733		
D2	1.61	1.81	1.71		
Е	2.40	2.60	2.50		
E2	2.05	2.35	2.20		
е	-	-	1.50		
Н	3.95	4.25	4.10		
H1	2.63	2.93	2.78		
L	0.90	1.20	1.05		
L1	0.327	0.527	0.427		
Z	0.20	0.40	0.30		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value
Dillicisions	(in mm)
С	1.500
G	0.244
Х	0.580
X1	0.760
X2	1.933
Υ	1.730
Y1	3.030
Y2	1.500
Y3	0.770
Y4	4 530



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