FS6S-SERIES
FS6S0965RT/FS6S1265RB
Fairchild Power Switch(FPS)

Features

- Wide Operating Frequency Range Up to 150Khz
- Lowest Cost SMPS Solution
- Lowest External Components
- Low Start up Current (max:170uA)
- Low Operating Current (max:15mA)
- Internal High Voltage SenseFET
- Built-in Auto Restart Circuit
- Over Voltage Protection (Auto Restart Mode)
- Over Load Protection (Auto Restart Mode)
- Over Current Protection With Latch Mode
- Internal Thermal Protection With Latch Mode
- Pulse By Pulse Over Current Limiting
- Internal Burst Mode Controller for Stand-by Mode
- Under Voltage Lockout With Hysteresis
- External Sync. Terminal

Application

- Monitor SMPS

Description

The Fairchild Power Switch(FPS) product family are specially designed for an off line SMPS with minimal external components. The Fairchild Power Switch(FPS) consists of a high voltage power SenseFET and a current mode PWM IC. Included PWM controller features the integrated fixed oscillator, the under voltage lockout, the optimized gate turn on/turn off driver, the thermal shut down protection, the over voltage protection, and the temperature compensated precision current sources for the loop compensation and the fault protection circuitry. Compared to a discrete MOSFET and a controller or a RCC switching converter solution, a Fairchild Power Switch(FPS) can reduce the total component count, design size, and weight and at the same time increase efficiency, productivity, and system reliability. It has a basic platform well suited for the cost effective monitor power supply.

TO-220F-5L **TO-3P-5L**

1. Drain 2. Gnd 3. V$_{CC}$ 4. FeedBack 5. Sync.

Internal Block Diagram

Rev.1.0.6

Absolute Maximum Ratings

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
FS6S0965RT			
Maximum Drain Voltage	$V_{D,MAX}$	650	V
Drain-Gate Voltage(R_{GS}=1MΩ)	V_{DGR}	650	V
Gate-Source(GND) Voltage	V_{GS}	±30	V
Drain Current Pulsed[1]	I_{DM}	36	ADC
Continuous Drain Current (Tc = 25°C)	I_D	9	ADC
Continuous Drain Current (Tc = 100°C)	I_D	7.2	ADC
Single Pulsed Avalanche Current(Energy [2])	$I_{AS}(E_{AS})$	25(950)	A(mJ)
Maximum Supply Voltage	$V_{CC,MAX}$	35	V
Input Voltage Range	V_{FB}	-0.3 to V_{CC}	V
	V_{SS}	-0.3 to 10	V
Total Power Dissipation	P_D (Watt H/S)	48	W
	Darting	0.385	W / °C
Operating Junction Temperature.	T_J	+160	°C
Operating Ambient Temperature.	T_A	-25 to +85	°C
Storage Temperature Range.	T_{STG}	-55 to +150	°C
FS6S1265RB			
Maximum Drain Voltage	$V_{D,MAX}$	650	V
Drain-Gate Voltage(R_{GS}=1MΩ)	V_{DGR}	650	V
Gate-Source(GND) Voltage	V_{GS}	±30	V
Drain Current Pulsed[1]	I_{DM}	48	ADC
Continuous Drain Current (Tc = 25°C)	I_D	12	ADC
Continuous Drain Current (Tc = 100°C)	I_D	8.4	ADC
Single Pulsed Avalanche Current(Energy [2])	$I_{AS}(E_{AS})$	30(950)	A(mJ)
Maximum Supply Voltage	$V_{CC,MAX}$	35	V
Input Voltage Range	V_{FB}	-0.3 to V_{CC}	V
	V_{SS}	-0.3 to 10	V
Total Power Dissipation	P_D (Watt H/S)	240	W
	Darting	1.92	W / °C
Operating Junction Temperature.	T_J	+160	°C
Operating Ambient Temperature.	T_A	-25 to +85	°C
Storage Temperature Range.	T_{STG}	-55 to +150	°C

Electrical Characteristics (SenseFET Part)

(Ta = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
FS6S0965RT						
Drain Source Breakdown Voltage	BV_{DSS}	V_{GS} = 0V, I_D = 50µA	650	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=Max., Rating, V_{GS}=0V	-	-	200	µA
		V_{DS}= 0.8Max., Rating, V_{GS} = 0V, T_C = 125°C	-	-	300	µA
Static Drain-Source On Resistance [1]	$R_{DS(on)}$	V_{GS} = 10V, I_D = 4.5A	-	1.1	1.2	Ω
Forward Transconductance [1]	gfs	V_{DS} = 50V, I_D = 4.5A	-	-	-	S
Input Capacitance	Ciss	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz	-	1300	-	pF
Output Capacitance	Coss		-	135	-	
Reverse Transfer Capacitance	Crss		-	25	-	
Turn On Delay Time	$t_{d(on)}$	V_{DD} = 0.5BV_{DSS}, I_D = 9.0A (MOSFET switching time is essentially independent of operating temperature)	-	25	-	nS
Rise Time	tr		-	75	-	
Turn Off Delay Time	$t_{d(off)}$		-	130	-	
Fall Time	tf		-	70	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	V_{GS} = 10V, I_D = 9.0A, V_{DS} = 0.5BV_{DSS}(MOSFET switching time is essentially independent of operating temperature)	-	45	-	nC
Gate-Source Charge	Qgs		-	8	-	
Gate-Drain (Miller) Charge	Qgd		-	22	-	
FS6S1265RB						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} = 0V, I_D = 50µA	650	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=Max, Rating, V_{GS} = 0V	-	-	200	µA
		V_{DS}= 0.8Max, Rating, V_{GS} = 0V, T_C = 125°C	-	-	300	µA
Static Drain-Source on Resistance [1]	$R_{DS(on)}$	V_{GS} = 10V, I_D = 4.5A	-	0.7	0.9	Ω
Forward Transconductance [1]	gfs	V_{DS} = 50V, I_D = 4.5A	-	-	-	S
Input Capacitance	Ciss	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz	-	1820	-	pF
Output Capacitance	Coss		-	185	-	
Reverse Transfer Capacitance	Crss		-	32	-	
Turn On Delay Time	$t_{d(on)}$	V_{DD} = 0.5BV_{DSS}, I_D = 12.0A (MOSFET switching time are essentially independent of operating temperature)	-	38	-	nS
Rise Time	tr		-	120	-	
Turn Off Delay Time	$t_{d(off)}$		-	200	-	
Fall Time	tf		-	100	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	V_{GS} = 10V, I_D = 12.0A, V_{DS} = 0.5BV_{DSS}(MOSFET switching time are essentially independent of operating temperature)	-	60	-	nC
Gate-Source Charge	Qgs		-	10	-	
Gate-Drain (Miller) Charge	Qgd		-	30	-	

Electrical Characteristics (Continued)

(V$_{CC}$=16V, Tamb = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
UVLO SECTION						
Start Threshold Voltage	V$_{START}$	V$_{FB}$=GND	14	15	16	V
Stop Threshold Voltage	V$_{STOP}$	V$_{FB}$=GND	8	9	10	V
OSCILLATOR SECTION						
Initial Frequency	F$_{OSC}$	-	22	25	28	kHz
Voltage Stability	F$_{STABLE}$	12V ≤ V$_{CC}$ ≤ 23V	0	1	3	%
Temperature Stability (Note2)	ΔF$_{OSC}$	-25°C ≤ Ta ≤ 85°C	0	±5	±10	%
Maximum Duty Cycle	D$_{MAX}$	-	92	95	98	%
Minimum Duty Cycle	D$_{MIN}$	-	-	-	0	%
FEEDBACK SECTION						
Feedback Source Current	I$_{FB}$	V$_{FB}$=GND	0.7	0.9	1.1	mA
Shutdown Feedback Voltage	V$_{SD}$	V$_{FB}$ ≥ 6.9V	6.9	7.5	8.1	V
Shutdown Delay Current	I$_{DELAY}$	V$_{FB}$=5V	1.6	2.0	2.4	μA
SYNC. & SOFTSTART SECTION						
Softstart Voltage	V$_{SS}$	V$_{FB}$=2V	4.7	5.0	5.3	V
Softstart Current	I$_{SS}$	V$_{SS}$=0V	0.8	1.0	1.2	mA
Sync High Threshold Voltage(Note3)	V$_{SYNCH}$	V$_{CC}$=16V , V$_{FB}$=5V	-	7.2	-	V
Sync Low Threshold Voltage(Note3)	V$_{SYNCL}$	V$_{CC}$=16V , V$_{FB}$=5V	-	5.8	-	V
BURST MODE SECTION						
Burst Mode Low Threshold Voltage	V$_{BURL}$	V$_{FB}$=0V	10.4	11.0	11.6	V
Burst Mode High Threshold Voltage	V$_{BURH}$	V$_{FB}$=0V	11.4	12.0	12.6	V
Burst Mode Enable Feedback Voltage	V$_{BEN}$	V$_{CC}$=10.5V	0.7	1.0	1.3	V
Burst Mode Peak Current Limit(Note4)	I$_{BURPK}$	V$_{CC}$=10.5V , V$_{FB}$=0V	0.6	0.85	1.1	A
Burst Mode Frequency	F$_{BUR}$	V$_{CC}$=10.5V , V$_{FB}$=0V	40	50	60	kHz
CURRENT LIMIT(SELF-PROTECTION)SECTION						
Peak Current Limit (Note4)	I$_{OVER}$	**FS6S0965RT**	5.28	6.0	6.72	A
		FS6S1265RB	7.04	8.0	8.96	
PROTECTION SECTION						
Over Voltage Protection	V$_{OVP}$	V$_{CC}$ ≥ 27V	27	30	33	V
Over Current Latch voltage(Note3)	V$_{OCL}$	-	0.9	1.0	1.1	V
Thermal Shutdown Temperature (Note2)	T$_{SD}$	-	140	160	-	°C
TOTAL DEVICE SECTION						
Start-Up Current	I$_{START}$	V$_{FB}$ = GND, V$_{CC}$ = 14V	-	0.1	0.17	mA
Operating Supply Current(Note1)	I$_{OP}$	V$_{FB}$ = GND, V$_{CC}$ = 16V	-	10	15	mA
	I$_{OP(MIN)}$	V$_{FB}$ = GND, V$_{CC}$ = 12V				
	I$_{OP(MAX)}$	V$_{FB}$ = GND, V$_{CC}$ = 30V				

Notes:

1. These parameters are the Current Flowing in the Control IC.

2. These parameters, although guaranteed, are not 100% tested in production

3. These parameters, although guaranteed, are tested in EDS(wafer test) process

4. These parameters are indicated Inductor Current.

# Typical Performance Characteristics
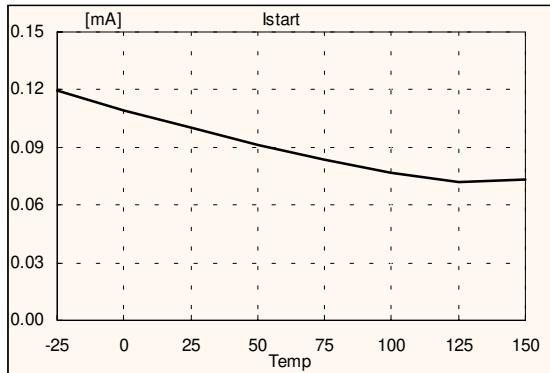
Figure 1. Start Up Current vs. Temp.
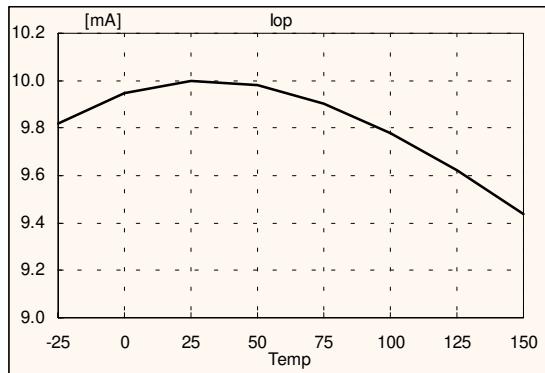

Figure 2. Operating Supply Current vs. Temp.
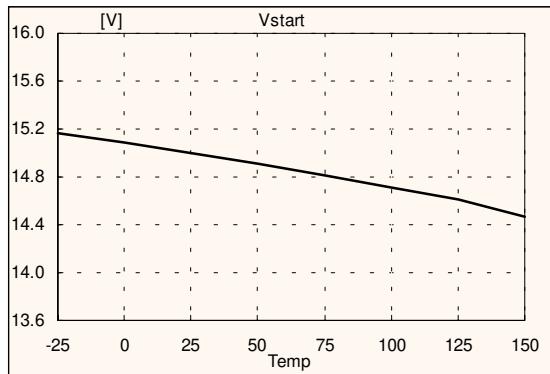
Figure 3. Start Threshold Voltage vs. Temp.
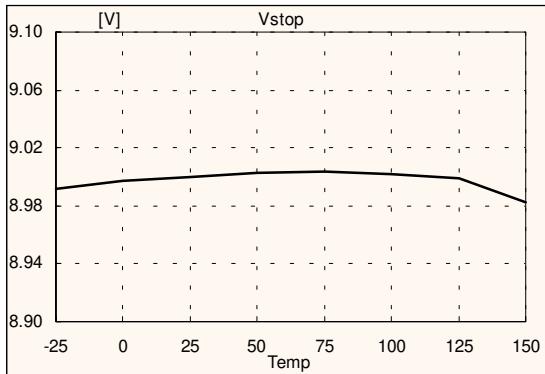
Figure 4. Stop Threshold Voltage vs. Temp.
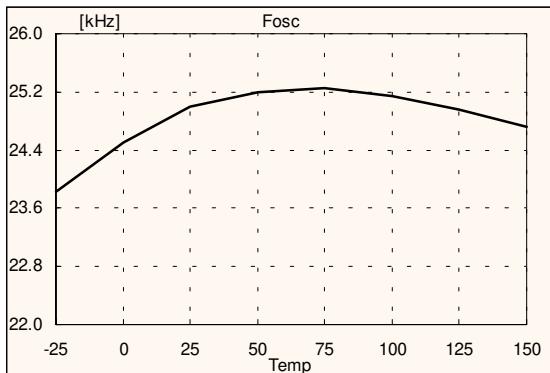
Figure 5. Initial Frequency vs. Temp.
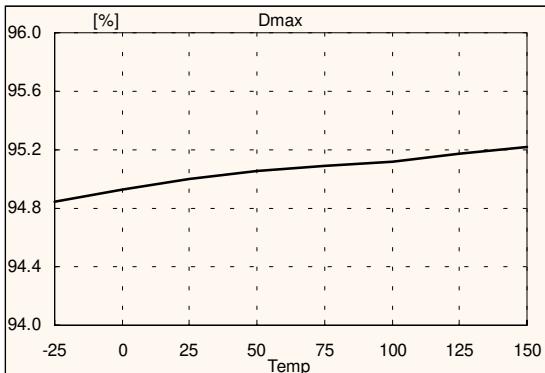
Figure 6. Maximum Duty vs. Temp.

Typical Performance Characteristics (Continued)
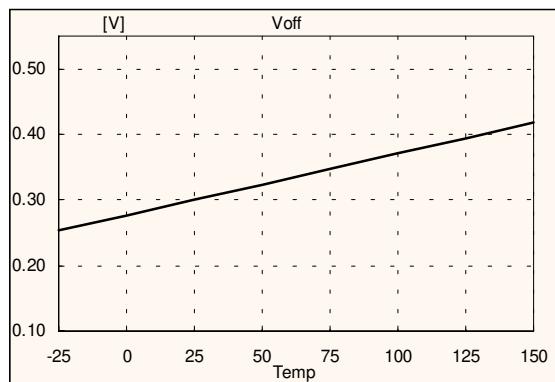

Figure 7. Feedback Offset Voltage vs. Temp.
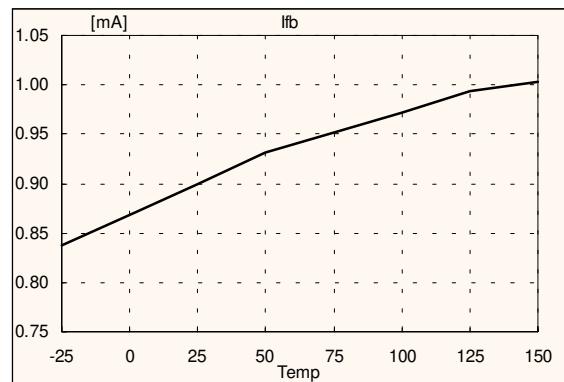

Figure 8. Feedback Source Current vs. Temp.
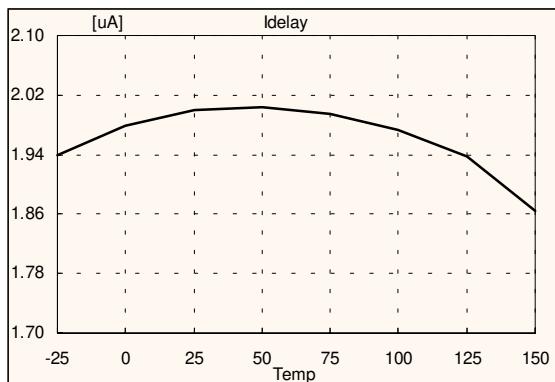
Figure 9. Shutdown Delay Current vs. Temp.
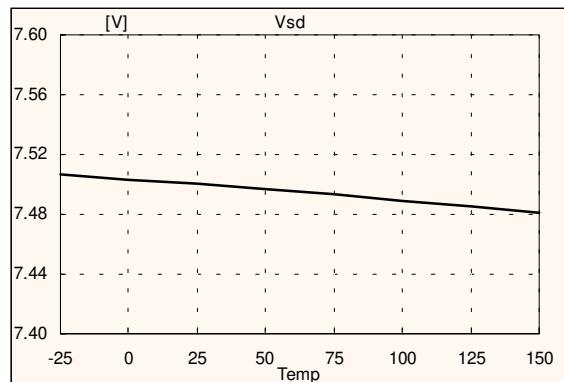
Figure 10. Shutdown Feedback Voltage vs. Temp.
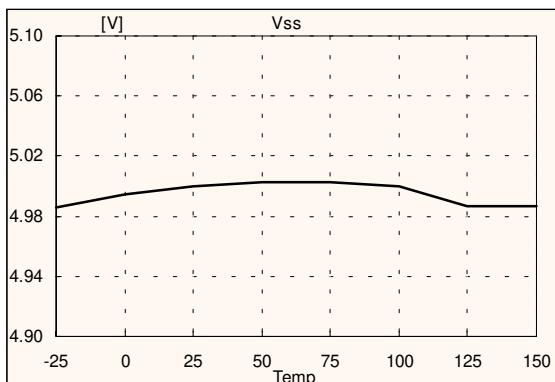
Figure 11. Softstart Voltage vs. Temp.
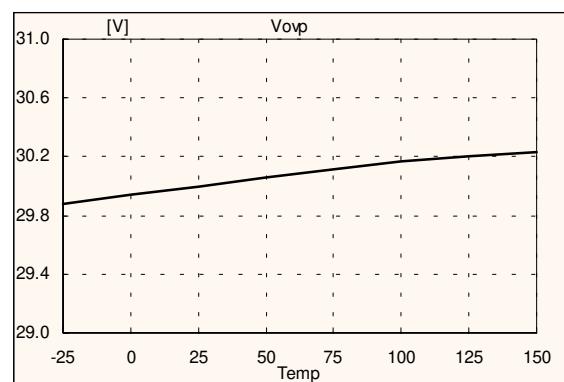
Figure 12. Over Voltage Protection vs. Temp.

Typical Performance Characteristics (Continued)
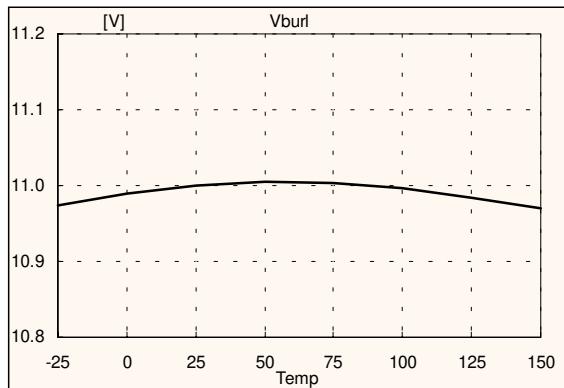
Figure 13. Burst Mode Low Voltage vs. Temp.
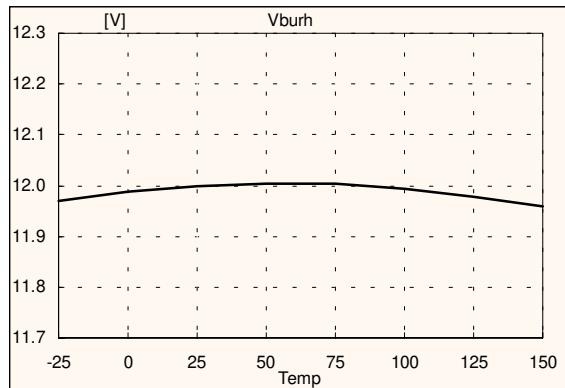
Figure 14. Burst Mode High Voltage vs. Temp.
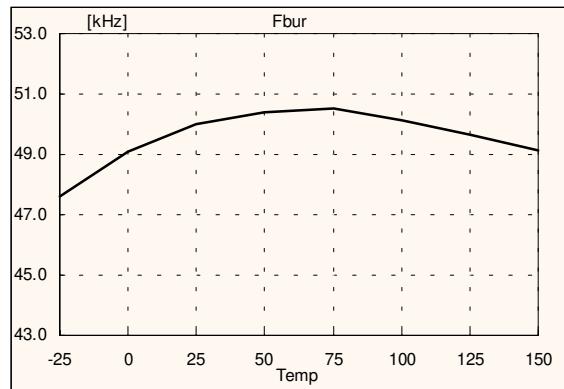
Figure 15. Burst Mode Frequency vs. Temp.
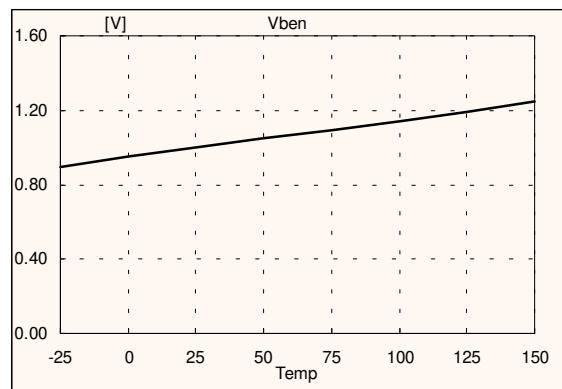
Figure 16. Burst Mode Enable Voltage vs. Temp.
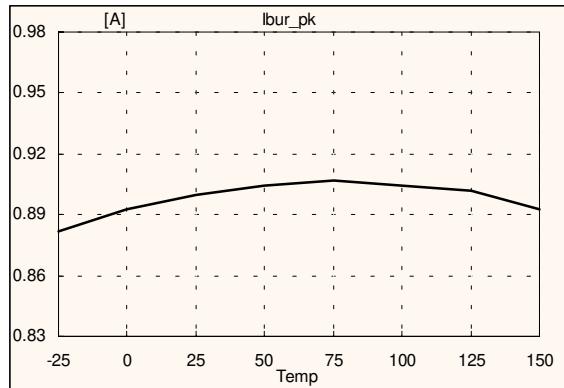
**Figure 17. Burst Mode Peak Current vs. Temp.**
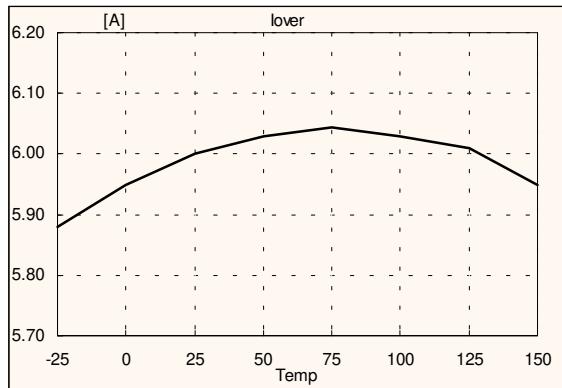
Figure 18. Peak Current Limit vs. Temp.

Package Dimensions

TO-3P-5L
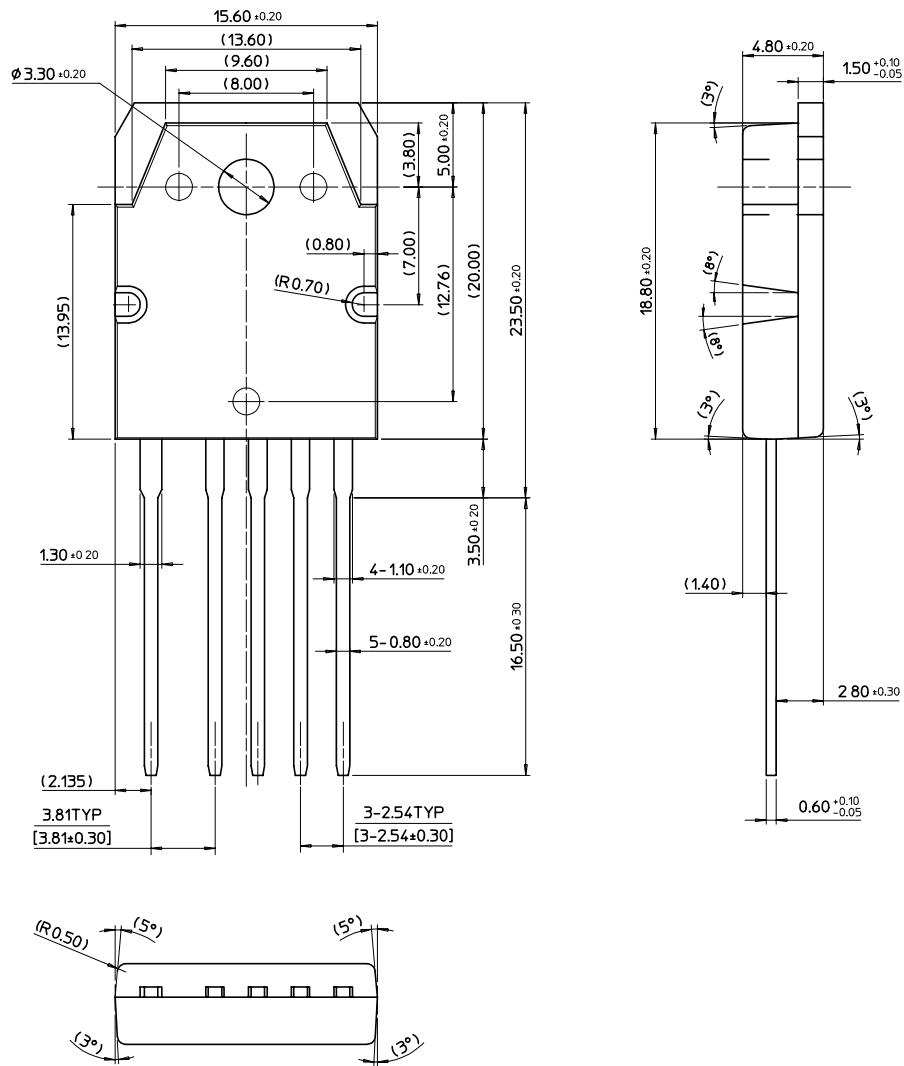
Package Dimensions (Continued)

TO-3P-5L(Forming)
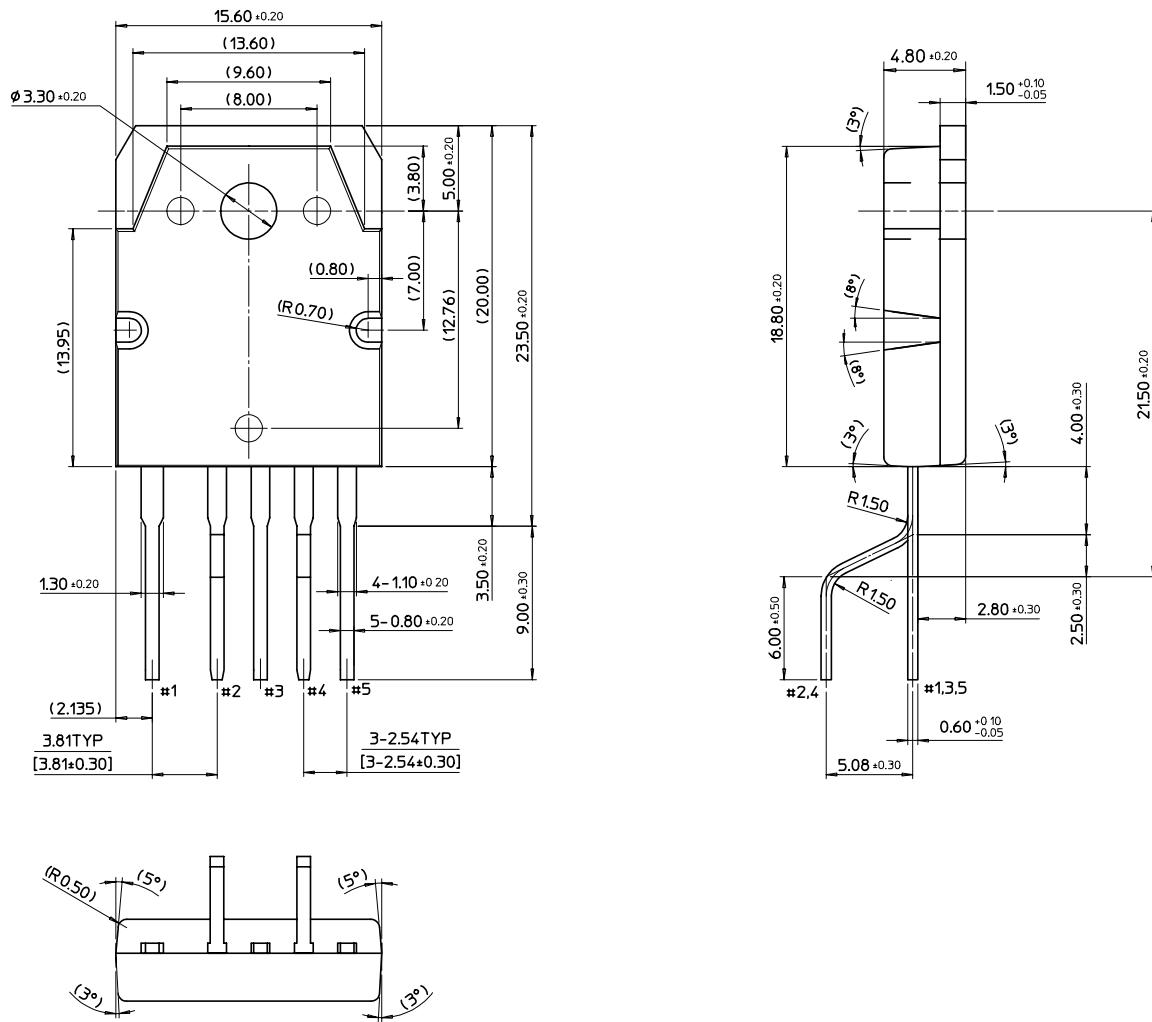
Package Dimensions (Continued)
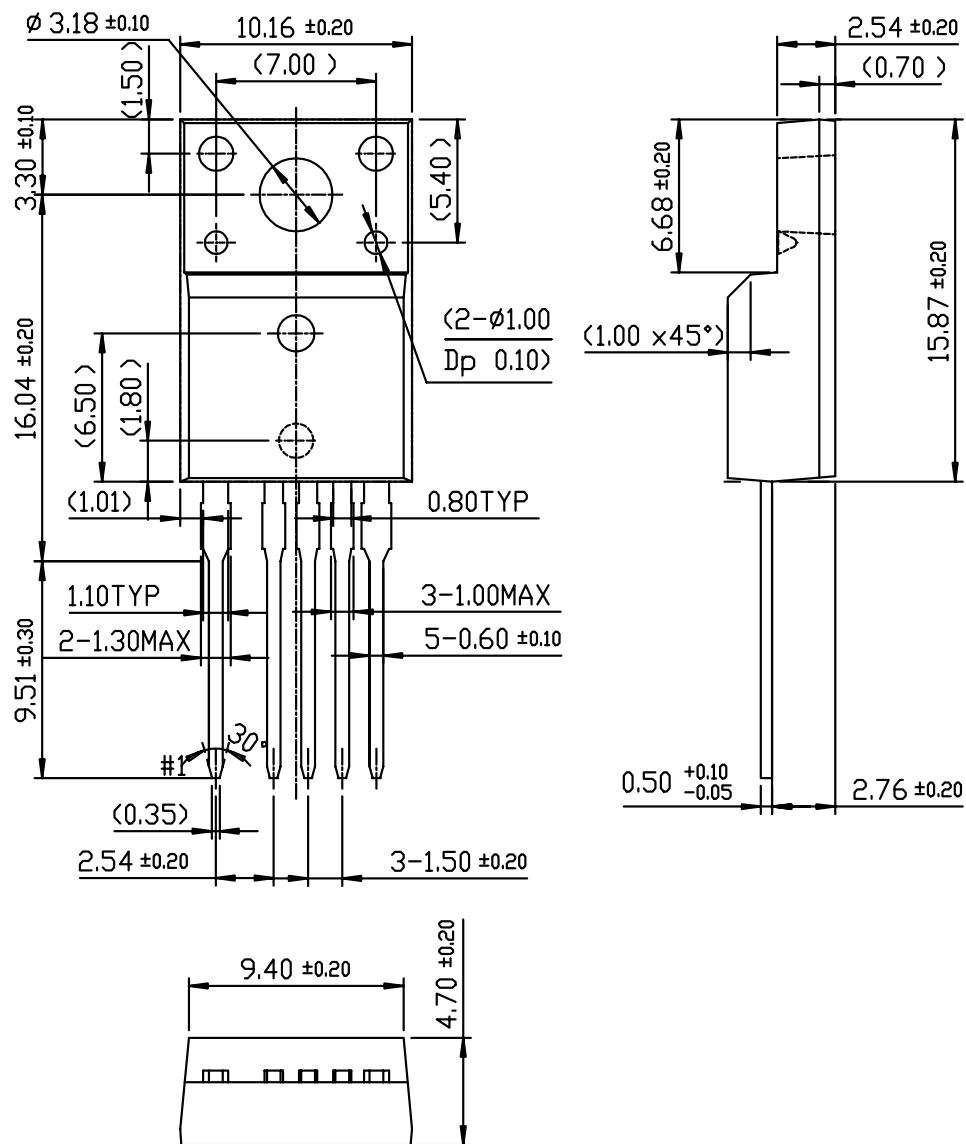
TO-220F-5L

Package Dimensions (Continued)

TO-220F-5L(Forming)
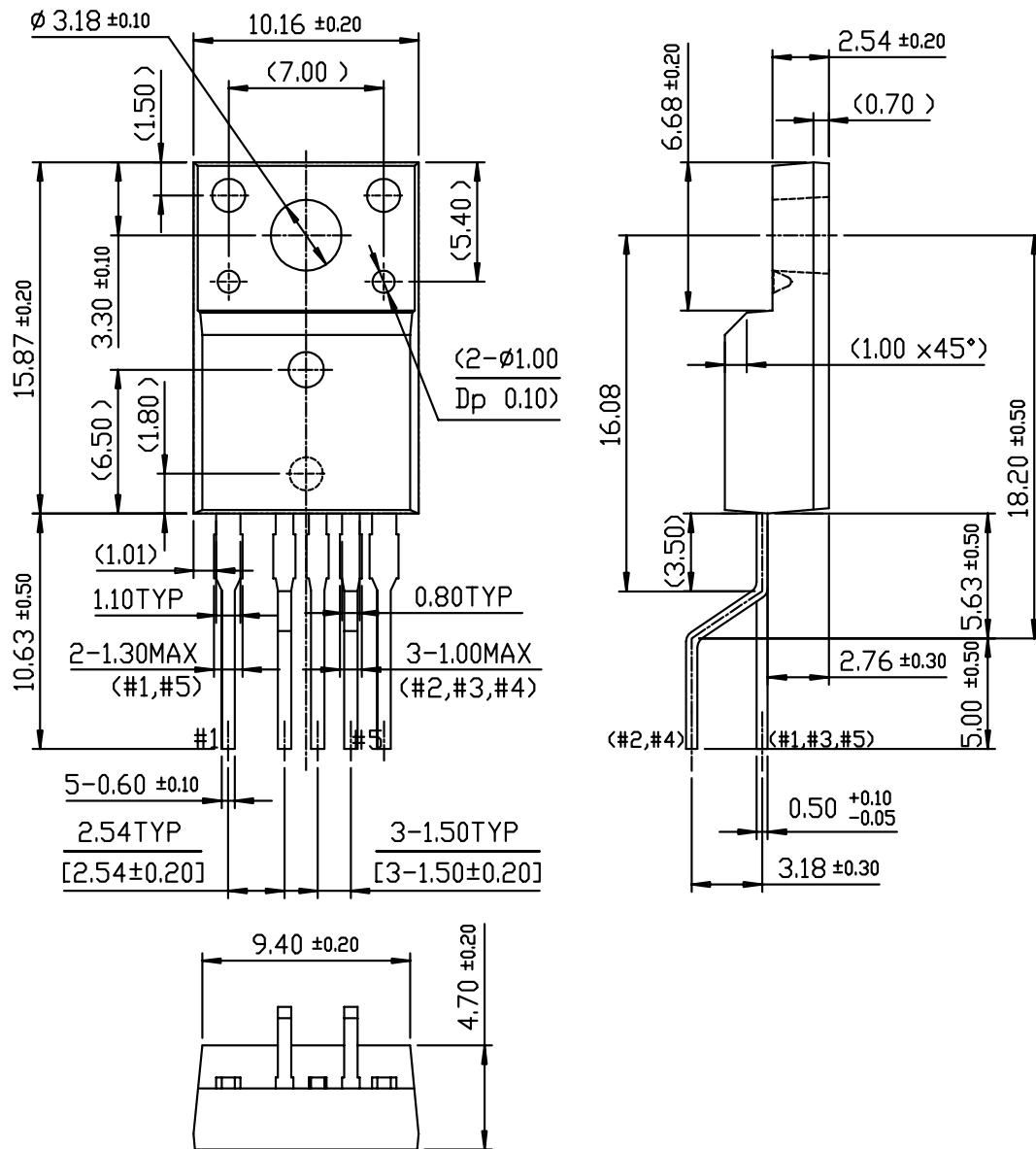
TOP Mark and Pinout Information
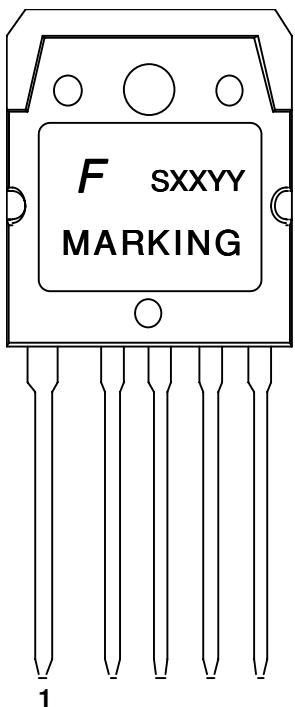
Pin No.	Symbol	Description
1	Drain	SenseFET Drain
2	GND	Ground (Source)
3	V$_{CC}$	Control Part Supply Input
4	F/B	PWM Non Inverting Input
5	S/S	Soft start & External Sync.

Device	Marking
FS6S0965RT	6S0965R
FS6S1265RB	6S1265RB

Notes:

(1) F : Fairchild Semiconductor

(2) 6S1265RB : Device Marking Name

(3) S : Plant Code (FPS: S)

(4) XX : Patweek Based on Fairchild Semiconductor Work Month Calender

(5) YY : Last Two Digit of Calender Year

Ordering Information

Product Number	Package	Marking Code	BVdss	Rds(on)
FS6S0965RT-TU	TO-220F-5L	6S0965R	650V	1.1Ω
FS6S0965RT-YDTU	TO-220F-5L(Forming)			
FS6S1265RB-TU	TO-3P-5L	6S1265RB	650V	0.7Ω
FS6S1265RB-YDTU	TO-3P-5L(Forming)			

TU : Non Forming Type
YDTU : Forming Type

Fairchild Semiconductor

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Features

The Fairchild Power Switch (FPS) product family are specially designed for an off line SMPS with minimal external components. The Fairchild Power Switch (FPS) consists of a high voltage power SenseFET and a current mode PWM IC. Included PWM controller features the integrated fixed oscillator, the under voltage lockout, the optimized gate turn on/turn off driver, the thermal shut down protection, the over voltage protection, and the temperature compensated precision current sources for the loop compensation and the fault protection circuitry. Compared to a discrete MOSFET and a controller or a R_{CC} switching converter solution, a Fairchild Power Switch (FPS) can reduce the total component count, design size, and weight and at the same time increase efficiency, productivity, and system reliability. It has a basic platform well suited for the cost effective monitor power supply.

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- Over Current Protection With Latch Mode

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- Under Voltage Lockout With Hysteresis
- External Sync. Terminal

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Applications

- Monitor SMPS

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Product status/pricing/packaging

Product	Product status	Package type	Leads	Packing method
FS6S1265RBSYDTU	Lifetime Buy	TO-3P	5	RAIL
FS6S1265RBTU	Lifetime Buy	TO-3P	5	RAIL
FS6S1265RBYDTU	Lifetime Buy	TO-3P	5	RAIL

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Application notes

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