VLMRY3420





Bicolor SMD LED PLCC-4



DESCRIPTION

These devices have been designed to meet the increasing demand for surface mounting technology.

The package of the VLMRY3420 is the PLCC-4.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

This SMD device consists of a amber and yellow chip. So it is possible to choose the color in one device.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD PLCC-4
- Product series: bicolor
- Angle of half intensity: ± 60°

FEATRUES

- SMD LED with exceptional brightness
- Multicolored
- Luminous intensity categorized
- EIA and ICE standard package
- Compatible with automatic placement equipment



RoHS

COMPLIANT

FREE

(5-2008)

- HALOGEN · Compatible with IR reflow, vapor phase and GREEN wave soldering processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape
- Low profile package
- · Non-diffused lens: Excellent for coupling to light pipes and backlighting
- Low power consumption
- Luminous intensity ratio in one packaging unit $I_{Vmax}/I_{Vmin} \le 1.6$
- Preconditioning according to JEDEC level 2a
- ESD-withstand voltage: Up to 2 kV according to JESD22-A114-B
- AEC-Q101 gualified
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Automotive: Backlighting in dashboards and switches
- Telecommunication: Indicator and backlighting in telephone and fax
- · Indicator and backlight in office equipment
- · Flat backlight for LCDs, switches, and symbols
- General use

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (mcd)		at I _F WAY		VELENGTH (nm)		at I _F (mA)	FORWARD VOLTAGE (V)		at I _F (mA)	TECHNOLOGY		
		MIN.	TYP.	MAX.	(111,4)	MIN.	TYP.	MAX.	(IIIA)	MIN.	TYP.	MAX.	(1174)	
VLMRY3420-GS08	Amber	355	-	900	50	-	617	-	50	-	2.1	2.6	50	AllnGaP on GaAs
VLMRY3420-GS08	Yellow	560	-	1120	50	581	588	594	50	-	2.1	2.6	50	AllnGaP on GaAs
VLMRY3420-GS18	Amber	355	I	900	50	-	617	-	50	-	2.1	2.6	50	AllnGaP on GaAs
VLMRY3420-GS18	Yellow	560	-	1120	50	581	588	594	50	-	2.1	2.6	50	AllnGaP on GaAs

VLMRY3420



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) VLMRY3420							
PARAMETER	TEST CONDITI	ON	SYMBOL	VALUE	UNIT		
Reverse voltage per diode (1)	I _R = 10 μA		V _R	5	V		
DC forward current per diode	T _{amb} ≤ 65 °C	I _F	50	mA			
Surge forward current per diode			I _{FSM}	0.1	А		
Power dissipation per diode			Pv	130	mW		
Junction temperature			Tj	125	°C		
Operating temperature range			T _{amb}	- 40 to + 100	°C		
Storage temperature range			T _{stg}	- 40 to + 100	°C		
Thermal resistance junction/ambient	Mounted on PC board (pad size > 16 mm ²)	1 chip on 2 chips on	R _{thJA}	480 650	K/W		

Note

⁽¹⁾ Driving the LED in reverse direction is suitable for short term application

OPTICAL AND ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified) **VLMRY3420, AMBER**

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I _F = 50 mA	VLMRY3420	Iv	355	-	900	mcd
Dominant wavelength	I _F = 50 mA		λ_d	-	617	-	nm
Peak wavelength	I _F = 50 mA		λp	-	624	-	nm
Angle of half intensity	I _F = 50 mA		φ	-	± 60	-	deg
Forward voltage	I _F = 50 mA		V _F	-	2.1	2.6	V
Reverse current	V _R = 5 V		I _R	-	-	10	μA
Junction capacitance	V _R = 0 V, f = 1 MHz		Cj	-	15	-	pF

OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified) **VLMRY3420**, **YELLOW**

-					1	1	1
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I _F = 50 mA	VLMRY3420	Ι _V	560	-	1120	mcd
Dominant wavelength	I _F = 50 mA		λ_d	581	588	594	nm
Peak wavelength	I _F = 50 mA		λp	-	590	-	nm
Angle of half intensity	I _F = 50 mA		φ	-	± 60	-	deg
Forward voltage	I _F = 50 mA		V _F	-	2.1	2.6	V
Reverse current	V _R = 5 V		I _R	-	-	10	μA
Junction capacitance	V _R = 0 V, f = 1 MHz		Cj	-	15	-	pF

CROSSING TABLE					
VISHAY	OSRAM				
VLMRY3420	LAYT67B				

LUMINOUS INTENSITY CLASSIFICATION AND GROUP COMBINATIONS, VLMRY3420

			RI	RED			
		T2 355 mcd to 450 mcd	U1 450 mcd to 560 mcd	U2 560 mcd to 710 mcd	V1 710 mcd to 900 mcd		
Y E	U2 560 mcd to 710 mcd	VLMRY3420	VLMRY3420	VLMRY3420	VLMRY3420		
L	V1 710 mcd to 900 mcd	VLMRY3420	VLMRY3420	VLMRY3420	VLMRY3420		
o w	V2 900 mcd to 1120 mcd	VLMRY3420	VLMRY3420	VLMRY3420	VLMRY3420		

Note

• Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.

Rev. 1.4, 16-May-13

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COLOR CLASSIFICATION							

	DOMINANT WAVELENGTH (nm) YELLOW				
GROUP					
	MIN.	MAX.			
1	581	584			
2	583	586			
3	585	588			
4	587	590			
5	589	592			
6	591	594			

Note

Wavelengths are tested at a current pulse duration of 25 ms. ٠

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

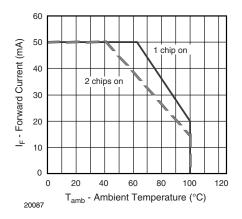


Fig. 1 - Forward Current vs. Ambient Temperature

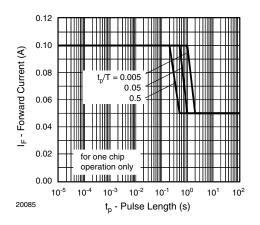


Fig. 2 - Forward Current vs. Pulse Duration

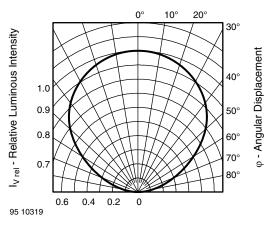


Fig. 3 - Relative Luminous Intensity vs. Angular Displacement

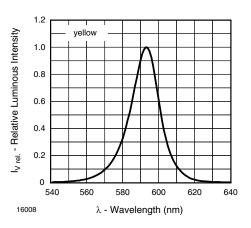


Fig. 4 - Relative Intensity vs. Wavelength

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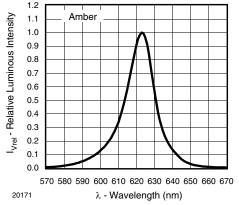


Fig. 5 - Relative Intensity vs. Wavelength

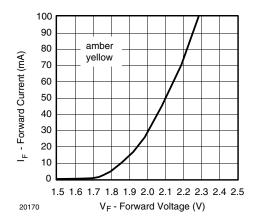


Fig. 6 - Relative Forward Voltage vs. Ambient Temperature

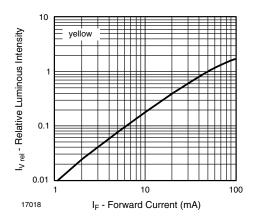


Fig. 7 - Relative Luminous Intensity vs. Forward Current

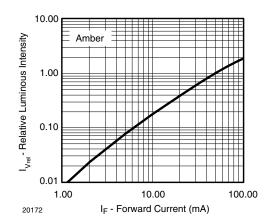


Fig. 8 - Relative Luminous Intensity vs. Forward Current

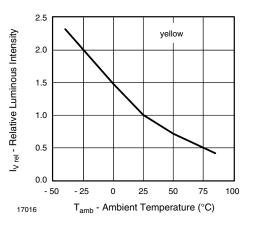


Fig. 9 - Relative Luminous Intensity vs. Ambient Temperature

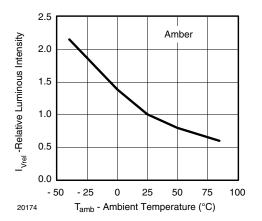


Fig. 10 - Relative Luminous Intensity vs. Ambient Temperature

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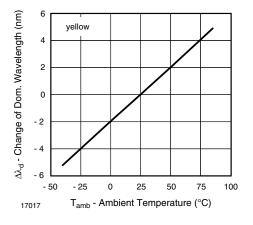


Fig. 11 - Change of Dominant Wavelength vs. Ambient Temperature

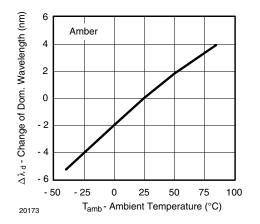


Fig. 12 - Change of Dominant Wavelength vs. Ambient Temperature

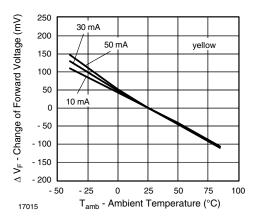


Fig. 13 - Change of Forward Voltage vs. Ambient Temperature

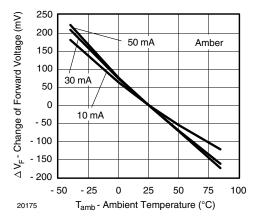


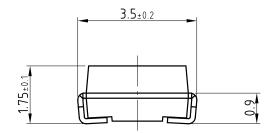
Fig. 14 - Change of Forward Voltage vs. Ambient Temperature

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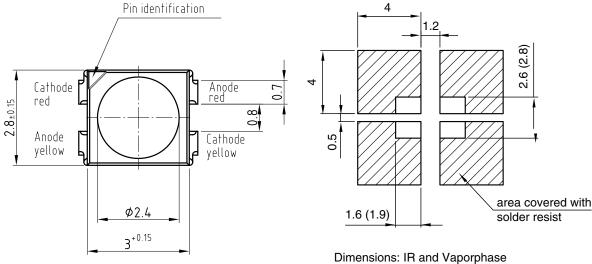
www.vishay.com

PACKAGE DIMENSIONS in millimeters





Mounting Pad Layout



(Wave Soldering)

Drawing-No.: 6.541-5057.01-4 Issue: 5; 30.05.07

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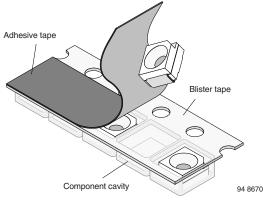




METHOD OF TAPING/POLARITY AND TAPE AND REEL

SMD LED (VLM.3 - SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3...

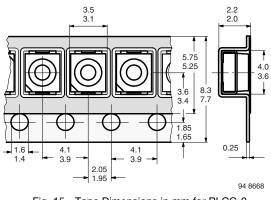


Fig. 15 - Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

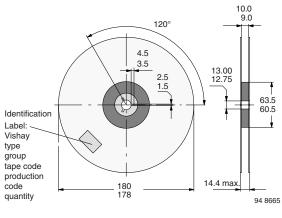


Fig. 16 - Reel Dimensions - GS08

Rev. 1.4, 16-May-13

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

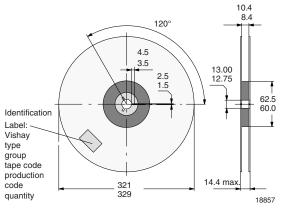


Fig. 17 - Reel Dimensions - GS18

SOLDERING PROFILE

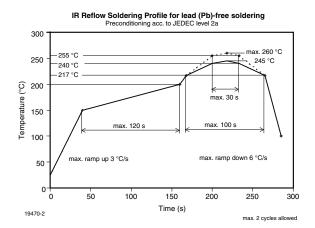


Fig. 18 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

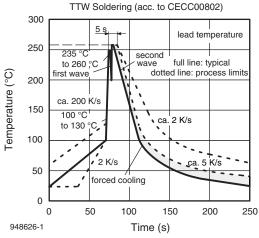


Fig. 19 - Double Wave Soldering of Opto Devices (all Packages)

Document Number: 81304

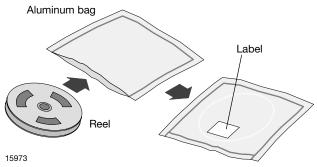
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DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

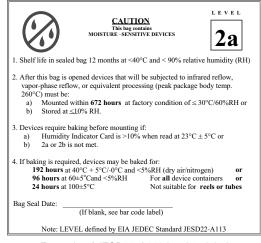
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 $^\circ\text{C}$ + 5 $^\circ\text{C}$ and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

Rev. 1.4, 16-May-13

8

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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