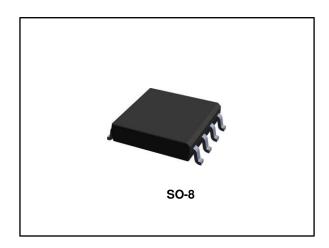
## LCP3121RL



## Programmable transient voltage suppressor for SLIC protection

Datasheet - production data



#### **Features**

- Programmable transient voltage suppressor compatible with:
  - single voltage SLICs
  - dual voltage SLICs
  - multi line SLICs
- Firing voltage range: -100 V to +100 V
- Peak pulse current:
  - IPP = 100 A (10/1000  $\mu$ s)
  - I<sub>PP</sub> = 150 A (5/310  $\mu$ s)
  - IPP = 250 A (2/10  $\mu$ s)
- Holding current: I<sub>H</sub> = 100 mA min.

#### **Benefits**

- A Trisil™ is not subject to ageing and provides a fail-safe mode in short circuit for a better protection.
- Trisils are used to help equipment to meet various standards such as UL1950, IEC 60950 / CSA C22.2, UL1459 and TIA-968-A (formerly FCC part 68).
- Trisils have UL94 V0 resin approved (Trisils are UL497B approved file: E136224).

#### **Description**

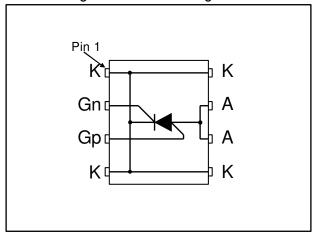
This device has been especially designed to protect single and dual voltages SLICs against transient overvoltages.

Connecting gate to the right supply voltage leads to overvoltage protection.

Used with diode bridges, LCP3121RL protects several twisted pairs.

LCP3121RL can be used to help equipment to meet various standards such as UL1950, IEC 60950 / CSAC22.2, UL1459 and TIA-968-A (formerly FCC part68).

Figure 1: Functional diagram



TM: Trisil is a trademark of STMicroelectronics.

Characteristics LCP3121RL

#### 1 Characteristics

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Table 1: Standards compliance

Standard	Peak surge voltage (V)	Voltage waveform	Required peak current (A)	Current waveform	Minimum serial resistor to meet standard (Ω)
GR-1089 core	2500	2/10 µs	500	2/10 μs	5
first level	1000	10/1000 µs	100	10/1000 µs	0
GR-1089 core second level	5000	2/10 μs	500	2/10 µs	10
GR-1089 core intra-building	1500	2/10 μs	100	2/10 µs	0
	6000		150		0
ITU-T-K20/K21	4000	10/700 µs	100	5/310 μs	0
	1500		37.5		0
ITU-T-K20	8000	ESD contact disc		discharge	0
(IEC 61000-4-2)	15000	1/60 ns	ESD air di	scharge	0
IEC 61000-4-5	4000	10/700 μs	100	5/310 µs	0
IEC 61000-4-5	4000	1.2/50 µs	100	8/20 µs	0

**Table 2: Thermal resistance** 

Symbol	Parameter	Value	Unit
R <sub>th(j-a)</sub>	Junction to ambient	120	°C/W

Table 3: Absolute ratings (T<sub>amb</sub> = 25 °C)

Symbol	Parameter	Value	Unit	
		10/1000 μs	100	
$I_{pp}$	Peak pulse current	5/310 μs	150	Α
		2/10 μs	250	
	Non repetitive surge peak on-state current (sinusoidal)	t = 0.1 s	25	Α
I <sub>TSM</sub>		t = 1 s	8	
	Current (sinusoidal)	t = 15 mn	3	
$V_{Gn}$	Negative battery voltage range	-100 to 0	V	
$V_{Gp}$	Positive battery voltage range	0 to 100	V	
T <sub>stg</sub>	Storage junction temperature range	55 to . 150	°C	
Tj	Maximum operating junction temperature	-55 to + 150	10	
T∟	Maximum temperature for soldering during	260	°C	

LCP3121RL Characteristics

Symbol Parameter  $V_{RM}$ Stand-off voltage  $V_{\mathsf{BR}}$ Breakdown voltage VBOBreakover voltage lΗ Holding current IBO Breakover current  $I_{BO}$ Leakage current at V<sub>RM</sub> IRM lн lR Leakage current at VR lpp Peak pulse current  $I_{RM}$  $V_{\mathsf{R}}$ Continuous reverse voltage  $V_{BR}$   $V_{BO}$ ۷g Gate voltage lGP Gp triggering current IGN Gn triggering current

Figure 2: Electrical characteristics (definitions)

Table 4: Parameters (T<sub>j</sub> = 25 °C unless otherwise specified)

Symbol	Test conditions	Min.	Max.	Unit
	V <sub>R</sub> = 60 V (between A and K)		5	
I <sub>R</sub>	V <sub>R</sub> = 90 V (between A and K)		8	μΑ
	V <sub>R</sub> = 180 V (between K and A)		50	
l <sub>Η</sub>	$V_{Gn} = -60 \text{ V or } V_{GP} = 60 \text{ V}$	100		mA
$V_{BR}$	I <sub>BR</sub> = 1mA (between A and K, Gp and Gn not connected)	100		V
$V_{GnK}$	I <sub>G</sub> = 200 mA (between Gn and K)	0.6	1.8	٧
$V_{GpA}$	I <sub>G</sub> = 200 mA (between Gp and A)	0.6	1.8	V
I <sub>Gn</sub>	V <sub>AK</sub> = 60 V		180	mA
$I_{Gp}$	V <sub>AK</sub> = 60 V	80	200	mA

Characteristics LCP3121RL

## 1.1 Characteristics (curves)

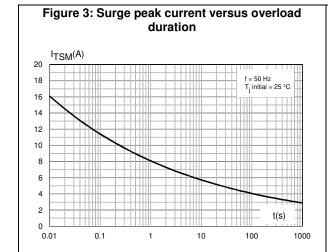


Figure 4: Relative variation of holding current versus junction temperature

1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
-40 -20 0 20 40 60 80 100 120 140

Figure 5: Schematic for single voltage SLIC

RING1

TIP1

RING1

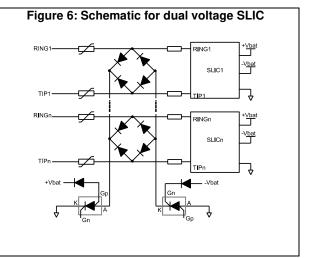
SLIC1

TIP1

RINGN

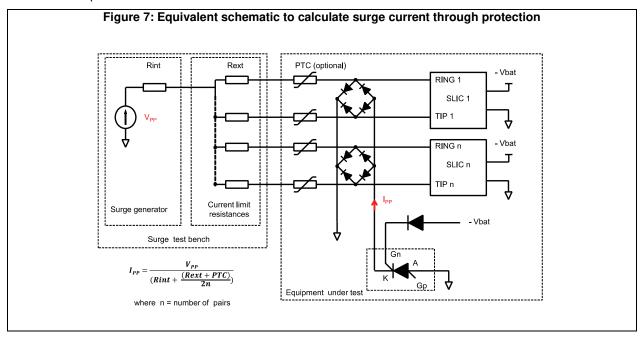
SLICN

TIPN



LCP3121RL Characteristics

This device is designed to protect several lines in parallel. Its surge capability must be higher than the current delivered by the surge generator depending on number of tested pairs.



Package information LCP3121RL

#### 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

• Flammability: Epoxy is rated UL94V-0

LCP3121RL Package information

## 2.1 SO-8 package information

Figure 8: SO-8 package outline

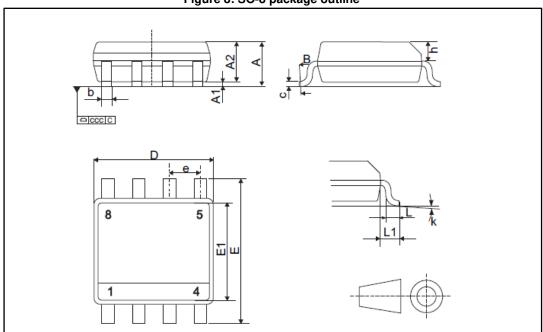


Table 5: SO-8 package mechanical data

	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			1.75			0.069
A1	0.1		0.25	0.004		0.010
A2	1.25			0.049		
b	0.31		0.51	0.012		0.020
С	0.10		0.25	0.004		0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
е		1.27			0.050	
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.05
L1		1.04			0.041	
k°	0		8	0		8
ccc			0.10			0.004

Package information LCP3121RL

Figure 9: Footprint recommendations, dimensions in mm (inches)

3.9
(0.153)

6.7
(0.263)

1.27
(0.050)

Figure 10: Marking layout (refer to ordering information table for marking)

Chamfer indicates pin 1

XXXXXX

XXXXXX: Marking ZZ: Manufacturing location Y: Year WW: week

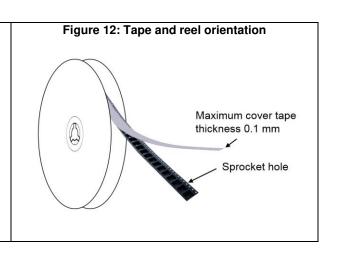
Figure 11: Package orientation in reel

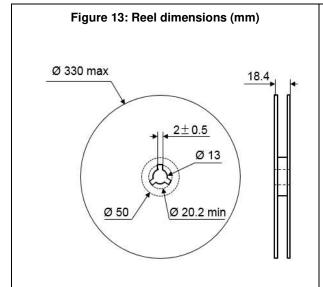
Pin 1

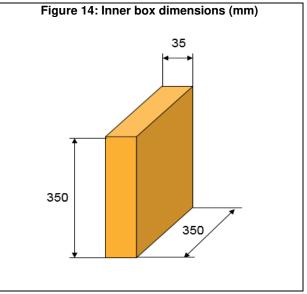
Pin 1

Taped according to EIA-481

Note: Pocket dimensions are not on scale
Pocket shape may vary depending on package







LCP3121RL Package information

Figure 15: Tape and reel outline

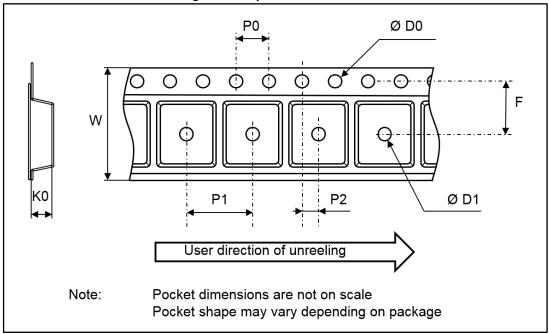


Table 6: Tape and reel mechanical data

	Dimensions				
Ref. Millimeters					
	Min.	Тур.	Max.		
P0	3.9	4	4.1		
P1	7.9	8	8.1		
P2	1.95	2	2.05		
ØD0	1.45	1.5	1.6		
ØD1	1.6				
F	5.45	5.5	5.55		
K0	2.5	2.6	2.7		
W	11.7	12	12.3		

Package information LCP3121RL

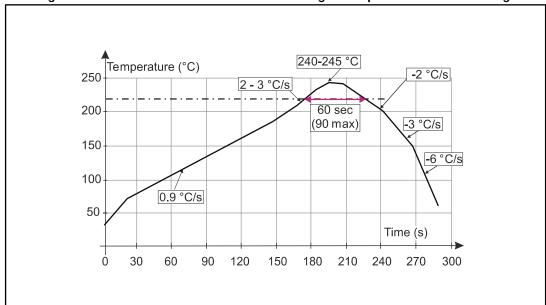


Figure 16: ST ECOPACK® recommended soldering reflow profile for PCB mounting



Minimize air convection currents in the reflow oven to avoid component movement. Maximum soldering profile corresponds to the latest IPC/JEDEC J-STD-020.

LCP3121RL Ordering information

# 3 Ordering information

**Table 7: Ordering information** 

Order code	Marking	Package	Weight	Base qty.	Delivery mode
LCP3121RL	CP3121	SO-8	0.08 g	2500	Tape and reel

# 4 Revision history

Table 8: Document revision history

Date	Revision	Changes
20-Dec-2016	1	Initial release.

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