

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

NPN Epitaxial Planar Silicon Transistor 2SC6082 - 50V / 15A High-Speed Switching **Applications**

Applications

• High-speed switching applications (switching regulator, driver circuit)

Features

- · Adoption of MBIT process
- Low collector-to-emitter saturation voltage
- Large current capacitance
- High-speed switching

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		60	V
Collector-to-Emitter Voltage	VCES		60	V
	VCEO		50	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		15	А
Collector Current (Pulse)	ICP	PW≤10μs, duty cycle≤1%	20	А
Base Current	IB		3	А
Collector Dissipation	PC		2	W
		Tc=25°C	23	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

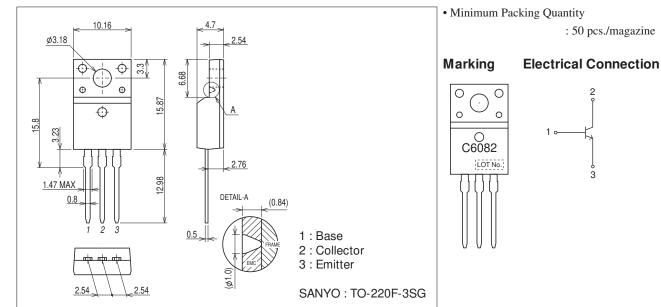
Product & Package Information : TO-220F-3SG

2

3

unit : mm (typ) 7529-002





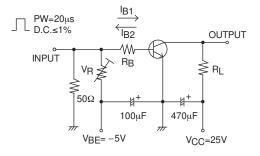
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12512 TKIM TC-00002709/72606/31506FA MSIM TB-00002089 No. A0279-1/5

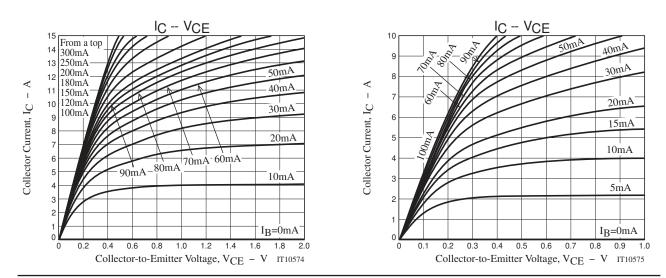
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Linit
			min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =40V, I _E =0A			10	μA
Emitter Cutoff Current	IEBO	V _{EB} =4V, I _C =0A			10	μA
DC Current Gain	hFE1	V _{CE} =2V, I _C =330mA	200		560	
	hFE2	V _{CE} =2V, I _C =10A	50			
Gain-Bandwidth Product	fT	VCE=10V, IC=2A		195		MHz
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		85		pF
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =7.5A, I _B =375mA		200	400	mV
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	IC=7.5A, IB=375mA			1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	IC=100μA, IE=0A	60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CES	IC=100μA, RBE=0Ω	60			V
	V(BR)CEO	I _C =1mA, R _{BE} =∞	50			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	IE=100μA, IC=0A	6			V
Turn-On Time	ton			52		ns
Storage Time	tstg	See specified Test Circuit		560		ns
Fall Time	tf			37		ns

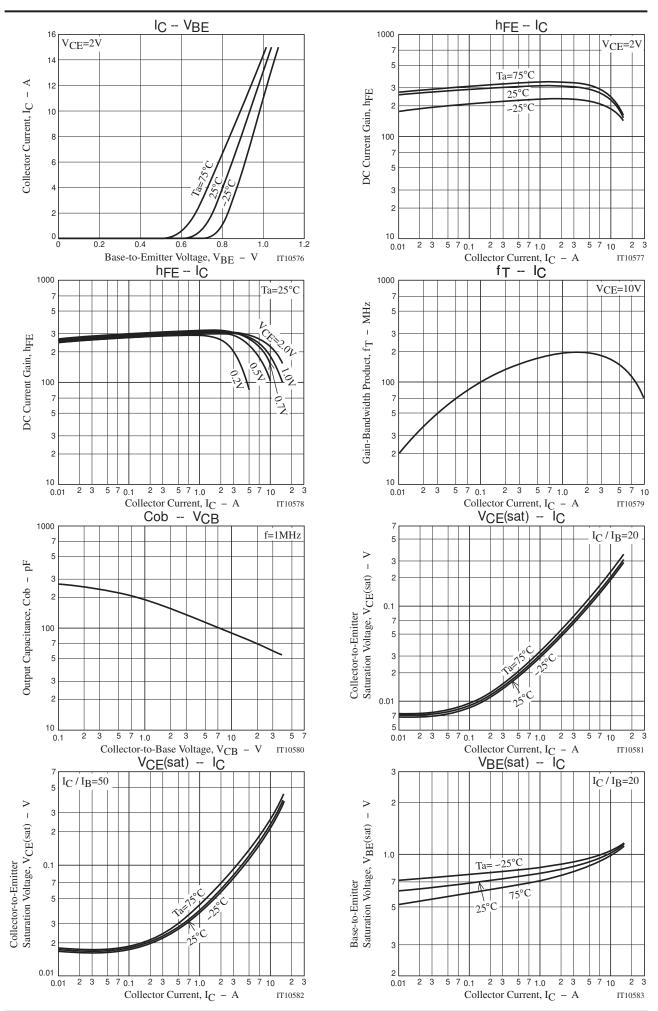
Switching Time Test Circuit

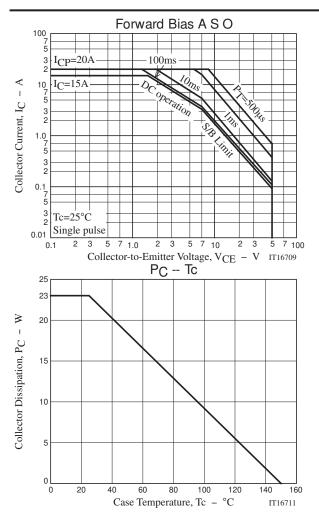


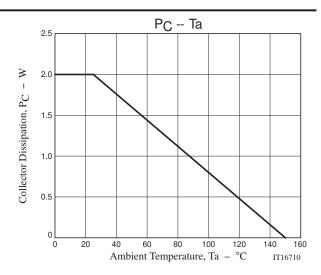
 $I_{C}=20I_{B1}=-20I_{B2}=5A$



2SC6082







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