

PMBT2907AYS

60V, 600 mA, double PNP switching transistor 26 June 2015

Product data sheet

1. General description

Double PNP switching transistor in a very small SOT363 (TSSOP6) Surface-Mounted Device (SMD) plastic package.

Double NPN complement: PMBT2222AYS

2. Features and benefits

- Double general-purpose switching transistor
- AEC-Q101 qualified

3. Applications

Switching and linear amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Per transistor								
h _{FE}	DC current gain	V_{CE} = -10 V; I_{C} = -150 mA; $t_{p} \le 300 \ \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C		100	-	300		
Per transistor								
V_{CEO}	collector-emitter voltage	open base		-	-	-60	V	
I _C	collector current			-	-	-600	mA	



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter TR1	654	6 5 4
2	В	base TR1		P = 100
3	С	collector TR2	0	TR1 TR2
4	E	emitter TR2	∐1 ∐2 ∐3	
5	В	base TR2	TSSOP6 (SOT363)	1 2 3
6	С	collector TR1		sym018

6. Ordering information

Table 3. Ordering information

Type number		Package					
		Name	Description	Version			
	PMBT2907AYS	TSSOP6	plastic surface-mounted package; 6 leads	SOT363			

7. Marking

Table 4. Marking codes

	Marking code [1]
PMBT2907AYS	BH%

[1] % = placeholder for manufacturing site code

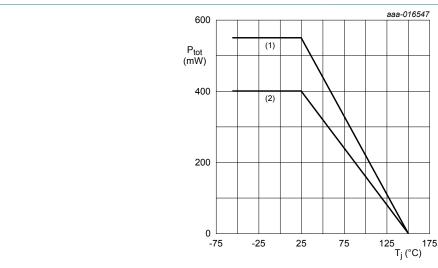
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transis	tor		<u> </u>			
V _{CBO}	collector-base voltage	open emitter		-	-60	V
V _{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-800	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
			[2]	-	300	mW
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	400	mW
			[2]	-	550	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm²



(1) FR4 PCB; mounting pad for collector 1 cm²

(2) FR4 PCB; standard footprint

Fig. 1. Per device: Power derating curves SOT363 (SC-88)

PMBT2907AYS

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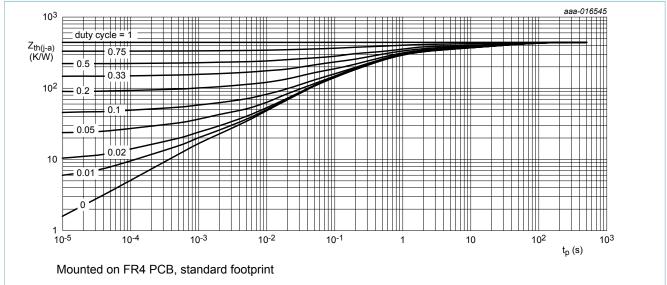
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Thermal characteristics

Thermal characteristics Table 6.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Per transistor								
R _{th(j-a)}	thermal resistance	in free air	[1]	-	-	500	K/W	
	from junction to ambient		[2]	-	-	417	K/W	
Per device		'	,					
R _{th(j-a)}	thermal resistance	in free air	[1]	-	-	313	K/W	
	from junction to ambient		[2]	-	-	227	K/W	

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm²



Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; Fig. 2. typical values

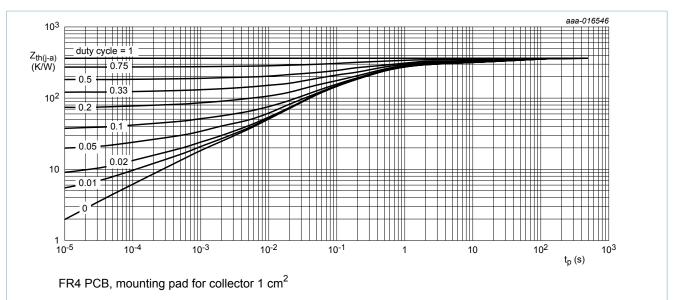
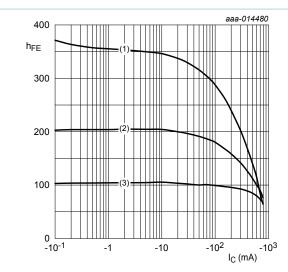


Fig. 3. Per Transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transi	stor					
I _{CBO}	collector-base cut-off	V _{CB} = -50 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-10	nA
	current	V _{CB} = -50 V; I _E = 0 A; T _j = 125 °C	-	-	-10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-50	nA
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -0.1 mA; T _{amb} = 25 °C	75	-	-	
		V _{CE} = -10 V; I _C = -1 mA; T _{amb} = 25 °C	100	-	-	
		V _{CE} = -10 V; I _C = -10 mA; T _{amb} = 25 °C	100	-	-	
		V_{CE} = -10 V; I_{C} = -150 mA; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	100	-	300	
		V_{CE} = -10 V; I_{C} = -500 mA; t_{p} ≤ 300 μ s; δ ≤ 0.02; T_{amb} = 25 °C	50	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = -150 mA; I_B = -15 mA; $t_p \le 300$ μs; $δ \le 0.02$; T_{amb} = 25 °C	-	-	-400	mV
		I_C = -500 mA; I_B = -50 mA; $t_p \le 300$ μs; $δ \le 0.02$; T_{amb} = 25 °C	-	-	-1.6	V
V _{BEsat}	base-emitter saturation voltage	I_C = -150 mA; I_B = -15 mA; $t_p \le 300$ μs; $δ \le 0.02$; T_{amb} = 25 °C	-	-	-1.3	V
		I_C = -500 mA; I_B = -50 mA; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	-2.6	V
t _d	delay time	I _C = -150 mA; I _{Bon} = -15 mA;	-	-	12	ns
t _r	rise time	I _{Boff} = 15 mA; T _{amb} = 25 °C	-	-	30	ns
t _{on}	turn-on time		-	-	40	ns
t _s	storage time		-	-	300	ns
t _f	fall time		-	-	65	ns
t _{off}	turn-off time		-	-	365	ns
C _C	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	-	-	8	pF
C _E	emitter capacitance	V_{EB} = -2 V; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	30	pF
f _T	transition frequency	V_{CE} = -20 V; I_{C} = -50 mA; f = 100 MHz; T_{amb} = 25 °C	200	-	-	MHz



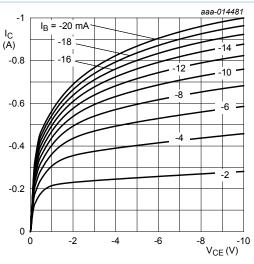
$$V_{CE} = -10 \text{ V}$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

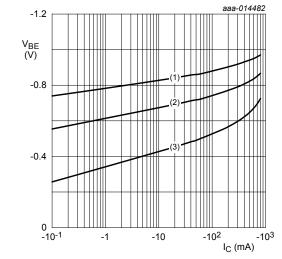
(3)
$$T_{amb} = -55$$
 °C

Fig. 4. DC current gain as a function of collector current; typical values



 T_{amb} = 25 °C

Fig. 5. Collector current as a function of collectoremitter voltage; typical values



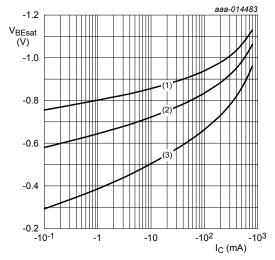
$$V_{CE}$$
 = -10 V

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 6. Base-emitter voltage as a function of collector current; typical values



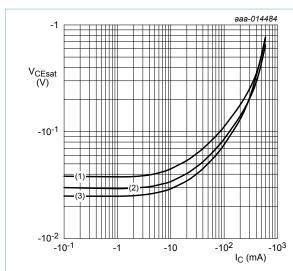
$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values



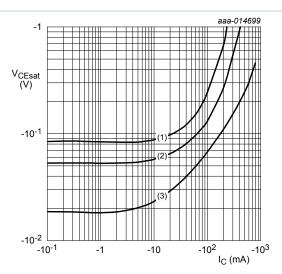
$$I_C/I_B = 20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values



$$T_{amb}$$
 = 25 °C

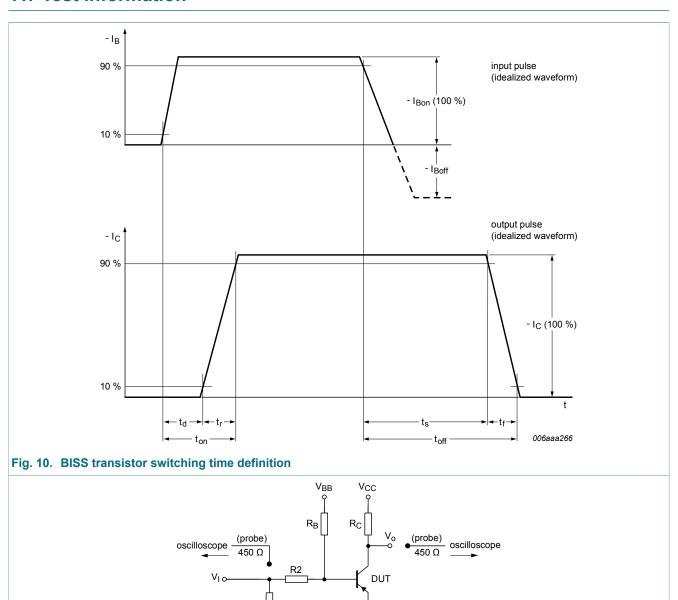
(1)
$$I_C/I_B = 100$$

(2)
$$I_C/I_B = 50$$

(3)
$$I_C/I_B = 10$$

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

11. Test information



11.1 Quality information

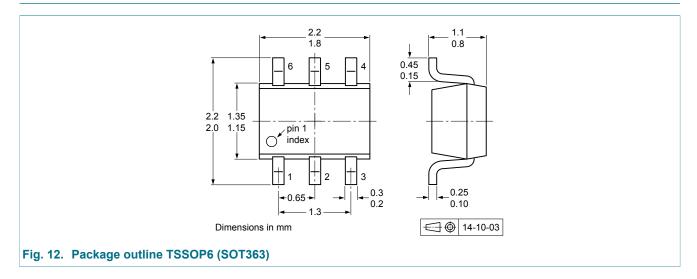
Fig. 11. Test circuit for switching times

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

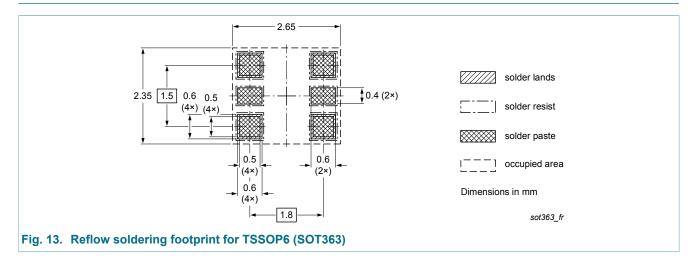
mgd624

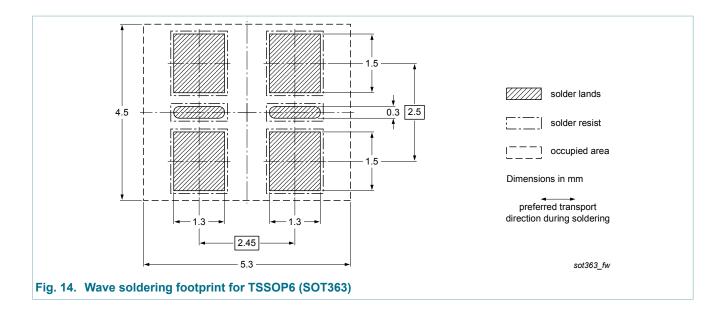
60V, 600 mA, double PNP switching transistor

12. Package outline



13. Soldering





14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBT2907AYS v.1	20150626	Product data sheet	-	-

60V, 600 mA, double PNP switching transistor

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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