

EM-1712

Shipped in packet-tape reel(5000pcs/Reel)

EM-1712 is ultra-small Hall effect ICs of a single silicon chip composed of Hall element and a signal processing IC.

Bipolar Hall Effect Latch Supply Voltage 1.6~5.5V

Power down Function

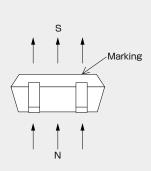
Ultra High Sensitivity Bop: 1.8mT

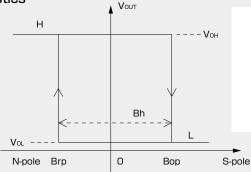
Output **CMOS**

SMT

Notice: It is requested to read and accept "IMPORTANT NOTICE" written on the back of the front cover of this catalogue.

Operational Characteristics







Magnetic flux density

● Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Min.	Max.	Unit
Supply Voltage	V _{DD}	-0.1	6.0	V
PDN input voltage	VIN	-0.1	V _{DD} +0.1	V
PDN input current	lin	-10	+10	mA
Output Current	Іоит	-0.5	+0.5	mA
Storage Temperature Range	Тѕтс	-40	+125	°C

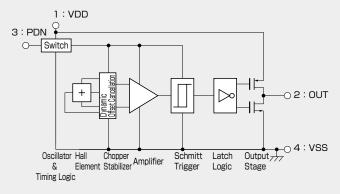
Recommended Operating Conditions

Item	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage	V _{DD}	1.6	3.0	5.5	V
Operating Temperature Range	Topr	-30	+25	+85	°C

■Magnetic ① and Electrical Characteristics (Ta=25°C VDD=3.0V)

Symbol	Conditions	Min.	Тур.	Max.	Unit
Вор			1.8	4.0	mT
Brp		-4.0	-1.8		mT
Bh			3.6		mT
VIH		0.7V _{DD}			V
VIL				0.3	V
Vон	lo=-0.5mA	V _{DD} -0.4			V
Vol	Io=+0.5mA			0.4	V
loo1	PDN=L			1	μΑ
loo2	PDN=H,Average		60	150	μΑ
lιΝ		-1		1	μΑ
Tpd1	Active→PDN			(36.6)	μs
T _{PD} 2	PDN→Active			100	μs
	Bop Brp Bh ViH VolL Voh Ibb1 Ibb2 IIN	Bop Brp Bh ViH ViL VoH Io=−0.5mA VoL Io=+0.5mA IDD1 PDN=L IDD2 PDN=H,Average IIN TPD1 Active→PDN	Bop	Bop 1.8 Brp -4.0 -1.8 Bh 3.6 Vih 0.7Vdd	Bop 1.8 4.0 Brp -4.0 -1.8 Bh 3.6 VIH 0.7Vod VIL 0.3 VOH Io=-0.5mA Vod -0.4 VOL Io=+0.5mA 0.4 IDD1 PDN=L 1 IDD2 PDN=H,Average 60 150 IIN -1 1 TPD1 Active→PDN (36.6)

Functional Block Diagram



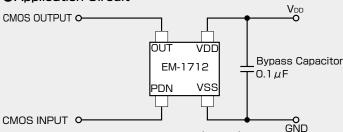
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Pulse Drive Period	T _{PD} 3	PDN=H	0.5	1.0	1.5	ms
PDN input Pluse Width	Tw		100			μs
Pulse Drive Time	T _{PD} 4	PDN=H	12.2	24.4	36.6	μs

■Magnetic Characteristics ② (Ta=-30~+85°C VDD=3.0V)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Operating Point	Вор			1.8	4.2	mT
Releasing Point	Brp		-4.2	-1.8		mT
Hysteresis	Bh			3.6		mT

Note) The above specifications are design targets.

Application Circuit



- *1: Positive("+") polarity flux is defined as the magnetic flux from south polewhich is direct toward to the branded face of the sensor (Bop, Brp)
 *2: In case of PDN pin is held at VDD or GND.
 *3: This transition time is not guarantee

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Package (Unit:mm) ●(For reference only)Land Pattern (Unit:mm) 0.25 0.50 03 2 φ0.3 2.1±0.2 0~0.1 Sensor center 0.90 25 8 3 0.05 0.1 Sensor çenter Note 1) The sensor center is located within the ϕ 0.3mm circle. 0.55 က Note2) The tolerances of dimensions with no mentions is ± 0.1 mm. Note3) Coplanarity: The differences between <u>5°</u> standoff of terminals are max.0.1mm. 1.30 Pin No. Note4) The sensor part is located 0.4mm(typ.) Pin Name | Function far from marking surface. VDD Power Supply OUT Output PDN Power Down

Function Timing Chart 1 B[mT] S Вор 0 Bro VPDN [V] Tpp2(<100 µs) Tpo2(≮100/μs) Vout [V] Undefined $T_{PD}1(<36.6\mu s)$ $T_{PD}1(<36.6\mu s)$ IDD [mA] **Functional Timing**

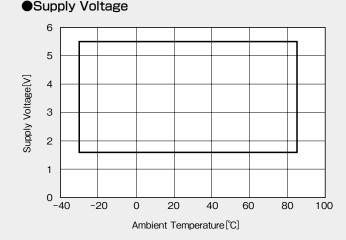
Note 1) During power down mode, output is latched in its previous state.

VSS

Ground

When VDD is supplied, the time from reaching V_{DD} =

1.6V to the update of the output state is equal to TPD2.





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●Function Timing Chart 2 (PDN=H)

В Brp

Vout

High

Low

Releasing Point Timing

T_{PD}3

Operating Point Timing

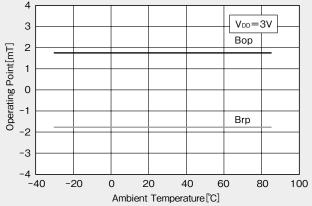
В

Bop

Vout

High

Low



g

n

p

0

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