



14 Gbps, 2:1 SELECTOR

Pin Descriptions

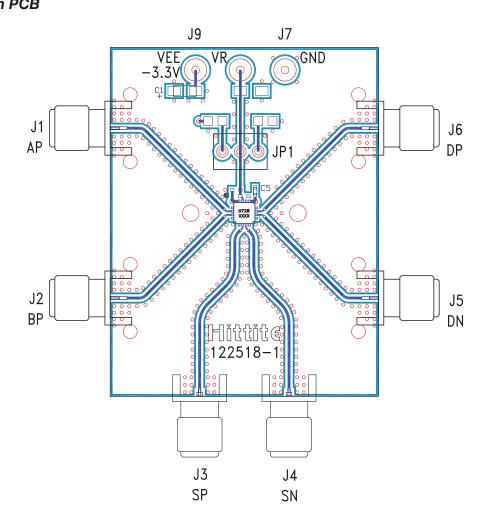
Pin Number	Function	Description	Interface Schematic
1, 4, 5, 8, 9, 12	GND	Signal Grounds.	→ GND —
2, 3	АР, ВР	Single-Ended Data Inputs: Current Mode Logic (CML) referenced to positive supply.	GND GND xP O
6, 7	SP, SN	Differential Select Inputs: Current Mode Logic (CML) referenced to positive supply	GND GND GND SP O SN
10, 11	DP, DN	Differential Data Outputs: Common Mode Logic (CML) referenced to positive supply.	GND O GND DP O DN
13, 16	Vee	Negative Supply	
14, Package Base	GND	Supply Ground	GND =
15	N/C	No Connection	

HIGH SPEED LOGIC - SMT

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Evaluation PCB



List of Materials for Evaluation PCB 122520 [1]

Item	Description
J1 - J6	PCB Mount SMA RF Connectors
J7, J9	DC Pin
C1	4.7 μF Capacitor, Tantalum
C5	100 pF Capacitor, 0402 Pkg.
U1	HMC728LC3C High Speed Logic, 2:1 Selector
PCB [2]	122518 Evaluation Board

^[1] Reference this number when ordering complete evaluation PCB

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads should be connected directly to the ground plane similar to that shown. The exposed package base should be connected to GND. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

^[2] Circuit Board Material: Arlon 25FR or Rogers 4350

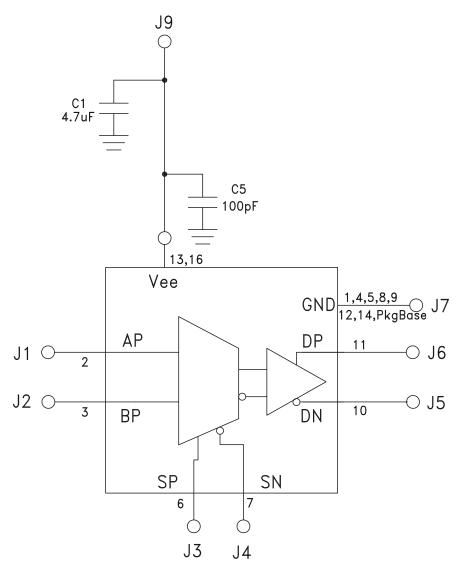
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Application Circuit



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