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# October 2003



# FDP7030BL/FDB7030BL

# N-Channel Logic Level PowerTrench<sup>®</sup> MOSFET

# **General Description**

This N-Channel Logic Level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

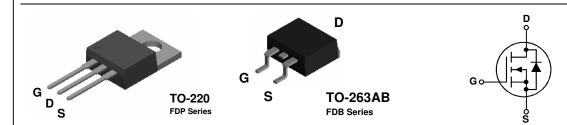
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable  $R_{\text{DS}(\text{ON})}$  specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

It has been optimized for low gate charge, low  $R_{\text{DS}(\text{ON})}$  and fast switching speed.

# Features

- 60 A, 30 V  $R_{DS(ON)} = 9 \ m\Omega \ @ V_{GS} = 10 \ V$  $R_{DS(ON)} = 12 \ m\Omega \ @ V_{GS} = 4.5 \ V$
- Critical DC electrical parameters specified at elevated temperature
- High performance trench technology for extremely low R<sub>DS(ON)</sub>
- 175°C maximum junction temperature rating



# Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		30	V
V <sub>GSS</sub>	Gate-Source Voltage		± 20	V
ID	Drain Current – Continuous	(Note 1)	60	А
	– Pulsed	(Note 1)	180	
P <sub>D</sub>	Total Power Dissipation @ $T_c = 25^{\circ}C$		60	W
	Derate a	above 25°C	0.4	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-65 to +175	°C

# **Thermal Characteristics**

R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	2.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

# Package Marking and Ordering Information

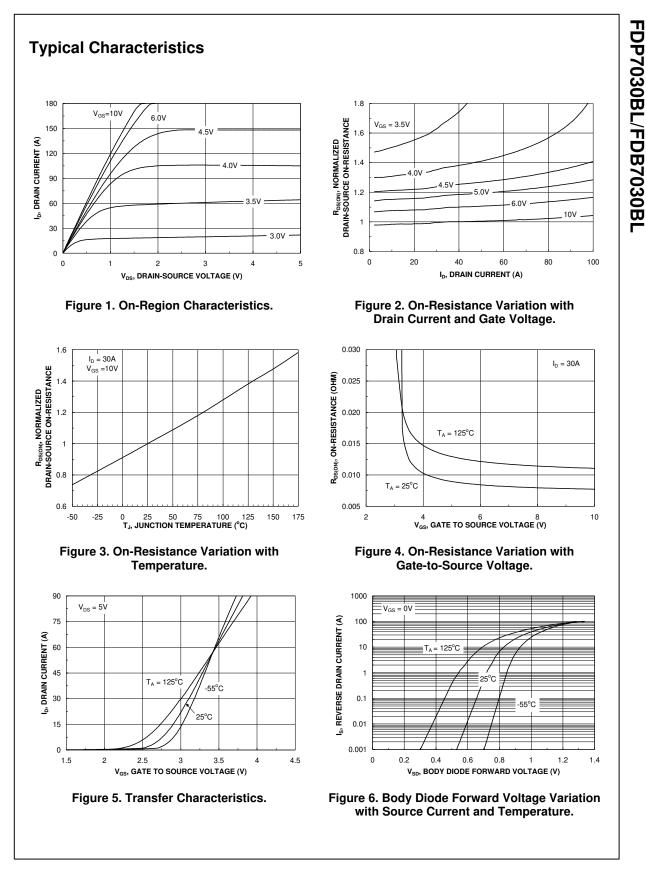
Device Marking	Device	Reel Size	Tape width	Quantity
FDB7030BL	FDB7030BL	13"	24mm	800 units
FDP7030BL	FDP7030BL	Tube	n/a	45

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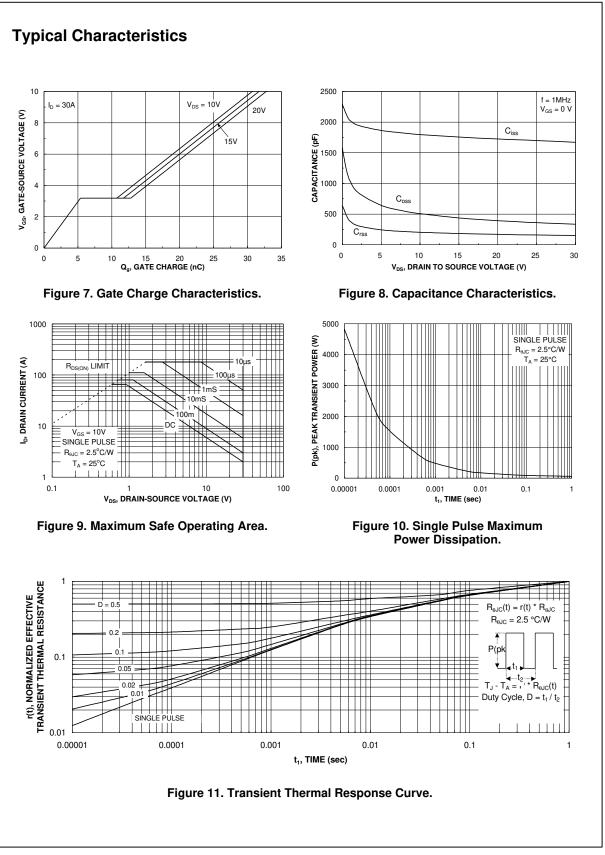
Symbol	Parameter	Test Conditions	Min	Тур	Мах	Units
-				71	_	
W <sub>DSS</sub>	Durce Avalanche Ratings (Note Single Pulse Drain-Source	$V_{DD} = 15 \text{ V},  I_D = 60 \text{ A}$			73	mJ
055	Avalanche Energy				70	mo
I <sub>AR</sub>	Maximum Drain-Source Avalanche Current				60	A
Off Char	acteristics					
BV <sub>DSS</sub>	Drain–Source Breakdown Voltage	$V_{GS}=0~V, \qquad I_D=250~\mu A$	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$		22		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 24 \ V, \qquad V_{\text{GS}} = 0 \ V$			1	μA
I <sub>GSS</sub>	Gate-Body Leakage	$V_{\text{GS}}=\pm20~\text{V}, V_{\text{DS}}=0~\text{V}$			± 100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1	1.9	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$		-5		mV/°C
R <sub>DS(on)</sub>	Static Drain-Source On-	$V_{GS} = 10 \text{ V}, \qquad I_D = 30 \text{ A}$		6.8	9	
	Resistance	$V_{\text{GS}} = 4.5 \ \text{V}, \qquad I_{\text{D}} = 25 \ \text{A}$		8.5	12	mΩ
		$V_{GS}$ = 10 V, $I_D$ = 30 A, $T_J$ =125°C		10.1	18	
I <sub>D(on)</sub>	On–State Drain Current	$V_{GS} = 10 \text{ V},  V_{DS} = 10 \text{ V}$	30			A
<b>g</b> fs	Forward Transconductance	$V_{DS} = 10V, \qquad I_D = 30 \text{ A}$		85		S
Dynamic	Characteristics	1				
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 15 V$ , $V_{GS} = 0 V$ ,		1760		pF
Coss	Output Capacitance	f = 1.0 MHz		440		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	7		185		pF
R <sub>G</sub>	Gate Resistance	$V_{GS} = 15 \text{ mV},  f = 1.0 \text{ MHz}$		1.2		Ω
Switchin	g Characteristics (Note 2)					
t <sub>d(on)</sub>	Turn–On Delay Time	$V_{\text{DD}} = 15V, \qquad I_{\text{D}} = 1 \text{ A},$		12	22	ns
tr	Turn–On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		12	22	ns
t <sub>d(off)</sub>	Turn–Off Delay Time	7		30	48	ns
t <sub>f</sub>	Turn–Off Fall Time			19	33	ns
Qg	Total Gate Charge	$V_{DS} = 15 V$ , $I_{D} = 30 A$ ,		17	24	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = 5 V$		5.4		nC
Q <sub>gd</sub>	Gate-Drain Charge			6.4		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				60	Α
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \ V,  I_S = 30 \ A $ (Note 1)		0.92	1.3	V
t <sub>rr</sub>	Diode Reverse Recovery Time	I <sub>F</sub> = 30 A,			30	nS
Q <sub>rr</sub>	Diode Reverse Recovery Charge	d <sub>i</sub> F/d <sub>t</sub> = 100 A/μs			20	nC

1. Pulse Test: Pulse Width < 300 $\mu$ s, Duty Cycle < 2.0%

# FDP7030BL/FDB7030BL



FDP7030BL/FDB7030BL Rev D1(W)



FDP7030BL/FDB7030BL Rev D1(W)

FDP7030BL/FDB7030BL

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