



BSS123

N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
100V	6.0Ω @ V _{GS} = 10V	0.17A

Features and Benefits

- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- High Drain-Source Voltage Rating
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

 $\underline{\text{https://www.diodes.com/products/automotive/automotive-products/.}}$

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Description and Applications

These N-Channel enhancement mode field effect transistors are produced using Diodes Incorporated's proprietary, high density and advanced trench technology. These products have been designed to minimize on-state resistance while providing rugged, reliable and fast switching performance. These products are particularly suited for low voltage, low current applications such as:

- Small Servo Motor Control
- Power MOSFET Gate Drivers
- Switching Applications

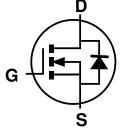
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)

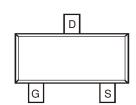




Top View



Equivalent Circuit



Top View

Ordering Information (Note 4)

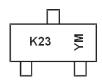
Part Number	Case	Packaging
BSS123-7-F	SOT23	3,000/Tape & Reel
BSS123-13-F	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



 $\begin{array}{l} \text{K23} = \text{Product Type Marking Code} \\ \text{YM} = \text{Date Code Marking} \\ \text{Y or } \overline{\text{Y}} = \text{Year (ex: I} = 2021) \\ \text{M} = \text{Month (ex: 9} = \text{September)} \end{array}$

Date Code Key

Year	2002		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	0		- 1	J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	Juii		IVIGI	- Api	iviay	oun	oui	Aug	Jep	OCI	1404	Dec

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage	Continuous	Vgss	±20	V
Continues Durin Comment (Nata 5) V	Continuous	ID	0.17	^
Continuous Drain Current (Note 5) V _{GS} = 10V	Pulsed	I _{DM}	0.68	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	Reja	417	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

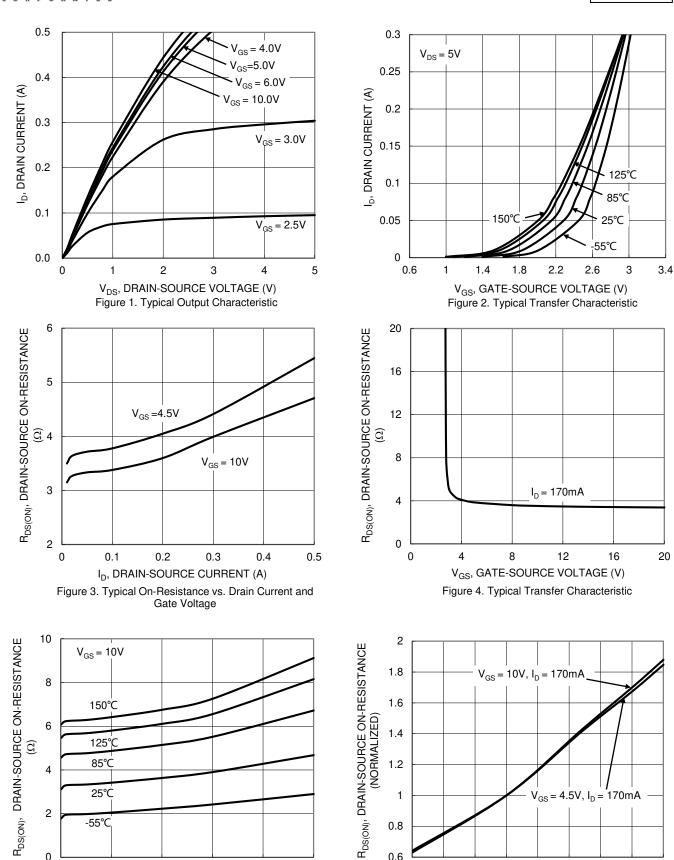
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 6)								
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	$V_{GS} = 0V, I_{D} = 250 \mu A$		
		_	_	0.1	μΑ	$V_{DS} = 100V$, $V_{GS} = 0V$		
Zero Gate Voltage Drain Current	IDSS	_	_	30	μΑ	$V_{DS} = 100V, V_{GS} = 0V$ @ $T_{A} = +150^{\circ}C$ (Note 7)		
		_	_	10	nA	V _{DS} = 20V, V _{GS} = 0V		
Gate-Source Leakage, Forward	IGSSF	_	_	50	nA	$V_{GS} = 20V$, $V_{DS} = 0V$		
ON CHARACTERISTICS (Note 6)								
Gate Threshold Voltage	V _{GS(TH)}	0.8	1.4	2.0	V	$V_{DS} = V_{GS}$, $I_D = 1mA$		
Static Drain-Source On-Resistance	Provent.	_	3.2	6.0	Ω	$V_{GS} = 10V, I_D = 0.17A$		
Static Diani-Source On-Resistance	RDS(ON)	_	3.8	10		$V_{GS} = 4.5V, I_{D} = 0.17A$		
Forward Transfer Admittance	grs	80	370	_	ms	$V_{DS} = 10V$, $I_{D} = 0.17A$, $f = 1.0kHz$		
Diode Forward Voltage	V_{SD}	_	0.84	1.3	>	$V_{GS} = 0V, I_S = 0.34A$		
DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance	C _{iss}	_	22	60				
Output Capacitance	Coss	_	3.5	15	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$		
Reverse Transfer Capacitance	Crss	_	2.0	6				
SWITCHING CHARACTERISTICS (Note 7)								
Turn-On Delay Time	td(ON)	_	_	8	ns			
Turn-On Rise Time	t _R	_	_	8	ns	$V_{GS} = 10V, V_{DD} = 30V$		
Turn-Off Delay Time	tD(OFF)	_	_	13	ns	$I_D = 0.28A$, $R_{GEN} = 50\Omega$		
Turn-Off Fall Time	tF	_	_	16	ns			

Notes: 5. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

^{6.} Short duration pulse test used to minimize self-heating effect.

^{7.} Guaranteed by design. Not subject to production testing.





I_D, DRAIN CURRENT (A) Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

0.3

0.4

0.2

50

75

25

0

0

-55°C

0.1

0.5

8.0

0.6

-50

125

150

100



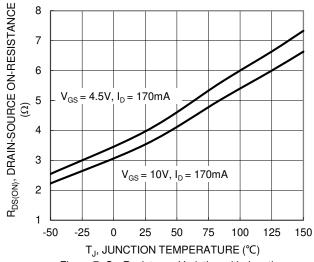
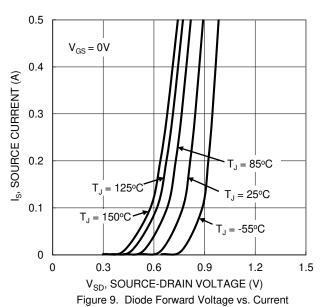


Figure 7. On-Resistance Variation with Junction Temperature



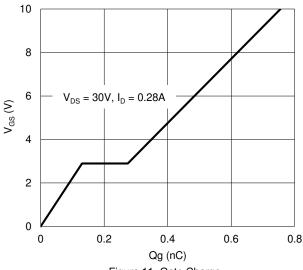


Figure 11. Gate Charge

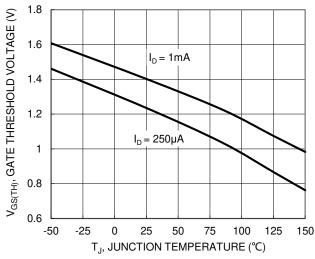
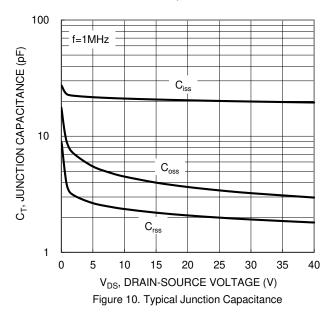


Figure 8. Gate Threshold Variation vs. Junction Temperature



R_{DS(ON)} Limited ID, DRAIN CURRENT (A) 0.1 P_W =10ms P_w =100ms 0.01 $T_{J(Max)} = 150^{\circ}C$ $T_{C} = 25^{\circ}C$ Single Pulse DUT on 1*MRP Board $P_W = 10s$ DC $V_{GS} = 10V$ 0.001 0.1 100 1000 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



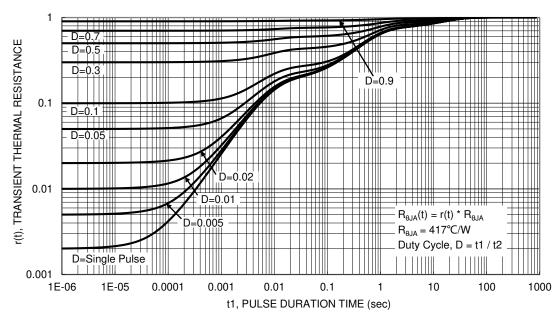


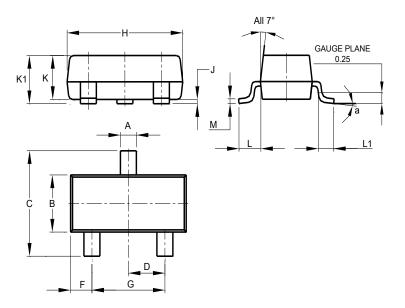
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

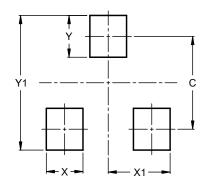
SOT23



	SOT23						
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
M	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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