

N-channel 80 V, 8.5 mΩ logic level MOSFET in LFPAK56 8 May 2013 Product data sheet

1. General description

Logic level N-channel MOSFET in an LFPAK56 (Power SO8) package using TrenchMOS technology. This product has been designed and qualified to AEC Q101 standard for use in high performance automotive applications.

2. Features and benefits

- Q101 compliant
- Repetitive avalanche rated
- Suitable for thermally demanding environments due to 175 °C rating
- True logic level gate with V_{GS(th)} rating of greater than 0.5 V at 175 °C

3. Applications

- 12 V, 24 V and 48 V Automotive systems
- Motors, lamps and solenoid control
- Transmission control
- Ultra high performance power switching

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	80	V
I _D	drain current	V _{GS} = 5 V; T _{mb} = 25 °C; <u>Fig. 1</u>	[1]	-	-	100	А
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	-	238	W
Static characte	eristics	·					
R _{DSon}	drain-source on-state resistance	V _{GS} = 5 V; I _D = 25 A; T _j = 25 °C; <u>Fig. 11</u>		-	6.3	8.5	mΩ
Dynamic characteristics							
Q _{GD}	gate-drain charge	$V_{GS} = 5 V; I_D = 25 A; V_{DS} = 64 V;$ T _j = 25 °C; <u>Fig. 13</u> ; <u>Fig. 14</u>		-	17.1	-	nC

[1] Continuous current is limited by package.

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S	source	mb	D
2	S	source		
3	S source	q	G	
4	G	gate	000	mbb076 S
mb	D	mounting base; connected to drain	1 2 3 4 LFPAK56; Power- SO8 (SOT669)	

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
BUK9Y8R5-80E	LFPAK56; Power-SO8	Plastic single-ended surface-mounted package (LFPAK56; Power-SO8); 4 leads	SOT669			

7. Marking

Table 4. Marking codes	
Type number	Marking code
BUK9Y8R5-80E	98E580

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	80	V
V _{DGR}	drain-gate voltage	R _{GS} = 20 kΩ		-	80	V
V _{GS}	gate-source voltage	T _j ≤ 175 °C; DC		-10	10	V
		$T_j \le 175 \text{ °C}; \text{ Pulsed}$	[1][2]	-15	15	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 5 V; <u>Fig. 1</u>	<u>[3]</u>	-	100	А
		T _{mb} = 100 °C; V _{GS} = 5 V; <u>Fig. 1</u>	[3]	-	75	А
I _{DM}	peak drain current	T_{mb} = 25 °C; pulsed; $t_p \le 10 \ \mu$ s; Fig. 4		-	423	А
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	238	W

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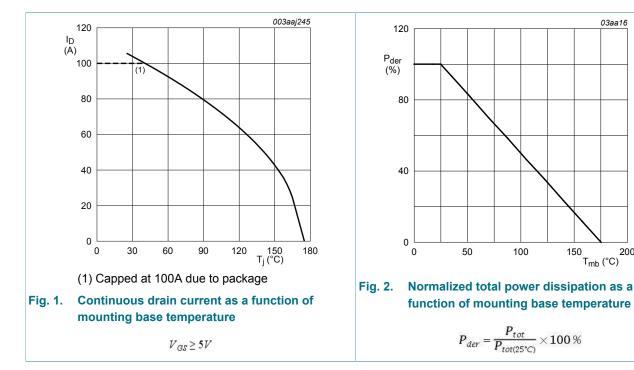
150 T_{mb} (°C)

200

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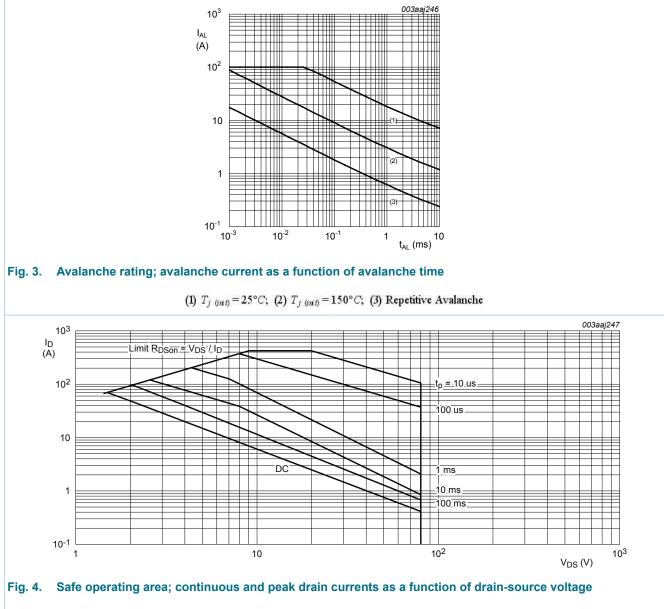
Symbol	Parameter	Conditions		Min	Max	Unit
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
Source-drai	in diode					
l _S	source current	T _{mb} = 25 °C	[3]	-	100	А
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^\circ C$		-	423	А
Avalanche r	ruggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 100 A; V _{sup} ≤ 80 V; R _{GS} = 50 Ω; V _{GS} = 5 V; T _{j(init)} = 25 °C; unclamped; Fig. 3	[4][5]	-	148	mJ

- Accumulated pulse duration up to 50 hours delivers zero defect ppm Significantly longer life times are achieved by lowering $\rm T_{j}$ and or $\rm V_{GS}$ [1]
- [2]
- Continuous current is limited by package. [3]
- Single-pulse avalanche rating limited by maximum junction temperature of 175 °C. [4]
- Refer to application note AN10273 for further information. [5]



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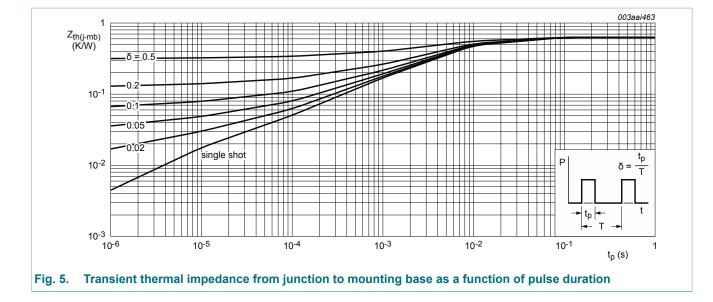


 $T_{mb} = 25^{\circ}C; \ I_{DM}$ is a single pulse

9. Thermal characteristics

Table 6. The	ermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	<u>Fig. 5</u>	-	-	0.63	K/W

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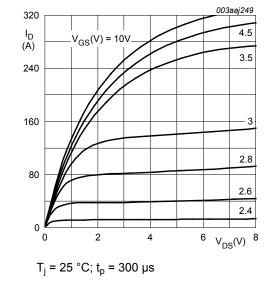


10. Characteristics

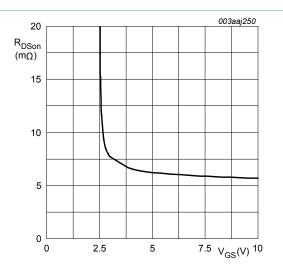
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	· · · · ·	I			
V _{(BR)DSS}	drain-source	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C	80	-	-	V
	breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = -55 °C	72	-	-	V
V _{GS(th)}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ Fig. 9; Fig. 10	1.4	1.7	2.1	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ Fig. 9	-	-	2.45	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ Fig. 9	0.5	-	-	V
I _{DSS}	drain leakage current	V_{DS} = 80 V; V_{GS} = 0 V; T_j = 25 °C	-	0.07	10	μA
I _{DSS}	drain leakage current	V_{DS} = 80 V; V_{GS} = 0 V; T_j = 175 °C	-	-	500	μA
I _{GSS}	gate leakage current	V_{GS} = 10 V; V_{DS} = 0 V; T_j = 25 °C	-	2	100	nA
		V_{GS} = -10 V; V_{DS} = 0 V; T_j = 25 °C	-	2	100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 5 V; I _D = 25 A; T _j = 25 °C; <u>Fig. 11</u>	-	6.3	8.5	mΩ
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; Fig. 11	-	5.8	8	mΩ
		V _{GS} = 5 V; I _D = 25 A; T _j = 175 °C; Fig. 11; Fig. 12	-	-	21.3	mΩ

N-channel 80 V, 8.5 mΩ logic level MOSFET in LFPAK56

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Dynamic cl	naracteristics	· · · · · · · · · · · · · · · · · · ·	I			
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 64 \text{ V}; V_{GS} = 5 \text{ V};$	-	54.7	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C; <u>Fig. 13; Fig. 14</u>	-	13.5	-	nC
Q _{GD}	gate-drain charge		-	17.1	-	nC
C _{iss}	input capacitance	V _{GS} = 0 V; V _{DS} = 25 V; f = 1 MHz; T _j = 25 °C; <u>Fig. 15</u>	-	6125	8167	pF
C _{oss}	output capacitance		-	397	476	pF
C _{rss}	reverse transfer capacitance		-	207	284	pF
t _{d(on)}	turn-on delay time	V_{DS} = 60 V; R _L = 2.4 Ω; V _{GS} = 5 V; R _{G(ext)} = 5 Ω; T _j = 25 °C	-	28	-	ns
t _r	rise time		-	50	-	ns
t _{d(off)}	turn-off delay time		-	82	-	ns
t _f	fall time		-	45	-	ns
Source-dra	in diode		I			
V _{SD}	source-drain voltage	I_{S} = 25 A; V_{GS} = 0 V; T_{j} = 25 °C; <u>Fig. 16</u>	-	0.82	1.2	V
t _{rr}	reverse recovery time	$I_{\rm S}$ = 20 A; dI_{\rm S}/dt = -100 A/µs; V _{GS} = 0 V;	-	30.9	-	ns
Q _r	recovered charge	V _{DS} = 25 V; T _j = 25 °C	-	36.3	-	nC







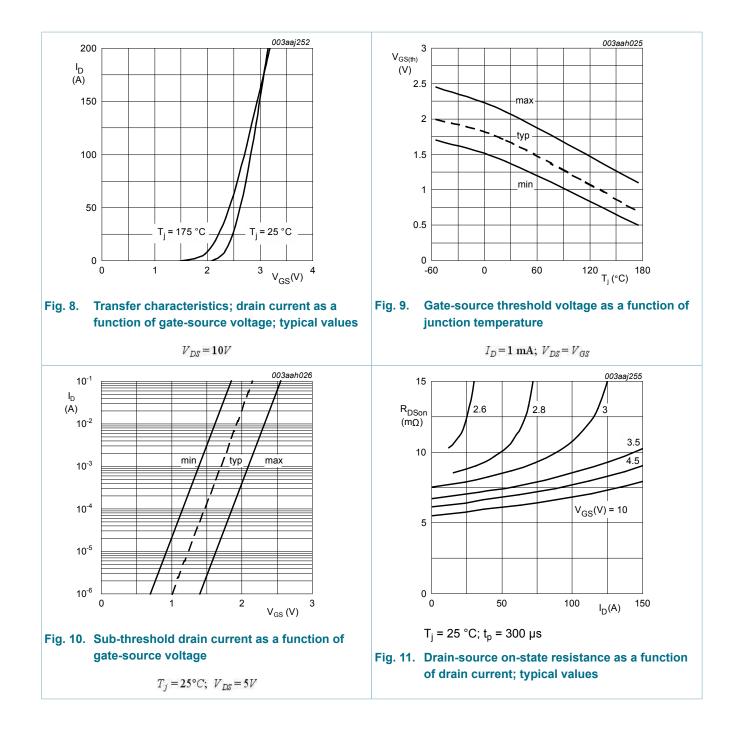


 $T_j = 25^{\circ}C; \ I_D = 25A$

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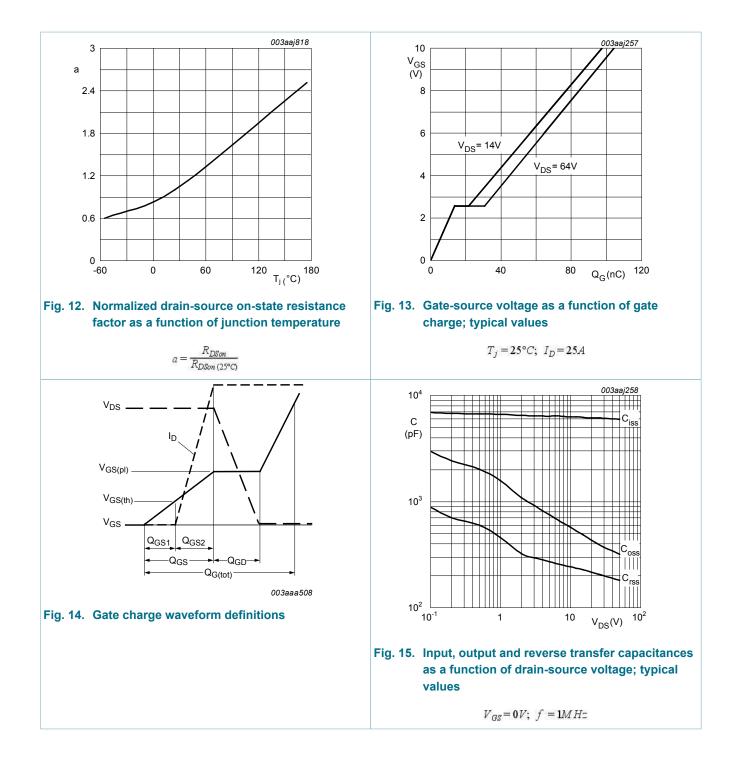
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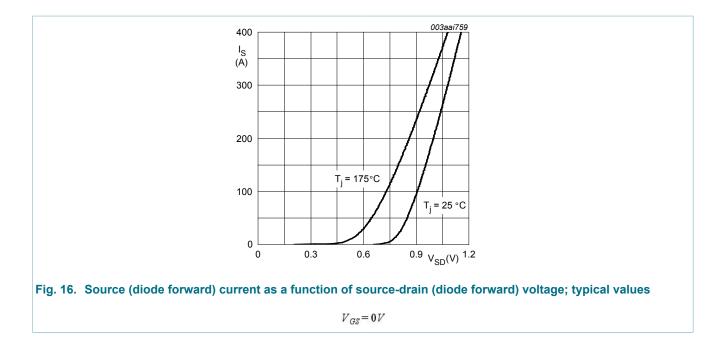
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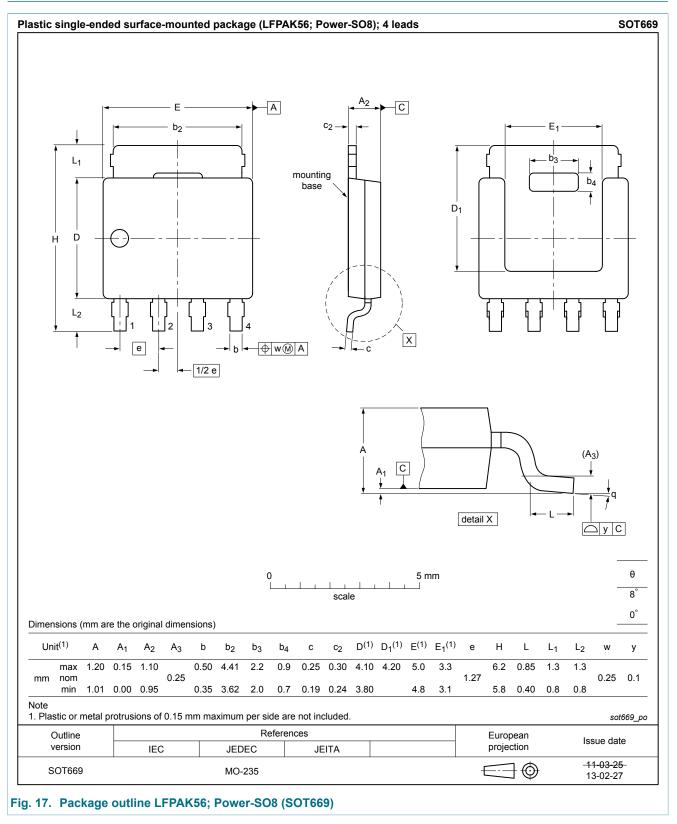
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11. Package outline



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12. Legal information

12.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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N-channel 80 V, 8.5 mΩ logic level MOSFET in LFPAK56

13. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	2
8	Limiting values	2
9	Thermal characteristics	4
10	Characteristics	5
11	Package outline	10
12	Legal information	11
12.1	Data sheet status	11
12.2	Definitions	11
12.3	Disclaimers	11
12.4	Trademarks	12

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