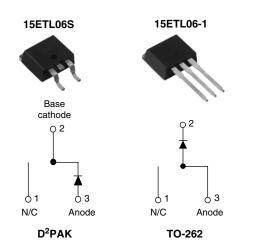
Vishay High Power Products

# Ultralow V<sub>F</sub> Hyperfast Rectifier for Discontinuous Mode PFC, 15 A FRED $Pt^{TM}$



PRODUCT SUMMARY				
V <sub>F</sub> (typical)	0.99 V			
I <sub>F(AV)</sub>	15 A			
V <sub>R</sub>	600 V			

#### FEATURES

- Benchmark ultralow forward voltage drop
- Hyperfast recovery time
- Low leakage current
- 175 °C operating junction temperature
- Designed and qualified for industrial level

#### DESCRIPTION

State of the art, ultralow  $V_F$ , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

#### **APPLICATIONS**

AC-DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC-DC power supplies.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V	
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 154 °C	15		
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	250	А	
Peak repetitive forward current	I <sub>FM</sub>		30		
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-		
Forward voltage	VF	I <sub>F</sub> = 15 A	-	0.99	1.05	V	
Forward voltage VF		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	0.85	0.92		
Povoroa loakago gurrant		$V_R = V_R$ rated	-	0.1	10		
Reverse leakage current I <sub>R</sub>		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	15	120	μΑ	
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	20	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body - 8.0		-	nH		

## 15ETL06S, 15ETL06-1



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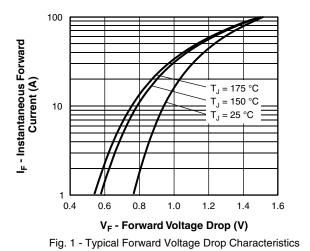
## Ultralow V<sub>F</sub> Hyperfast Rectifier for Discontinuous Mode PFC, 15 A FRED Pt<sup>TM</sup>

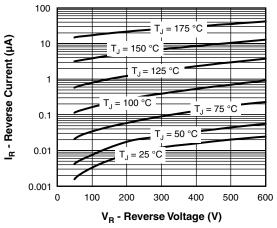
<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_c = 25 \degree C$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time		$I_F=1~A,~dI_F/dt=100~A/\mu s,~V_R=30~V$		-	60	120	
		$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	190	270	
	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	220	-	ns
		T <sub>J</sub> = 125 °C	l <sub>F</sub> = 15 A dl <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 390 V	-	320	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	19	-	A
		T <sub>J</sub> = 125 °C		-	26	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	2.2	-	μC
		T <sub>J</sub> = 125 °C		-	4.3	-	

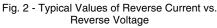
THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	1.0	1.3		
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-		
Maisht			-	2.0	-	g	
Weight			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)	
		Case style D <sup>2</sup> PAK		15ETL06S			
Marking device		Case style TO-262		15ETL06-1			



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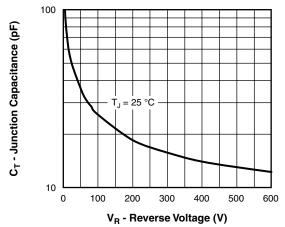


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

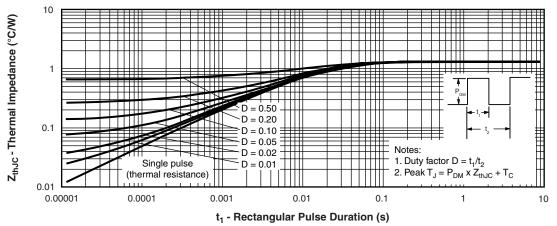
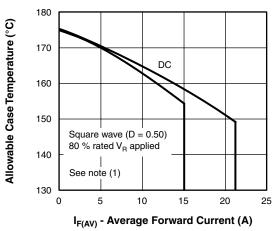


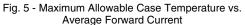
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

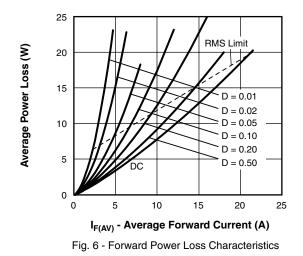
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#### Note

 $\begin{array}{l} \mbox{$^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) $x $R_{thJC}$;} \\ \mbox{$Pd = Forward power loss = I_{F(AV)} $x $V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);} \\ \mbox{$Pd_{REV} = Inverse power loss = $V_{R1} $x $I_R$ (1 - D); $I_R$ at $V_{R1} = Rated $V_R$} \end{array}$ 

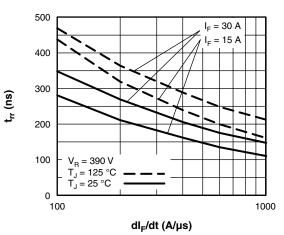


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

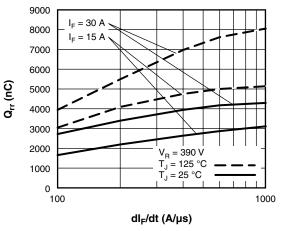


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



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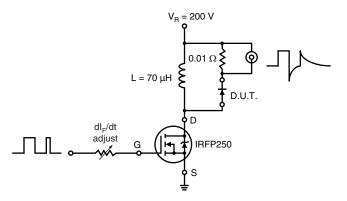


Fig. 9 - Reverse Recovery Parameter Test Circuit

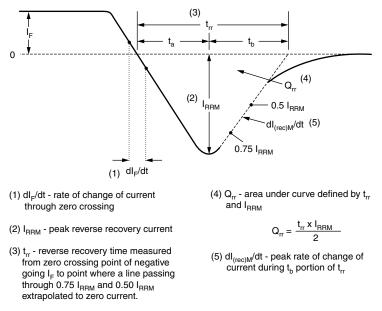


Fig. 10 - Reverse Recovery Waveform and Definitions

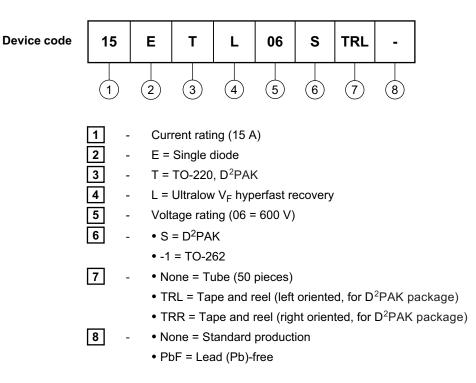
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#### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95014				
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			
SPICE model	www.vishay.com/doc?95270			



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