MSCMC120AM02CT6LIAG

Datasheet

Very Low Stray Inductance Phase Leg SiC MOSFET Power Module

Final May 2018



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1 Revision History

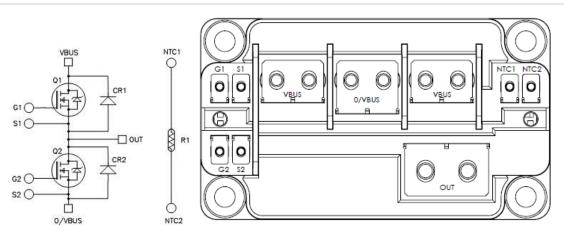
The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

1.1 Revision A

Revision A was published in May 2018. It is the first publication of this document.







2.1 Features

The following are key features of the MSCMC120AM02CT6LIAG device:

- Very low stray inductance
- Internal thermistor for temperature monitoring
- M4 and M5 power connectors
- M2.5 signals connectors
- AIN substrate for improved thermal performance

SiC Power MOSFET

- Low RDS(on)
- High temperature performance

SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature independent switching behavior
- Positive temperature coefficient on VF

2.2 Benefits

The following are benefits of the MSCMC120AM02CT6LIAG device:

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS compliant

2.3 Applications

The MSCMC120AM02CT6LIAG device is designed for the following applications:

• Motor control

*All ratings taken at $T_{J} = 25$ °C unless otherwise specified.

*Caution: the devices are sensitive to elctrostatic discharge (ESD). Proper handling procedures should be followed.



3 Electrical Specifications

This section shows the electrical specifications for the MSCMC120AM02CT6LIAG device.

3.1 Absolute Maximum Ratings

The following table shows the SiC MOSFET absolute maximum ratings (per SiC MOSFET) for the MSCMC120AM02CT6LIAG device.

Table 1 • Absolute Maximum Ratings

Symbol	Parameter		Ratings	Unit
VDSS	Drain - source voltage		1200	V
lo	Continuous drain current	Tc = 25 °C	742	А
		Tc = 80 °C	586	
ldм	Pulsed drain current		1500	
Vgs	Gate - source voltage		-10 to 23	V
Vgsop	Gate - source voltage; recommended operation values		-5 to 18	
RDS(on)	Drain - source ON resistance		2.85	mΩ
PD	Power dissipation	Tc = 25 °C	3200	W



3.2 Electrical Performance

The following tables show the SiC MOSFET characteristics (per SiC MOSFET) of the MSCMC120AM02CT6LIAG device.

Table 2 • Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
Idss	Zero gate voltage drain current	V _{GS} = 0 V, V _{Ds} = 1200 V			200	1200	μA
RDS(on)	Drain- source on resistance	V _{GS} = 20 V; I _D = 600 A	T _j = 25 °C		2.1	2.85	mΩ
		V _{GS} = 18 V; I _D = 600 A	T _j = 175 °C		4.5		-
VGS(th)	Gate threshold voltage	$V_{GS} = V_{Ds}$, $I_D = 180 \text{ mA}$		2	2.6	4	V
lgss	Gate- source leakage current	$V_{GS} = 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$				7.2	μΑ

Table 3 • Dynamic Characteristics

Symbol	Characteristic	Test conditions		Min	Тур	Max	Unit
Ciss	Input capacitance	$V_{GS} = 0 V$			33.5		- 5
Coss	Output capacitance	V _{DS} = 1000 V	V _{DS} = 1000 V f = 1 MHz		2.6		– nF
Crss	Reverse transfer capacitance	f = 1 MHz			0.18		_
Qg	Total gate charge	V _{GS} = -5 to 20 V			1932		
Qgs	Gate – source charge	- V _{Bus} = 800 V - I _D = 600 A			552		– nC
\mathbf{Q}_{gd}	Gate – drain charge					600	
Td(on)	Turn-on delay time	$V_{GS} = -5 \text{ to } 20 \text{ V}$			21		
Tr	Rise time	- V _{Bus} = 600 V -			19 50		_ ns
Td(off)	Turn-off delay time						-
Tf	Fall time	= 10 - 600 A R _L = 1 Ω ; R _G = 0.25 Ω			30		_
Eon	Turn on energy	Inductive Switching	T _i = 150 °C		8.9		mJ
Lon	run on energy	- V _{GS} = -5 to 20 V	1, 150 0		5.8		_
Eoff	Turn off energy	V _{Bus} = 600 V	T _j = 150 °C		5.0		
		I _D = 600 A					
		R _G = 0.25 Ω					
R _{Gint}	Internal gate resistance				0.6		Ω
RthJC	Junction-to-case thermal resist	ance				0.047	°C/V

Table 4 • Body Diode Ratings and Characteristics

Symbol	Characteristic	Test conditions		Min	Тур	Max	Unit
	Diode forward voltage	$V_{GS} = -5 V$	T _j = 25 °C		4		V
Vsd		I _{SD} = 300 A	T _j = 175 °C		3.5		_
trr	Reverse recovery time	$L = 600 A \cdot M_{\odot}$			45		ns
Qrr	Reverse recovery charge	- I _{SD} = 600 A ; V _{GS} = -5 V - V _R = 800 V ; di _F /dt = 12000 A/μs			4.9		μC
lrr	Reverse recovery current				162		А

device.



The following table shows the SiC diode characteristics (per SiC diode) of the MSCMC120AM02CT6LIAG

Symbol	Characteristics	Test conditions		Min	Тур	Max	Unit
Vrrm	Peak repetitive reverse voltage					1200	V
Irm	Reverse leakage current	V _R = 1200 V	Tj = 25 °C		0.6	3	mA
			T _j = 175 °C		1.8	6	_
l f	DC forward current		Tc = 100 °C		300		А
VF	Diode forward voltage	IF = 300 A	Tj = 25 °C		1.6	1.8	V
			T _j = 175 °C		2.25	2.7	-
Qc	Total capacitive charge	V _R = 800 V			1476		nC
С	Total capacitance	f = 1 MHz, V _R = 4	00 V		1380		pF
		f = 1 MHz, V _R = 8	00 V		1038		-
RthJC	Junction-to-case thermal resistance					0.091	°C/W

Table 5 • SiC Diode Charcteristics

The following tables show the thermal and package characteristics of the MSCMC120AM02CT6LIAG device.

Table 6 • Package Charcteristics

Symbol	Characteristic			Min	Max	Unit
VISOL	RMS isolation voltage, any	lation voltage, any terminal to case t =1 min, 50 to 60 Hz				V
TJ	Operating junction temperature range				175	°C
TJOP	Recommended junction te	-40	Tımax –25	_		
Tstg	Storage temperature range	-40	125	_		
Tc	Operating case temperatu	re		-40	125	_
Torque	Mounting torque	For terminals	M2.5	0.4	0.6	N.m
			M4	2	3	_
			M5	2	3.5	_
		To heatsink	M6	3	5	_
Ldc	Module stray inductance b	etween VBUS and 0/VBUS			3	nH
Wt	Package weight				320	g

Table 7 • Temperature Sensor NTC

Symbol	Characteristic	Min	Тур	Max	Unit
R25	Resistance at 25 °C		50		kΩ
ΔR25/R25			5		%
B25/85	T ₂₅ = 298.15 K		3952		К
ΔВ/В	Tc= 100 °C		4		%



Figure 1 • NTC Formula

$$R_{T} = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

T: Thermistor temperature R_T: Thermistor value at T

Note: See the APT0406 application note at www.microsemi.com.

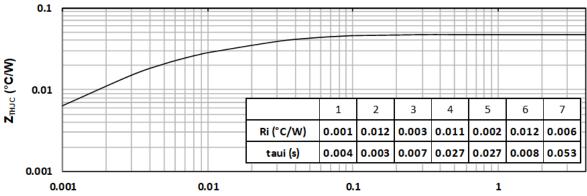


3.3 Typical Performance Curves

This section shows the typical performance curves for the MSCMC120AM02CT6LIAG device.

The following section shows the typical performance curves for SiC MOSFET.







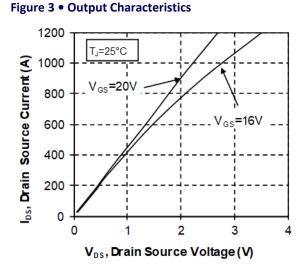


Figure 4 • Output Characteristics II

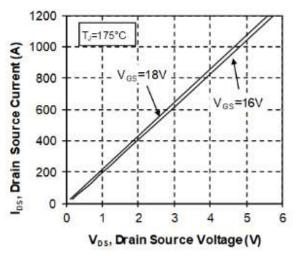
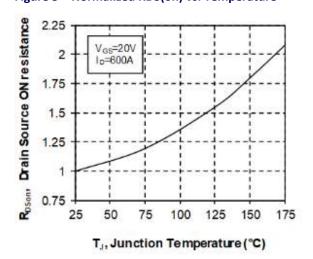
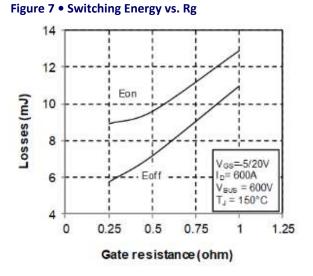




Figure 5 • Normalized RDS(on) vs. Temperature Figu









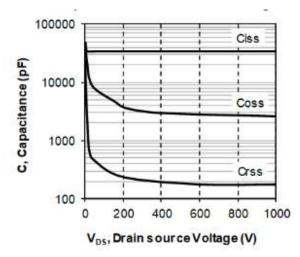
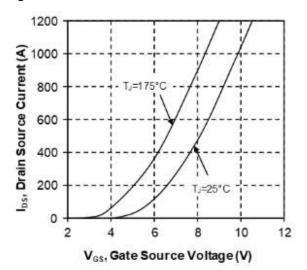
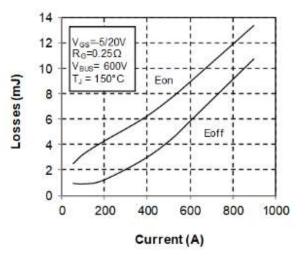


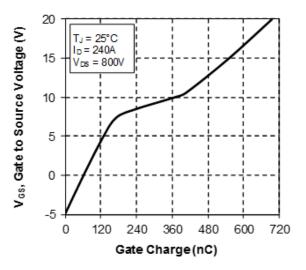
Figure 6 • Transfer Characteristics













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Figure 11 • Body Diode Characteristics

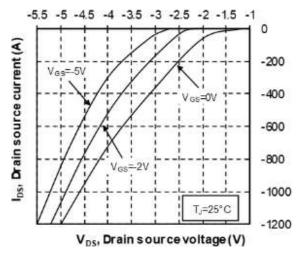
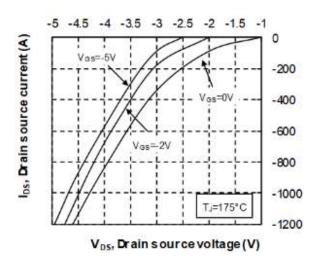


Figure 13 • Body Diode Characteristics II



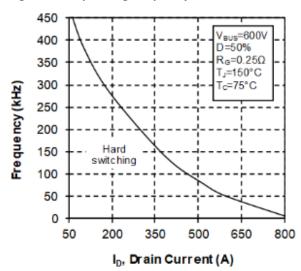


Figure 15 • Operating Frequency vs. Drain Current

Figure 12 • 3rd Quadrant Characteristics

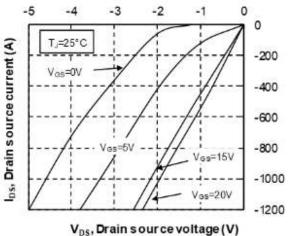
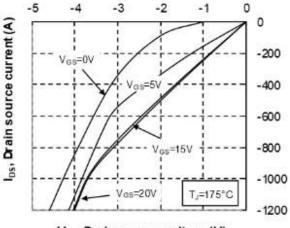




Figure 14 • 3rd Quadrant Characteristics







The following section shows the typical performance curves for SiC Diode.

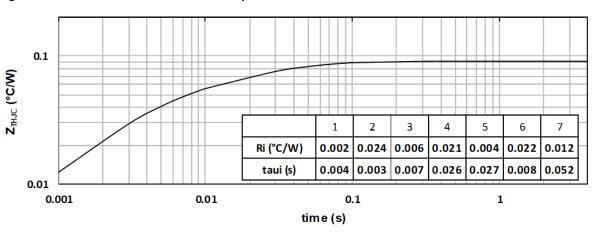


Figure 16 • SiC Diode Maximum Thermal Impedance



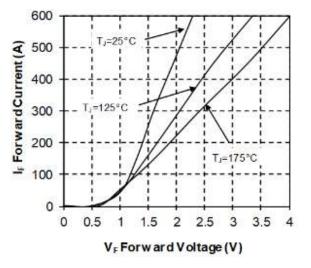


Figure 18 • Reverse Characteristics

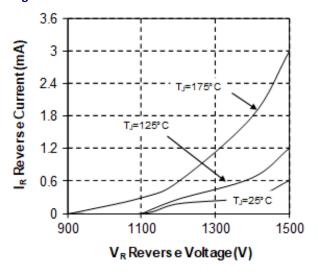
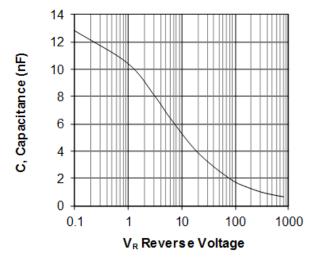




Figure 19 • Capacitance vs. Reverse Voltage





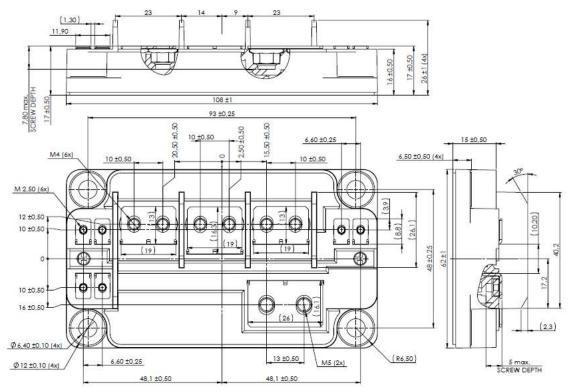
4 Package Specification

This section outlines the package specification for the MSCMC120AM02CT6LIAG device.

4.1 Package Outline Drawing

This section shows the package drawing of the MSCMC120AM02CT6LIAG device. Dimensions are in millimeters.

Figure 20 • Package Outline Drawing



Note: See the AN1911 application note at www.microsemi.com.





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