



30V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON) max}	I _{D max} T _A = +25°C
30V	10mΩ @ V _{GS} = 10V	12.0A
30 V	16mΩ @ V _{GS} = 4.5V	10.4A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

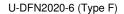
- General Purpose Interfacing Switch
- Power Management Functions

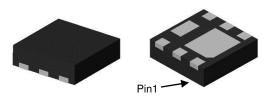
Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

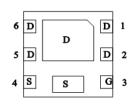
Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.0065 grams (Approximate)

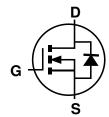




Top View Bottom View



Pin Out Bottom View



Equivalent Circuit

March 2020

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Ordering Information (Note 4)

Part Number	Marking	Reel Size (inches)	Quantity Per Reel
DMT3008LFDF-7	Т3	7	3,000
DMT3008LFDF-13	T3	13	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1

U-DFN2020-6 (Type F)



T3 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Date Code Rey												
Year	2014		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	В		Н		J	K		М	N	0	Р	R
0.0.0.0	_				•		_			•		
							_					
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2

U-DFN2020-6 (Type F)



T3 = Product Type Marking Code YWX = Date Code Marking
Y = Year (ex: 0 = 2020)
W = Week (ex: a = week 27; z represents week 52 and 53)
X = Internal Code (ex: U = Monday)

Date Code Kev

Year	2014		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	4		0	1	2	3	4	5	6	7	8	9
Week		1	-26			27	-52			5	3	
Code		P	∖-Z			а	-Z			7	7	
Internal Code	Sur	1	Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т		U		V	V	V	Х		Υ		Z



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	30	V		
Gate-Source Voltage	V_{GSS}	±20	V		
Continuous Drain Current (Note C) V 10.0V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	lo	12.0 9.5	Α
Continuous Drain Current (Note 6) V _{GS} = 10.0V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	lo	13.6 11.0	А
Continuous Dunin Courset (Nata C) V 45V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	lo	10.4 8.4	Α
Continuous Drain Current (Note 6) VGS = 4.5V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	ID	11.9 9.6	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%		I _{DM}	70	Α	
Maximum Body Diode Continuous Current	Is	2	Α		
Avalanche Current (Note 7) L = 0.1mH			las	8	Α
Avalanche Energy (Note 7) L = 0.1mH			Eas	3.2	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	T _A = +25°C	D-	0.8	W	
Total Fower Dissipation (Note 5)	$T_A = +70^{\circ}C$	PD	0.5	VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	156	°C/W	
Thermal hesistance, Junction to Ambient (Note 3)	t<10s	ПӨЈА	116	C/VV	
Total Power Dissipation (Note 6)	$T_A = +25$ °C	Б	2.1	W	
Total Fower Dissipation (Note 6)	T _A = +70°C	PD	1.3	VV	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	60.8		
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	Меја	45.0	°C/W	
Thermal Resistance, Junction to Case (Note 6)	Rejc	13			
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)		•				
Drain-Source Breakdown Voltage	BVDSS	30.0	_		٧	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	1.0	μA	$V_{DS} = 24V$, $V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	3.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
Static Drain-Source On-Resistance	-		_	10.0	mΩ	$V_{GS} = 10V, I_{D} = 9.0A$
Static Drain-Source On-Resistance	RDS(ON)	_	_	16.0	11177	$V_{GS} = 4.5V, I_D = 8.5A$
Diode Forward Voltage	V _{SD}	_	_	1.2	V	V _G S = 0V, I _S = 2A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss		886		рF	\
Output Capacitance	Coss	_	531		рF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	53	_	pF	1 = 1.000112
Gate Resistance	Rg	_	1.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$
Total Gate Charge (VGS = 10V)	Qg	_	14	_	nC	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	5.8	_	nC	\/ 10\/ I- 20A
Gate-Source Charge	Qgs	_	2.6	_	nC	$V_{DD} = 10V, I_D = 30A$
Gate-Drain Charge	Q _{gd}	_	2.5	_	nC]
Turn-On Delay Time	td(ON)	_	3.8	_	ns	
Turn-On Rise Time	t _R	_	1.7	_	ns	$V_{DD} = 10V, V_{GS} = 10V,$
Turn-Off Delay Time	t _{D(OFF)}	_	12.5	_	ns	$R_L = 0.67\Omega$, $R_G = 4.7\Omega$, $I_D = 15A$
Turn-Off Fall Time	tF	_	3.6	_	ns	150
Reverse Recovery Time	t _{RR}	_	18.4	_	ns	1 45A -II/-II 400A/
Reverse Recovery Charge	Qrr	_	7.6	_	nC	I _F = 15A, dI/dt = 100A/μs

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_{J} = +25^{\circ}C$.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.



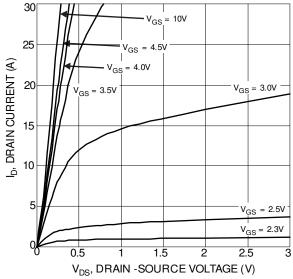
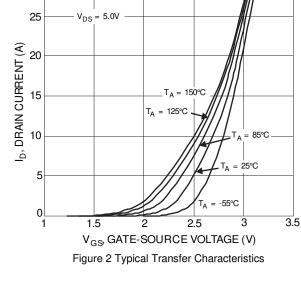
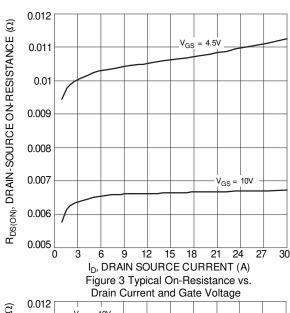
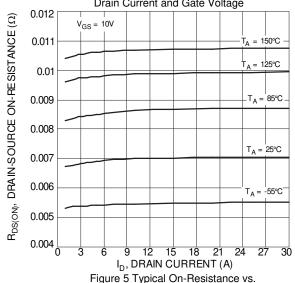


Figure 1 Typical Output Characteristics

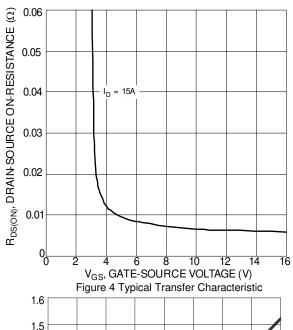


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Drain Current and Temperature



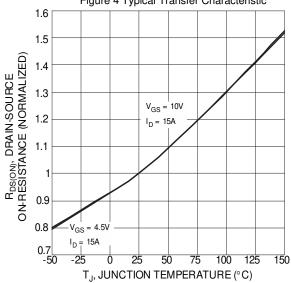


Figure 6 On-Resistance Variation with Temperature



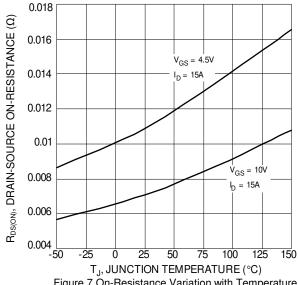


Figure 7 On-Resistance Variation with Temperature

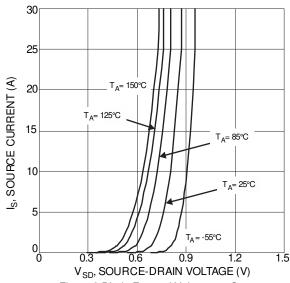
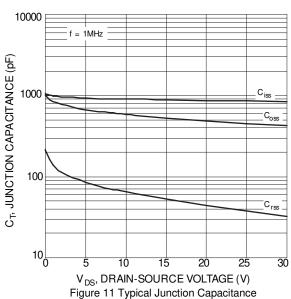


Figure 9 Diode Forward Voltage vs. Current



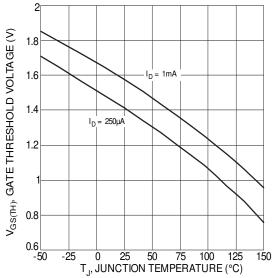


Figure 8 Gate Threshold Variation vs. Junction Temperature

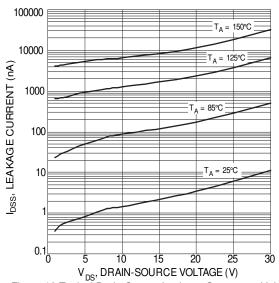
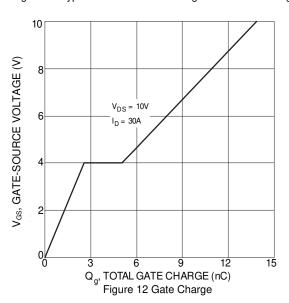
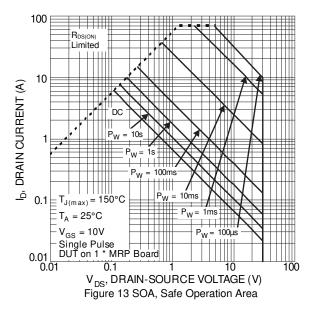


Figure 10 Typical Drain-Source Leakage Current vs. Voltage







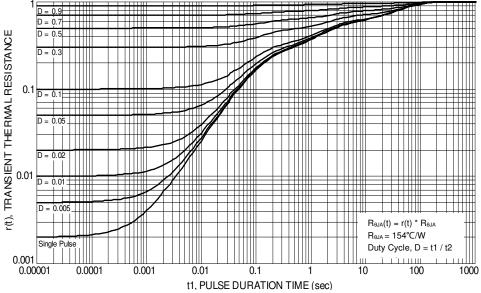


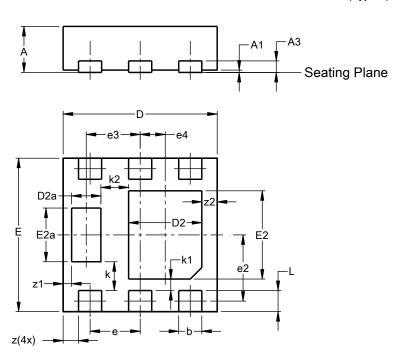
Figure 14 Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

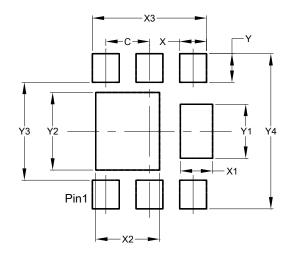


U-DFN2020-6							
		pe F)					
Dim	Min	Max	Тур				
Α	0.57	0.63	0.60				
A 1	0.00 0.05 0.03						
А3	-	0.15					
b	0.25	0.25 0.35 0.3					
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
D2a	0.33	0.43	0.38				
E	1.95	2.05	2.00				
E2	1.05	1.25	1.15				
E2a	0.65	0.75	0.70				
е		0.65 BS	С				
e2	C).863 BS	SC				
е3		0.70 BS	С				
e4	C).325 BS	SC				
k		0.37 BS	С				
k1	0.15 BSC						
k2	0.36 BSC						
L	0.225 0.325 0.275						
Z		0.20 BS	С				
z1	C).110 BS	SC				
z2		0.20 BS	С				
All C	imens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value
Dilliensions	(in mm)
С	0.650
X	0.400
X1	0.480
X2	0.950
Х3	1.700
Υ	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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