

Description



SINGLE LOW VOLTAGE RAIL-TO-RAIL OUTPUT OPERATIONAL AMPLIFIER

The AZV321 is single low voltage (2.7V to 5.5V) operational amplifier which has rail-to-rail output swing capability. The input common-mode voltage range includes ground. The chip exhibits excellent speed-power ratio, achieving 1MHz of bandwidth and 1V/ μ s of slew rate with low supply current.

The AZV321 is built with BiCMOS process. It has bipolar input and output stages for improved noise performance, low input offset and higher output current drive.

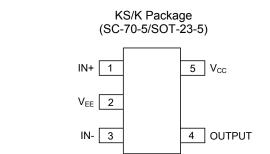
The AZV321 is available in the package of SC-70-5, which is approximately half the size of SOT-23-5. The small package saves space on pc boards, and enables the design of small portable electronic devices. It also allows the designer to place the device closer to the signal source to reduce noise pickup and increase signal integrity.

The AZV321 is also available in standard SOT-23-5 package.

Features (For $V_{\text{CC}}\text{=}5V$ and $V_{\text{EE}}\text{=}0V,$ Typical unless Otherwise Noted)

- Guaranteed 2.7V to 5.5V Performance
- No Crossover Distortion
- Gain-Bandwidth Product 1MHz
- Industrial Temperature Range: -40°C to +85°C
- Low Supply Current: 130µA
- Rail-to-Rail Output Swing under 10k Ω Load: V_{OH} up to V_{CC}-10mV V_{OL} near to V_{EE}+65mV
- V_{CM}: -0.1V to V_{CC}-0.8V

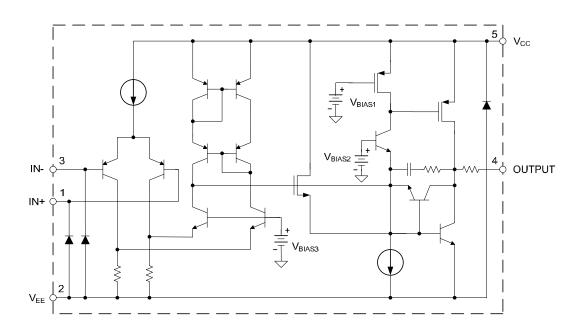
Functional Block Diagram



Applications

Pin Assignments

- Active Filters
- Low Power, Low Voltage Applications
- General Purpose Portable Devices
- Cellular Phone, Cordless Phone
- Battery-Powered Systems







Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Rating	Unit
V _{CC}	Power Supply Voltage	6	V
TJ	Operation Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-65 to 150	°C
T _{LEAD}	Lead Temperature (Soldering, 10 Seconds)	260	°C
	ESD (Machine Model)	200	V
	ESD (Human Body Model)	2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{cc}	Supply Voltage	2.7	5.5	V
T _A	Ambient Operating Temperature Range	-40	85	°C





Electrical Characteristics

AZV321-2.7V Electrical Characteristics (All limits are guaranteed for $T_A=25^{\circ}$ C, $V_{CC}=2.7$ V, $V_{EE}=0$ V, $V_{CM}=1.0$ V, $V_0=V_{CC}/2$ and $R_L>1M\Omega$, limits in **bold types** are guaranteed for $T_A=-40^{\circ}$ C to 85°C, unless otherwise specified. Note 2)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
				1.7	7	
V _{IO}	Input Offset Voltage				9	mV
	Janut Diag Current			11	250	- 1
Ι _Β	Input Bias Current				500	nA
I _{IO}	Input Offset Current			5	50	54
ιЮ					150	nA
V _{CM}	Input Common Mode Voltage Range	for CMRR≥50dB	-0.1		1.9	V
1	Supply Current	$V_0=V_{CC}/2$, $A_{VCL}=1$, no load		80	170	
lcc					270	μA
CMRR	Common Mode Rejection Ratio	0≤V _{CM} ≤1.7V	50	65		dB
PSRR	Power Supply Rejection Ratio	2.7V≤V _{CC} ≤5V, V _O =1V	50	60		dB
I _{SOURCE}	Output Chart Circuit Current	V _O =0V	5	20		mA
I _{SINK}	Output Short Circuit Current	V ₀ =2.7V	10	30		mA
V _{OH}		D = 10kO = 1.25V	2.60	2.69		V
V _{OL}	 Output Voltage Swing 	$R_L=10k\Omega$ to 1.35V		60	180	mV
GBWP	Gain Bandwidth Product	C _L =200pF		1		MHz
фм	Phase Margin			60		Deg
Gм	Gain Margin			10		dB

Note 2: Limits over the full temperature are guaranteed by design, but not tested in production.





Electrical Characteristics (Cont.)

AZV321-5V Electrical Characteristics (All limits are guaranteed for $T_A=25^{\circ}$ C, $V_{CC}=5V$, $V_{EE}=0V$, $V_{CM}=2.0V$, $V_0=V_{CC}/2$ and $R_L>1M\Omega$, limits in **bold types** are guaranteed for $T_A=-40^{\circ}$ C to 85°C, unless otherwise specified. Note 2)

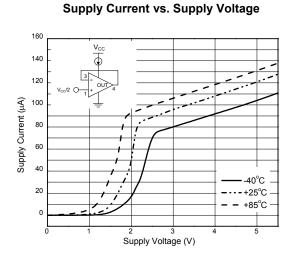
Symbol	Parameter	Conditions	Min	Min Typ	Max	Unit
				1.7	7	
V _{IO}	Input Offset Voltage				9	mV
				11	250	nA
Ι _Β	Input Bias Current				500	
l.	Input Offset Current			5	50	nA
I _{IO}					150	
V_{CM}	Input Common Mode Voltage Range	for CMRR≥50dB	-0.1	-0.1		V
Icc	Supply Current	$V_0=V_{CC}/2$, $A_{VCL}=1$, no load		130	250	
ICC		V0-VC0/2, AVCL-1, 110 10au			350	μA
Gv	Large Signal Voltage Gain	$R_L=2k\Omega$	84	100		- dB
	Large Signal Voltage Gall		80			
CMRR	Common Mode Rejection Ratio	0≤V _{CM} ≤4V	50	65		dB
PSRR	Power Supply Rejection Ratio	2.7V≤V _{CC} ≤5V, V _O =1V, V _{CM} =1V	50	60		dB
ISOURCE	Output Short Circuit Current	V _O =0V	5	60		mA
I _{SINK}	 Output Short Circuit Current 	V _O =5V	10	160		mA
		$R_L=2k\Omega$ to 2.5V	4.7	4.96		- V
V			4.6			
V _{OH}		R_L =10k Ω to 2.5V	4.9	4.99		
	 Output Voltage Swing 		4.8			
	Output voltage Swing	$R_L=2k\Omega$ to 2.5V		120	300	
V _{OL}					400	- mV
VOL		$P_{1} = 10kO$ to 2.5\/		65	180	
		$R_L=10k\Omega$ to 2.5V			280	
SR	Slew Rate			1		V/µS
GBWP	Gain Bandwidth Product	C _L =200pF		1		MHz
фм	Phase Margin			60		Deg
G _M	Gain Margin			10		dB

Note 2: Limits over the full temperature are guaranteed by design, but not tested in production.

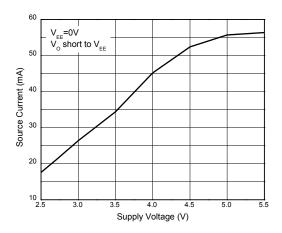




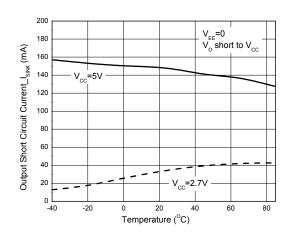
Performance Characteristics



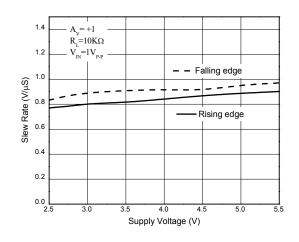
Output Source Current vs. Supply Voltage



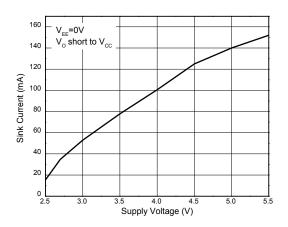
Short Circuit Current_ I_{SINK} vs. Temperature



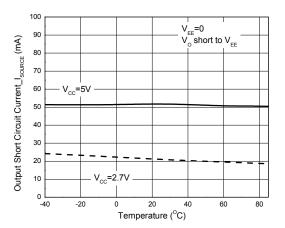
Slew Rate vs. Supply Voltage



Output Sink Current vs. Supply Voltage



Short Circuit Current_I_{SOURCE} vs. Temperature

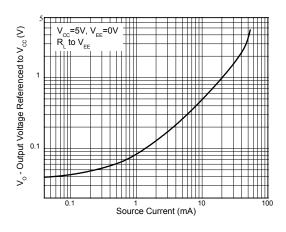




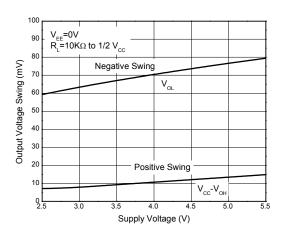


Performance Characteristics (Cont.)

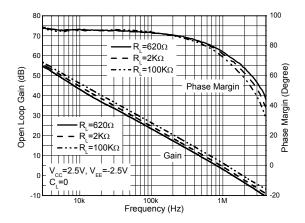
Output Voltage vs. Source Current



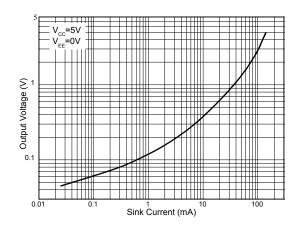
Output Voltage Swing vs. Supply Voltage



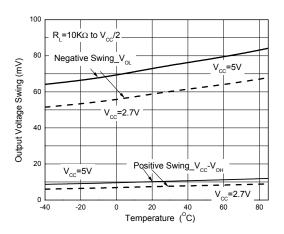
Gain and Phase vs. Frequency and Resistive Load



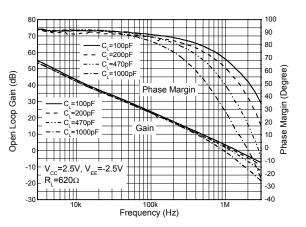
Output Voltage vs. Sink Current



Output Voltage Swing vs. Temperature



Gain and Phase vs. Frequency and Capacitive Load

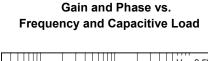


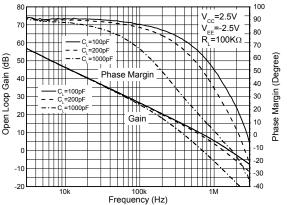


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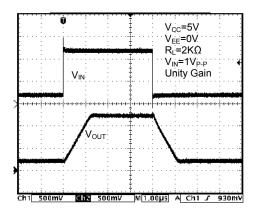


Performance Characteristics (Cont.)

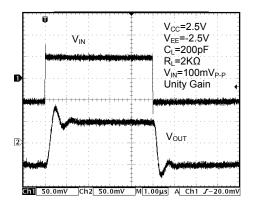




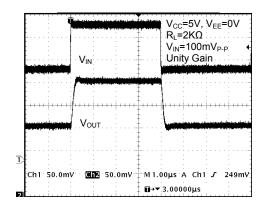
Non-Inverting Input Large Signal **Pulse Response**



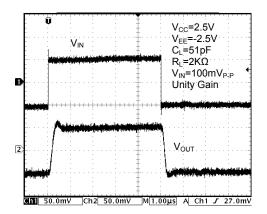
Output with Excessive Capacitive Load



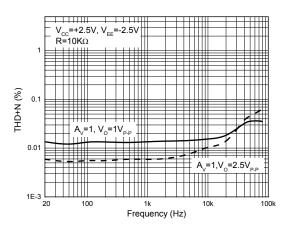
Non-Inverting Input Small Signal Pulse Response



Output with Excessive Capacitive Load



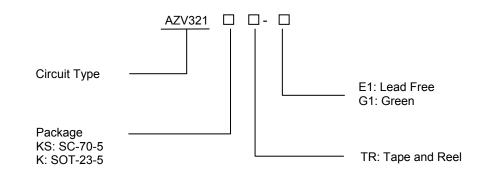
THD+N vs. Frequency







Ordering Information



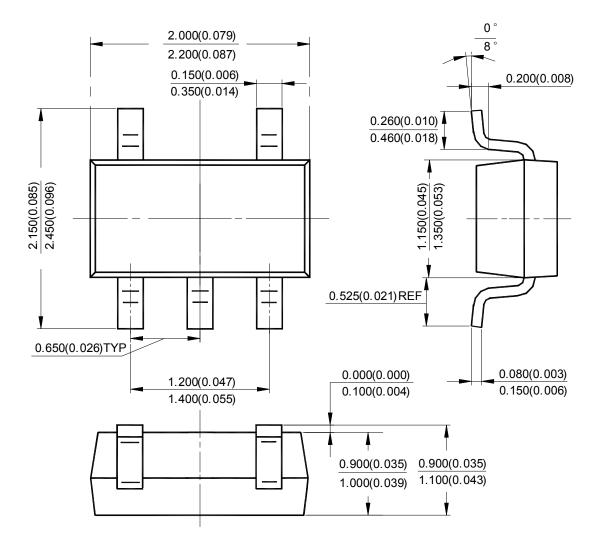
Deskare Temperature		Part Number		Marking ID		Decking Turc	
Package	Range	Lead Free	Green	Lead Free	Green	Packing Type	
SC-70-5	-40 to 85°C	AZV321KSTR-E1	AZV321KSTR-G1	21	B1	Tape & Reel	
SOT-23-5		AZV321KTR-E1	AZV321KTR-G1	E6D	G6D	Tape & Reel	

BCD Semiconductor's Pb-free products, as designated with "E1" suffix in the part number, are RoHS compliant. Products with "G1" suffix are available in green packages.





Package Outline Dimensions (All dimensions in mm(inch).)

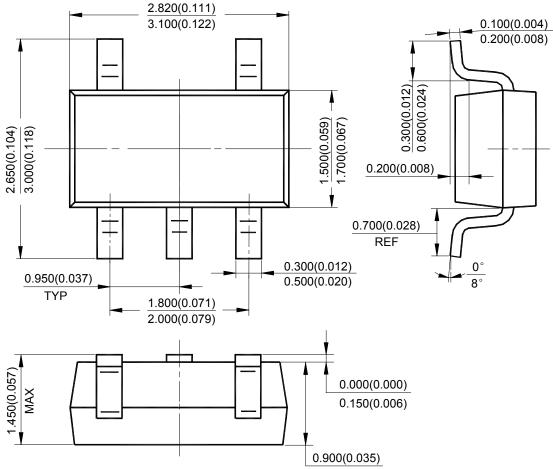


SC-70-5





Package Outline Dimensions (Cont. All dimensions in mm(inch).)



SOT-23-5

1.300(0.051)

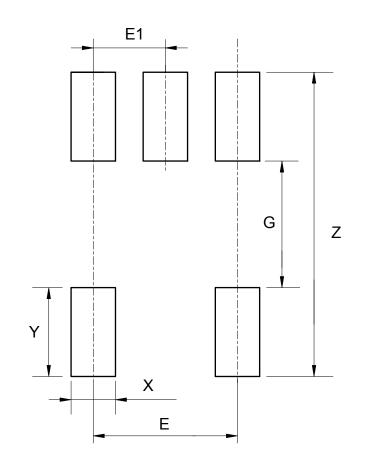
AZV321 Document number: DS36528 Rev. 2 - 0





Suggested Pad Layout





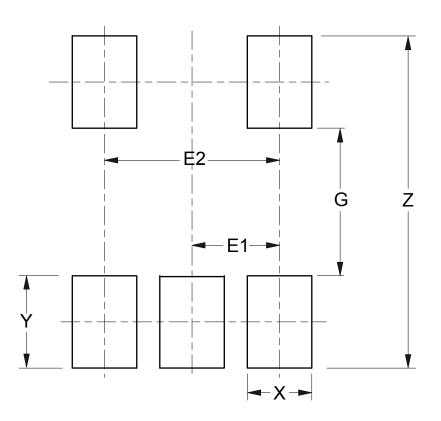
Dimonsions	Ζ	G	Х	Y	Е	E1
Dimensions	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	2.740/0.108	1.140/0.045	0.400/0.016	0.800/0.031	1.300/0.051	0.650/0.026





Suggested Pad Layout (Cont.)





Dimonsions	Ζ	G	Х	Y	E1	E2
Dimensions	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	3.600/0.142	1.600/0.063	0.700/0.028	1.000/0.039	0.950/0.037	1.900/0.075





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