SINGLE/DUAL CHANNEL INTELLIGENT POWER LOW SIDE SWITCH

Features

- Over temperature shutdown
- Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current
- Lead free and RoHS compliant

Product Summary

Rds(on)	250mΩ (max.)
Vclamp	39V
Ishutdown	2.8A (typ.)

Packages

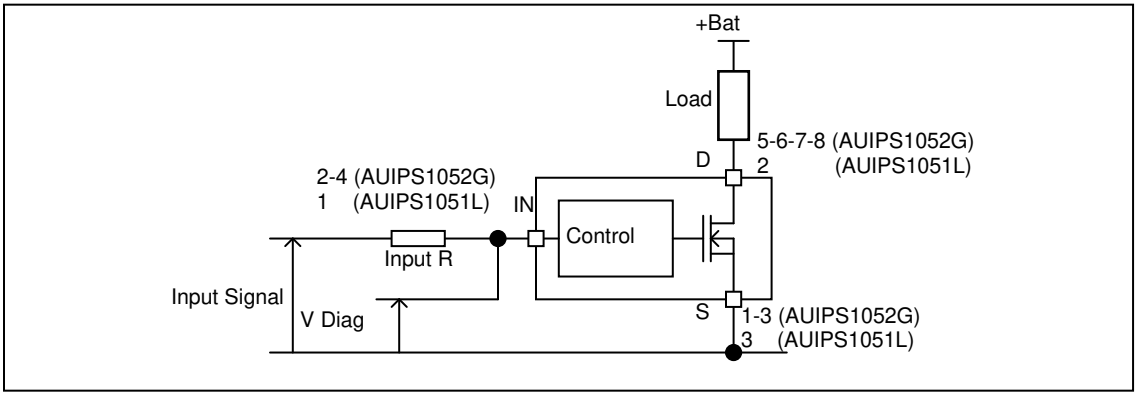
SOT-223
AUIPS1051L

SO-8
AUIPS1052G

Description

The AUS1051L and AUIPS1052G are Intelligent Power Switches (IPS) featuring low side MOSFETs with over-current, over-temperature, ESD protection and drain to source active clamp. The AUIPS1052G is a dual channel device while the AUIPS1051 is a single channel. These devices offer protections and the high reliability required in harsh environments. Each switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 3A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

Typical Connection



Qualification Information†

Qualification Level		Automotive (per AEC-Q100††)	
		Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
Moisture Sensitivity Level		SOT223-3L	MSL2, 260°C (per IPC/JEDEC J-STD-020)
		8L-SOICN	MSL2, 260°C (per IPC/JEDEC J-STD-020)
ESD	Machine Model	Class M4 (+/-450V) (per AEC-Q-100-003)	
	Human Body Model	Class H3A (+/-4500V) (per AEC-Q100-002)	
	Charged Device Model	Class C4 (+/-1000V) (per AEC-Q100-011)	
IC Latch-Up Test		Class II, Level A (per AEC-Q100-004)	
RoHS Compliant		Yes	

† Qualification standards can be found at International Rectifier's web site http://www.irf.com/
†† Exceptions to AEC-Q100 requirements are noted in the qualification report.

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (Tj= -40°C..150°C, Vcc=6..36V unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vds	Maximum drain to source voltage	-0.3	36	V
Vds cont.	Maximum continuous drain to source voltage	-	28	V
Vin	Maximum input voltage	-0.3	6	V
Isd cont.	Max diode continuous current (limited by thermal dissipation)	—	1.3	A
Pd	Maximum power dissipation (internally limited by thermal protection) Rth=60°C/W AUIPS1051L 1" sqrt. Footprint		2	W
	Rth=100°C/W AUIPS1052G std. footprint		1.25	
Tj max.	Maximum operating junction temperature	-40	150	°C
	Maximum storage temperature	-55	150	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
Rth1	Thermal resistance junction to ambient AUIPS1051L SOT-223 std. footprint	100	—	°C/W
Rth2	Thermal resistance junction to ambient AUIPS1051L SOT-223 1" sqrt. Footprint	60	—	
Rth1	Thermal resistance junction to ambient AUIPS1052G SO-8 std. Footprint 1 die active	100	—	
Rth1	Thermal resistance junction to ambient AUIPS1052G SO-8 std. footprint 2 die active	130	—	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
VIH	High level input voltage	4.5	5.5	V
VIL	Low level input voltage	0	0.5	
Ids	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V Rth=60°C/W AUIPS1051L 1" sqrt. Footprint	—	1.4	A
	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V Rth=100°C/W AUIPS1052G 1" sqrt. Footprint - 1 die active	—	1.1	A
	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V Rth=130°C/W AUIPS1052G 1" sqrt. Footprint - 2 die active		0.5	A
Rin	Recommended resistor in series with IN pin to generate a diagnostic	0.5	10	kΩ
Max L	Max. recommended load inductance (including line inductance)(1)	—	30	µH
Max. F	Max. frequency	—	10	kHz
Max. t rise	Max. input rise time	—	1	µs

(1) Higher inductance is possible if maximum load current is limited - see figure 11

Static Electrical Characteristics

Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Rds(on)	ON state resistance Tj=25°C	—	160	250	mΩ	Vin=5V, Ids=1A
	ON state resistance Tj=150°C	—	340	450		
Idss1	Drain to source leakage current	—	0.1	2	µA	Vcc=14V, Tj=25°C
Idss2	Drain to source leakage current	—	0.2	4		Vcc=28V, Tj=25°C
V clamp1	Drain to source clamp voltage 1	36	38	—	V	Id=20mA
V clamp2	Drain to source clamp voltage 2	—	39	42		Id=0.5A
Vin clamp	IN to source pin clamp voltage	5.5	6.5	7.5		Iin=1mA
Vth	Input threshold voltage	—	1.7	—		Id=10mA

Switching Electrical Characteristics

Vcc=14V, Resistive load=10Ω, Rinput=50Ω, Vin=5V, Tj=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Tdon	Turn-on delay time to 20%	1	3	10	µs	See figure 2
Tr	Rise time 20% to 80%	1	3	10		
Tdoff	Turn-off delay time to 80%	3	15	40		
Tf	Fall time 80% to 20%	2	4	10		
Eon + Eoff	Turn on and off energy	—	0.1	—	mJ	

Protection Characteristics

Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Tsd	Over temperature threshold	150(2)	165	—	°C	See figure 1
Isd	Over current threshold	1.9	2.8	3.8	A	See figure 1
OV	Over voltage protection (not active when the device is ON)	34	37	—	V	
Vreset	IN protection reset threshold	—	1.7	—	V	
Treset	Time to reset protection	15(2)	50	200	µs	Vin=0V, Tj=25°C

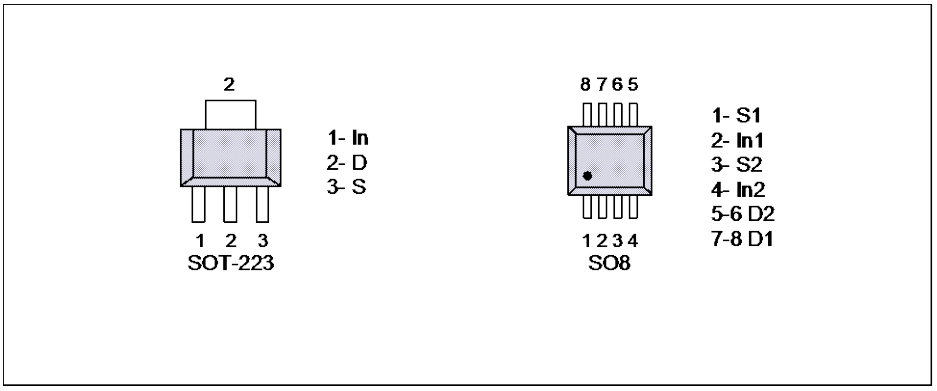
(2)Guaranteed by design

Diagnostic

Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

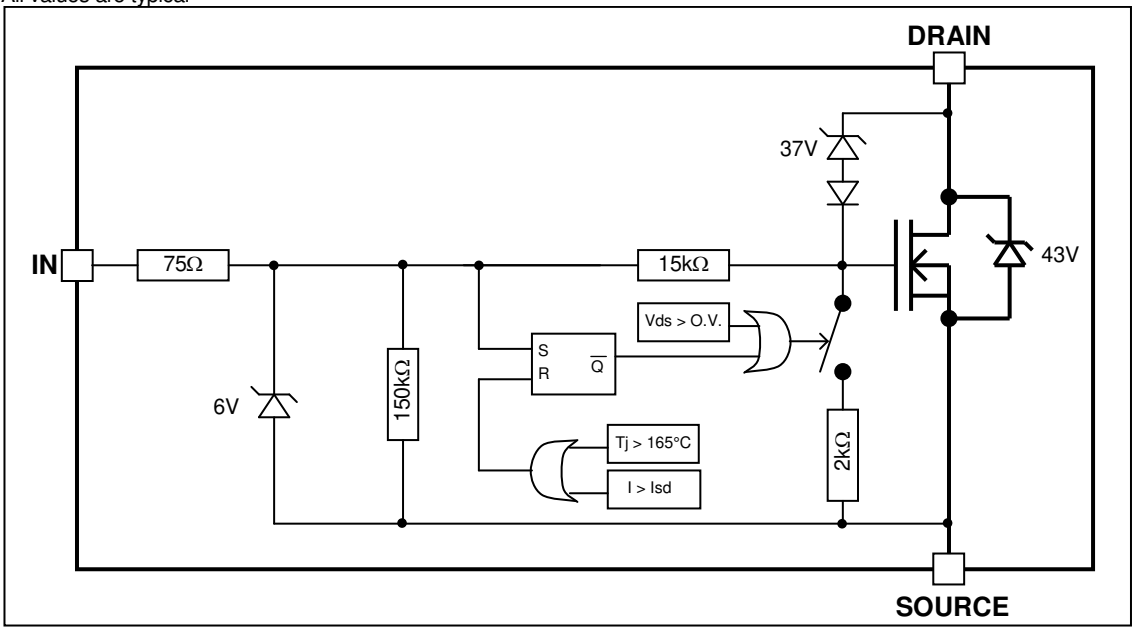
Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Iin, on	ON state IN positive current	10	32	80	µA	Vin=5V
Iin, off	OFF state IN positive current (after protection latched – fault condition)	120	230	350		

Lead Assignments



Functional Block Diagram

All values are typical



All curves are typical values. Operating in the shaded area is not recommended.

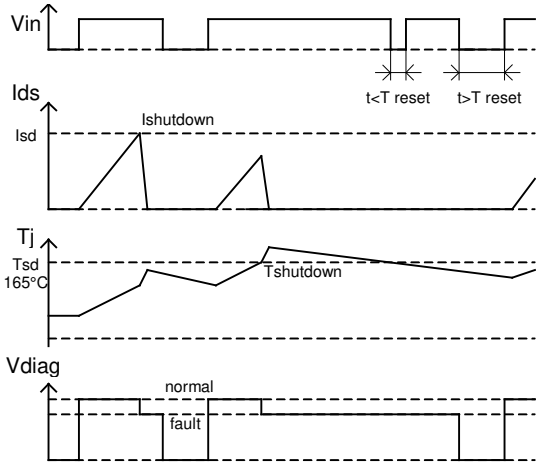


Figure 1 – Timing diagram

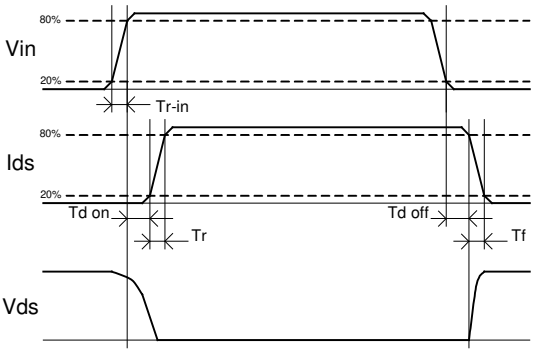
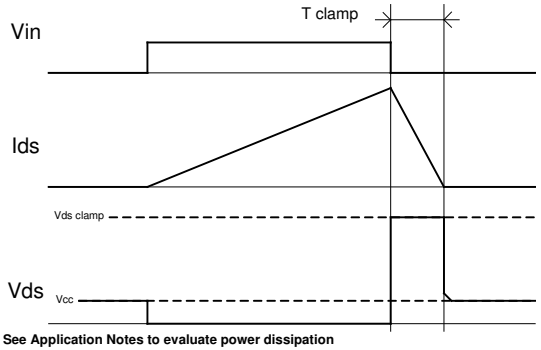


Figure 2 – IN rise time & switching definitions



See Application Notes to evaluate power dissipation

Figure 3 – Active clamp waveforms

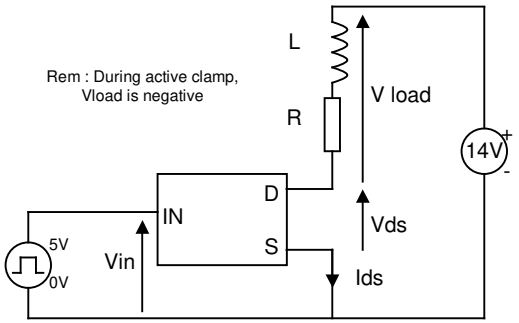


Figure 4 – Active clamp test circuit

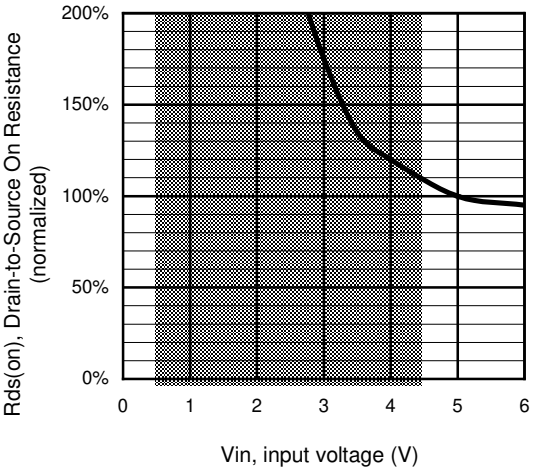


Figure 5 – Normalized Rds(on) (%) Vs Input voltage (V)

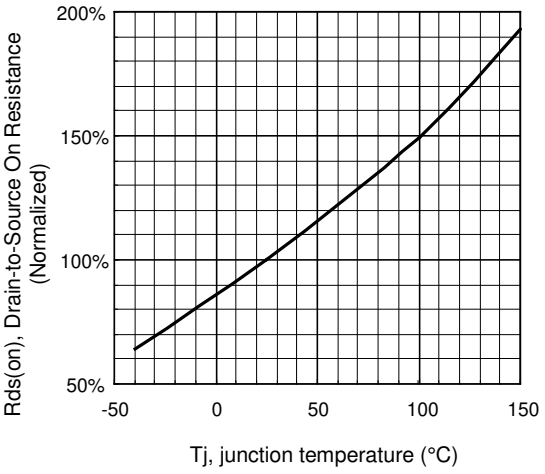


Figure 6 - Normalized Rds(on) (%) Vs Tj (°C)

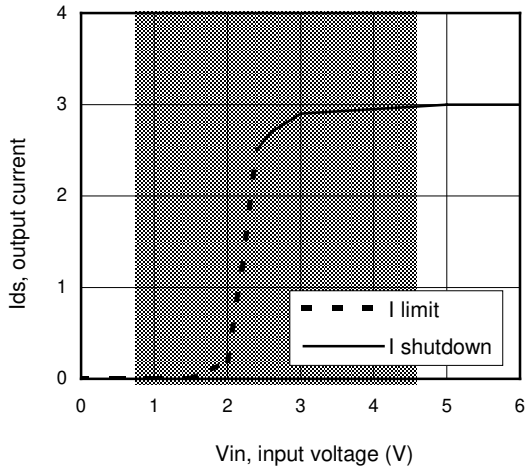


Figure 7 – Current limitation and current shutdown Vs Input voltage (V)

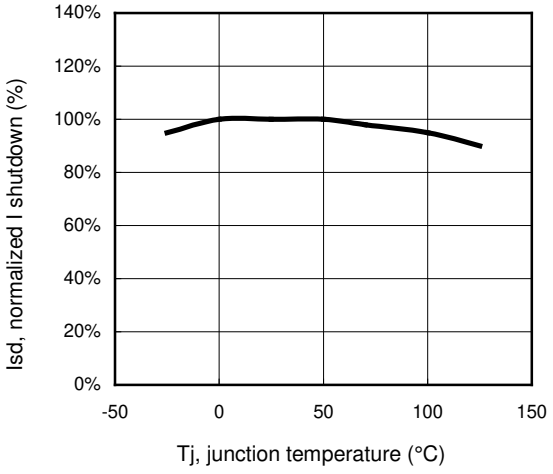


Figure 8 – Normalized I shutdown (%) Vs junction temperature (°C)

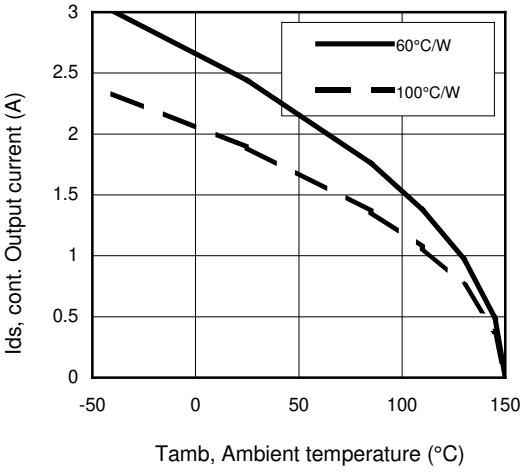


Figure 9 – Max. continuous output current (A) Vs Ambient temperature (°C)

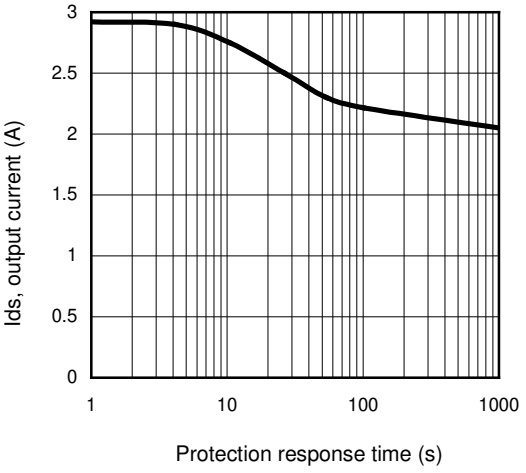


Figure 10 – Ids (A) Vs over temperature protection response time (s) / IPS1051L

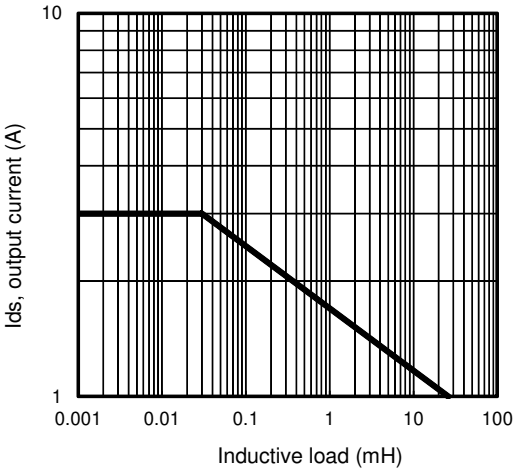


Figure 11 – Max. ouput current (A) Vs Inductive load (mH)

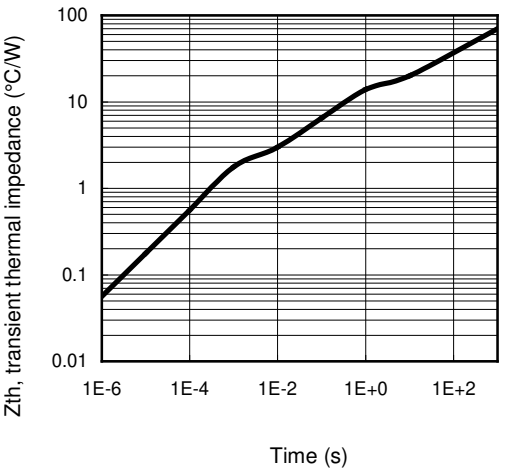


Figure 12 – Transient thermal impedance (°C/W) Vs time (s)

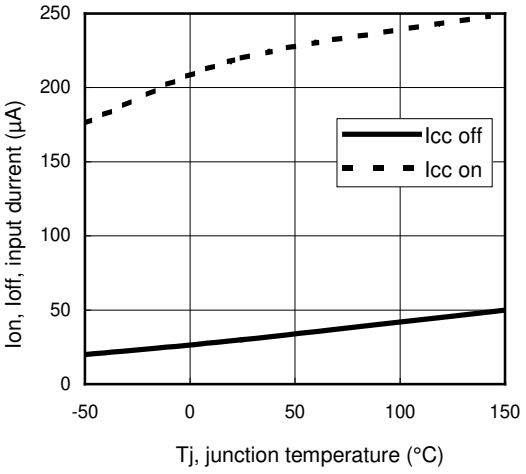


Figure 13 – Input current (µA) On and Off Vs junction temperature (°C)

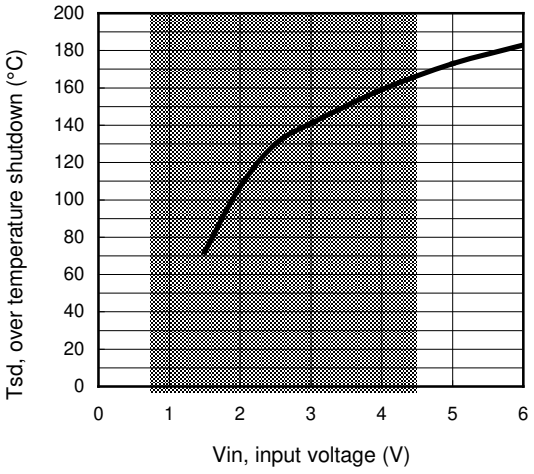
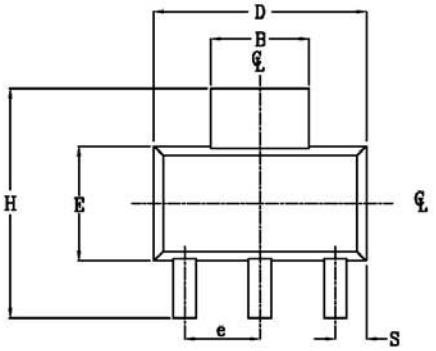


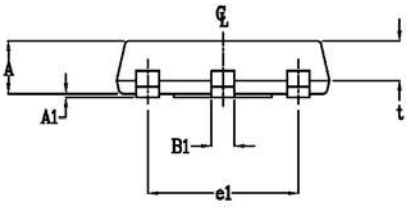
Figure 14 – Over temperature shutdown (°C) Vs input voltage (V)

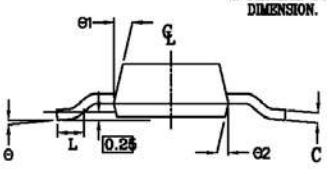
Case Outline - SOT-223 - Automotive Q100 PbF MSL2 qualified



POS	MILLIMETERS		INCHES	
1	MAX	MIN	MAX	MIN
A	1.70	1.50	.067	.060
A1	0.10	0.02	.004	.0008
B	3.15	2.95	.124	.116
B1	0.85	0.65	.033	.026
C	0.35	0.25	.014	.010
D	6.70	6.30	.264	.248
e	2.30 NOM		.0905 NOM	
e1	4.60 NOM		.181 NOM	
E	3.70	3.30	.146	.130
H	7.30	6.70	.287	.264
S	1.05	0.85	.041	.033
t	1.30	1.10	.051	.043
Θ	10° MAX		10° MAX	
Θ1	16°	10°	16°	10°
Θ2	16°	10°	16°	10°
L	0.75 MIN		0.0295 MIN	

NOTE:
1. PACKAGE OUTLINE EXCLUSIVE OF ANY MOLD FLASHES DIMENSION.
2. PACKAGE OUTLINE EXCLUSIVE OF BURR DIMENSION.

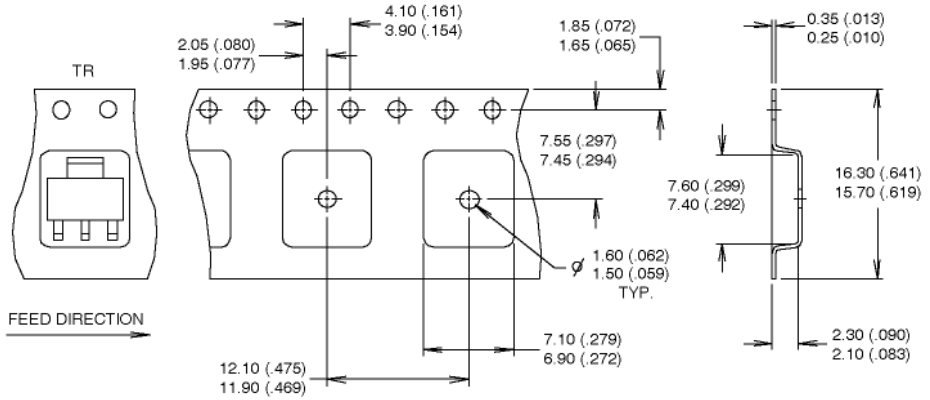




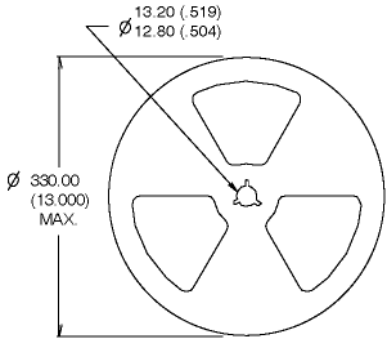
Leads and drain are plated with 100% Sn

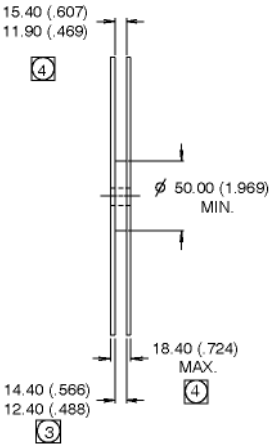
Tape & Reel - SOT-223

Dimensions are shown in milimeters (inches)



NOTES :
1. CONTROLLING DIMENSION: MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.
3. EACH Ø330.00 (13.00) REEL CONTAINS 2,500 DEVICES.

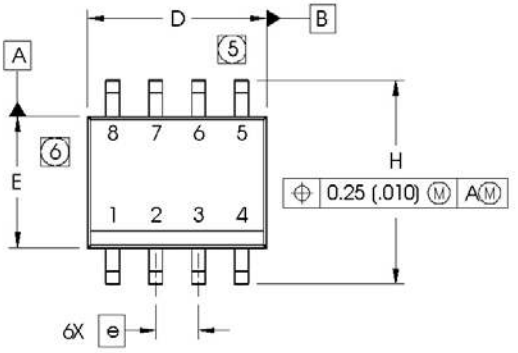




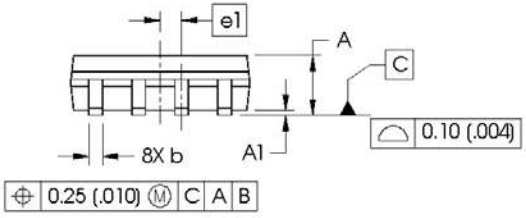
NOTES :
1. OUTLINE COMFORMS TO EIA-418-1.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION MEASURED @ HUB.
4. INCLUDES FLANGE DISTORTION @ OUTER EDGE.

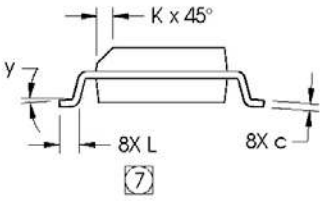
Case Outline - SO-8 - Automotive Q100 PbF MSL2 qualified

Dimensions are shown in millimeters (inches)



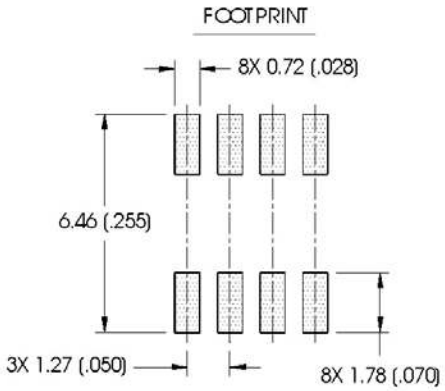
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°





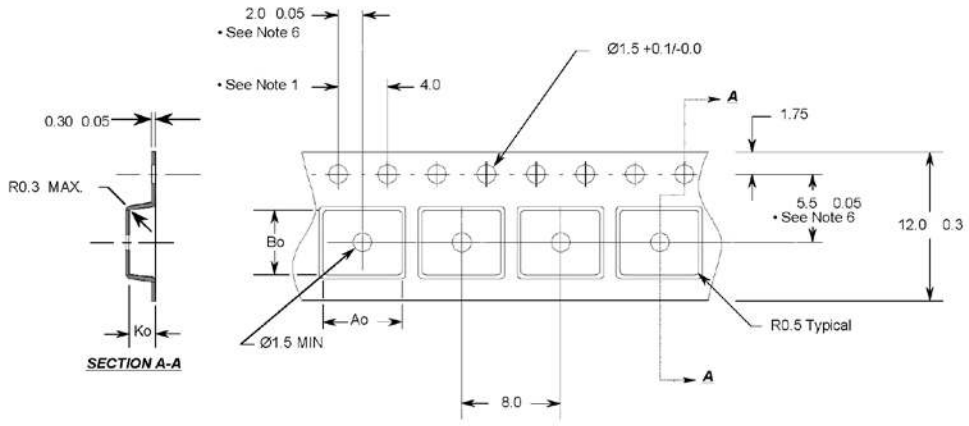
NOTES:

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
5. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
6. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
7. DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.



Leads and drain are plated with 100% Sn

Tape & Reel - SO-8

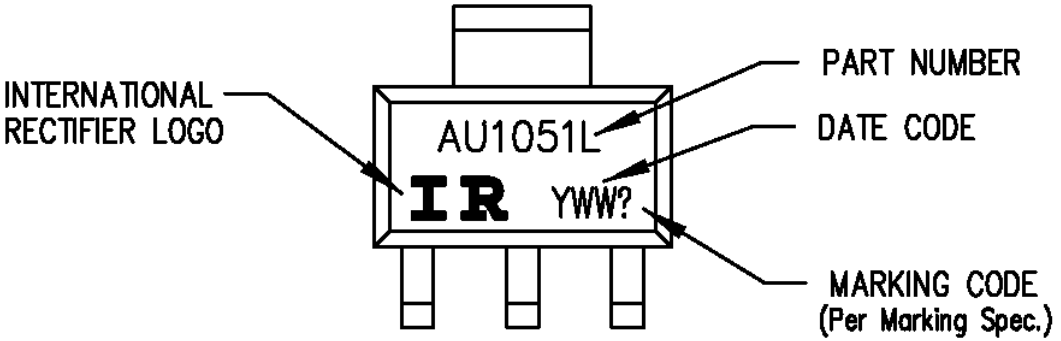


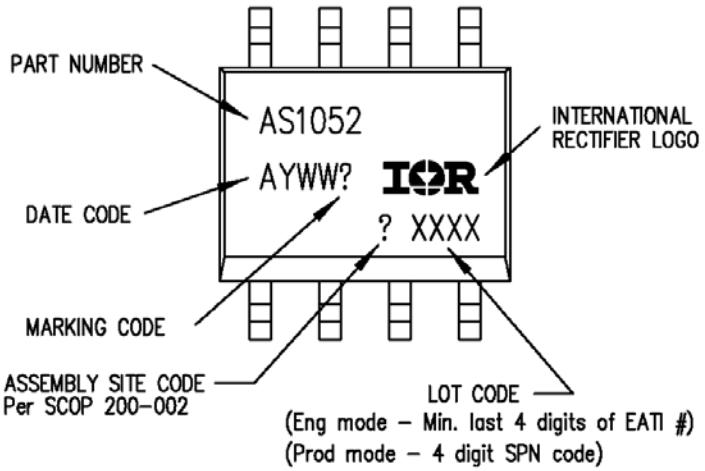
Notes:
1. 10 sprocket hole pitch cumulative tolerance 0.2
2. Camber not to exceed 1mm in 100mm
3. Material: Black Conductive Advantek Polystyrene
4. Ao and Bo measured on a plane 0.3mm above the bottom of the pocket
5. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Ao = 6.4 mm
Bo = 5.2 mm
Ko = 2.1 mm

- All Dimensions in Millimeters -

Part Marking Information





Ordering Information

Base Part Number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIPS1051	SOIC-8	Tube	95	AUIPS1052G
		Tape and reel	2500	AUIPS1052GTR
AUIPS1051	SOT-223	Tube	80	AUIPS1051L
		Tape and reel	2500	AUIPS1051LTR

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Revision History

Revision	Date	Notes/Changes
C1	November, 24th, 2010	AU release
C2	December, 7th 2010	ESD section removed page 3
C3	February, 28th 2011	Update Max rating voltage
C4	March, 14th 2011	Update Part Marking
C5	March, 17th 2011	Update ESD level and Lead free/RoHS compliant
D	November, 14th, 2011	Update T&R SOT223
E	January, 11th 2012	Update fig. 11
F	May 9th, 2012	Update the component number of the SOT223 tube
G	June, 21st 2012	Update storage temperature, Figure 9
H	April, 30th 2013	Correct the functional block diagram page 5

