# **Dual General Purpose Transistors**

# **NPN Duals**

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-563 which is designed for low power surface mount applications.

#### **Features**

• These are Pb-Free Devices

#### **MAXIMUM RATINGS**

Rating	Symbol	BC847	BC848	Unit
Collector - Emitter Voltage	V <sub>CEO</sub>	45	30	V
Collector - Base Voltage	V <sub>CBO</sub>	50	30	V
Emitter – Base Voltage	V <sub>EBO</sub>	6.0	5.0	V
Collector Current – Continuous	Ι <sub>C</sub>	100	100	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### THERMAL CHARACTERISTICS

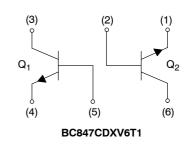
Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation, (Note 1) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	357 2.9	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\thetaJA}$	350	°C/W
Characteristic (Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation, (Note 1) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	P <sub>D</sub>	500 4.0	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	250	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

1. FR-4 @ Minimum Pad



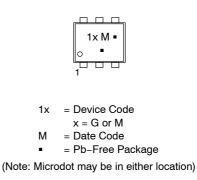
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#### **MARKING DIAGRAMS**



#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.



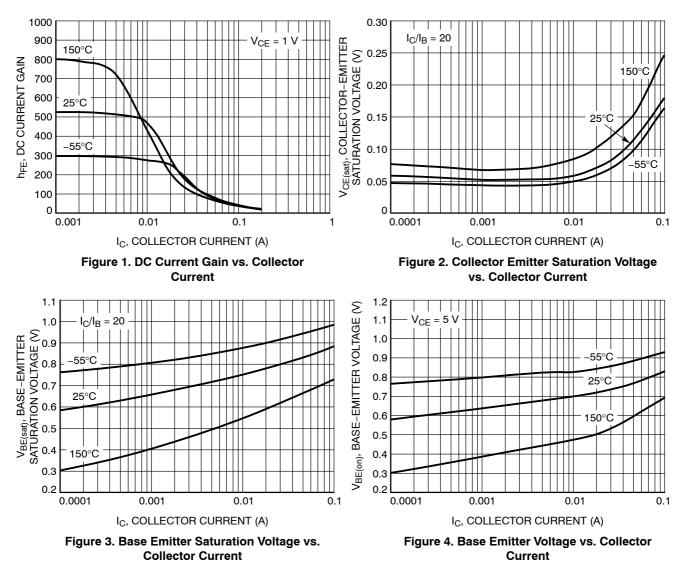
Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS		-	•	•		
(0)	C847CDXV6T1 C848CDXV6T1	V <sub>(BR)CEO</sub>	45 30			V
	C847CDXV6T1 C848CDXV6T1	V <sub>(BR)CES</sub>	50 30		-	V
	C847CDXV6T1 C848CDXV6T1	V <sub>(BR)CBO</sub>	50 30			V
	C847CDXV6T1 C848CDXV6T1	V <sub>(BR)EBO</sub>	6.0 5.0			V
Collector Cutoff Current (V_{CB} = 30 V) (V_{CB} = 30 V, T_A = 150^{\circ}C)		I <sub>CBO</sub>			15 5.0	nA μA
ON CHARACTERISTICS						
DC Current Gain (I <sub>C</sub> = 10 $\mu$ A, V <sub>CE</sub> = 5.0 V) (I <sub>C</sub> = 2.0 mA, V <sub>CE</sub> = 5.0 V)		h <sub>FE</sub>	_ 420	270 520	_ 800	-
Collector – Emitter Saturation Voltage (I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0.5 m/ (I <sub>C</sub> = 100 mA, I <sub>B</sub> = 5.0 m	V <sub>CE(sat)</sub>			0.25 0.6	V	
Base – Emitter Saturation Voltage (I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0.5 mA) (I <sub>C</sub> = 100 mA, I <sub>B</sub> = 5.0 mA)	V <sub>BE(sat)</sub>	-	0.7 0.9	-	V	
Base – Emitter Voltage (I <sub>C</sub> = 2.0 mA, V <sub>CE</sub> = 5.0 V) (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 V)	V <sub>BE(on)</sub>	580 -	660 -	700 770	mV	
SMALL-SIGNAL CHARACTERISTICS						
Current – Gain – Bandwidth Product ( $I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz}$ )		f <sub>T</sub>	100	-	-	MHz
Output Capacitance (V <sub>CB</sub> = 10 V, f = 1.0 MHz)	C <sub>obo</sub>	-	-	4.5	pF	
Noise Figure (I <sub>C</sub> = 0.2 mA, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 2.0 k $\Omega$ ,f = 1.0 kHz, BW =	NF	_	_	10	dB	

#### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

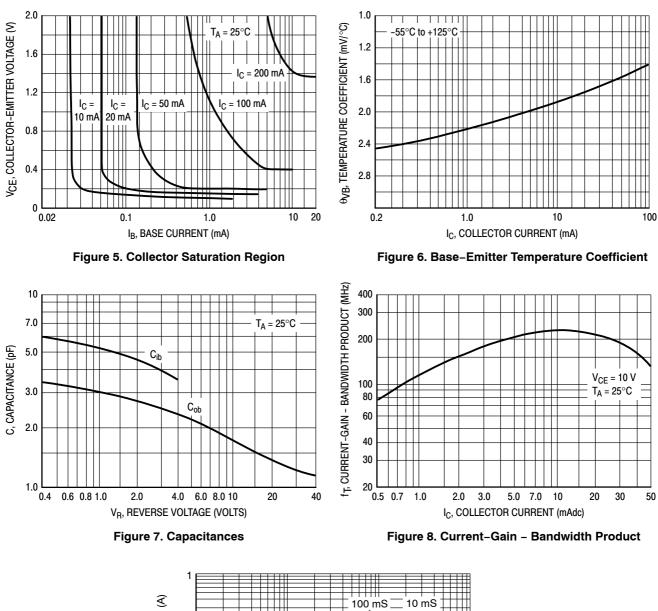
#### **ORDERING INFORMATION**

Device	Specific Marking	Package	Shipping <sup>†</sup>
BC847CDXV6T1		SOT-563	4000 Units / Tape & Reel
BC847CDXV6T1G		SOT-563 (Pb-Free)	4000 Units / Tape & Reel
BC847CDXV6T5		SOT-563	8000 Units / Tape & Reel
BC847CDXV6T5G		SOT–563 (Pb–Free)	8000 Units / Tape & Reel
BC848CDXV6T1		SOT-563	4000 Units / Tape & Reel
BC848CDXV6T1G	1L	SOT–563 (Pb–Free)	4000 Units / Tape & Reel
BC848CDXV6T5		SOT-563	8000 Units / Tape & Reel
BC848CDXV6T5G		SOT-563 (Pb-Free)	8000 Units / Tape & Reel

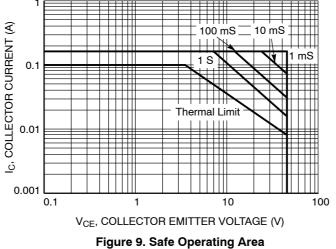
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



## **TYPICAL CHARACTERISTICS**

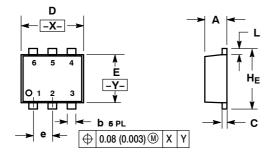


### **TYPICAL CHARACTERISTICS**



#### PACKAGE DIMENSIONS

SOT-563, 6 LEAD CASE 463A-01 **ISSUE F** 



NOTES

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982.
- 2 CONTROLLING DIMENSION: MILLIMETERS

3

MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

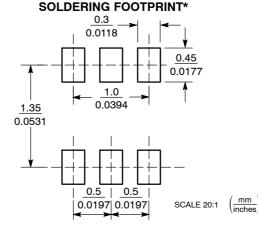
	MILLIMETERS		INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.50	0.55	0.60	0.020	0.021	0.023
b	0.17	0.22	0.27	0.007	0.009	0.011
С	0.08	0.12	0.18	0.003	0.005	0.007
D	1.50	1.60	1.70	0.059	0.062	0.066
Е	1.10	1.20	1.30	0.043	0.047	0.051
е	0.5 BSC		C	.02 BSC	)	
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	1.50	1.60	1.70	0.059	0.062	0.066

STYLE 1:

PIN 1. EMITTER 1 2. BASE 1

3. COLLECTOR 2 4. EMITTER 2

5. BASE 2 6. COLLECTOR 1



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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