- **BiCMOS Design Substantially Reduces Standby Current**
- **Functionally Equivalent to ′ALS29862 and AMD Am29862A**
- **Power-Up High-Impedance State**
- **ESD Protection Exceeds 2000 V Per Mil-STD-883C, Method 3015**
- **Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK) and Flatpacks (W), and Standard Plastic and Ceramic 300-mil DIPs (JT, NT)**
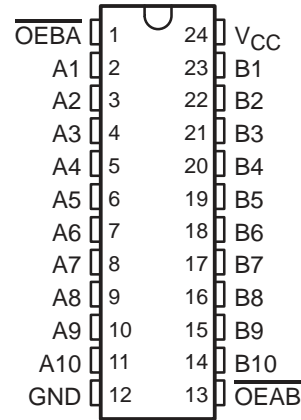
description

These 10-bit bus transceivers are designed for asynchronous communication between data buses. The control-function implementation allows for maximum flexibility in timing.

These devices allow data transmission from the A bus to the B bus or from the B bus to the A bus, depending upon the logic levels at the output-enable ($\overline{\text{OEBA}}$ and $\overline{\text{OEAB}}$) inputs.
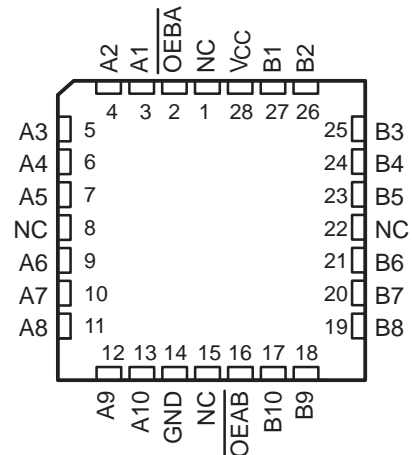
The outputs are in the high-impedance state during power-up and power-down conditions. The outputs remain in the high-impedance state while the device is powered-down.

The SN54BCT29862B is characterized for operation over the full military temperature range of −55°C to 125°C. The SN74BCT29862B is characterized for operation from 0°C to 70°C.

SN54BCT29862B . . . JT OR W PACKAGE
SN74BCT29862B . . . DW OR NT PACKAGE
(TOP VIEW)

$\overline{\text{OEBA}}$	1	24	V_{CC}
A1	2	23	B1
A2	3	22	B2
A3	4	21	B3
A4	5	20	B4
A5	6	19	B5
A6	7	18	B6
A7	8	17	B7
A8	9	16	B8
A9	10	15	B9
A10	11	14	B10
GND	12	13	$\overline{\text{OEAB}}$

SN54BCT29862B . . . FK PACKAGE
(TOP VIEW)

Top pins: A2 (4), A1 (3), $\overline{\text{OEBA}}$ (2), NC (1), V_{CC} (28), B1 (27), B2 (26)

Left pins: A3 (5), A4 (6), A5 (7), NC (8), A6 (9), A7 (10), A8 (11)

Right pins: B3 (25), B4 (24), B5 (23), NC (22), B6 (21), B7 (20), B8 (19)

Bottom pins: A9 (12), A10 (13), GND (14), NC (15), $\overline{\text{OEAB}}$ (16), B10 (17), B9 (18)

NC - No internal connection

FUNCTION TABLE

INPUTS		OPERATION
$\overline{\text{OEAB}}$	$\overline{\text{OEBA}}$	
L	H	$\overline{\text{A}}$ to B
H	L	$\overline{\text{B}}$ to A
H	H	Isolation
L	L	Latch A and B ($\text{A} = \overline{\text{B}}$)

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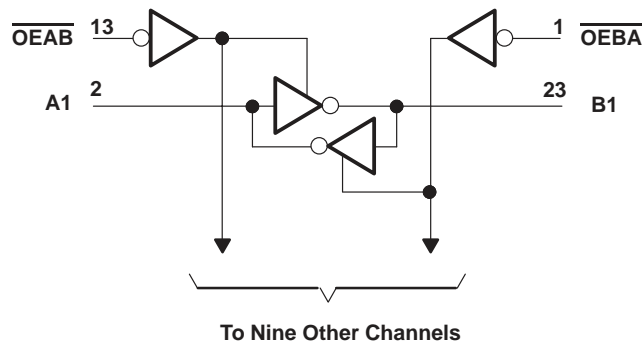
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SN54BCT29862B, SN74BCT29862B
10-BIT BUS TRANSCEIVERS
WITH 3-STATE OUTPUTS

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logic symbol†
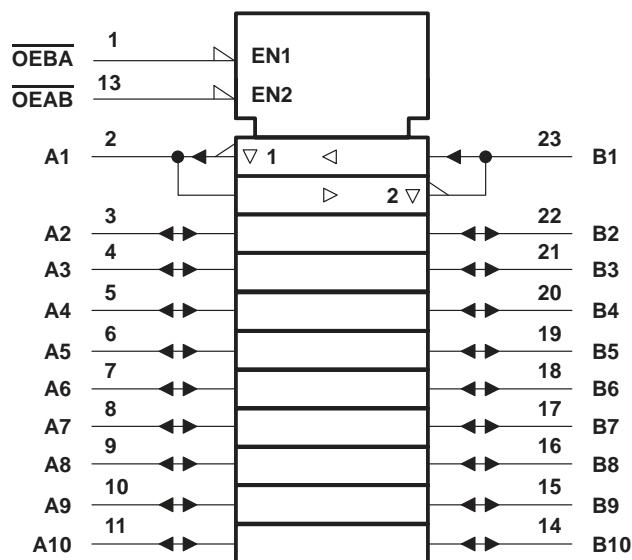
logic diagram (positive logic)

To Nine Other Channels

† This symbol is in accordance with ANSI/IEEE Std 91-1984
and IEC Publication 617-12.
Pin numbers shown are for the DW, JT, NT, and W packages.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V_{CC}	−0.5 V to 7 V
Input voltage (I/O ports) (see Note 1)	−0.5 V to 5.5 V
Input voltage (excluding I/O ports) (see Note 1)	−0.5 V to 7 V
Voltage applied to any output in the high state	−0.5 V to V_{CC}
Input clamp current	−30 mA
Current into any output in the low state: SN54BCT29862B	48 mA
SN74BCT29862B	96 mA
Operating free-air temperature range: SN54BCT29862B	−55°C to 125°C
SN74BCT29862B	0°C to 70°C
Storage temperature range	−65°C to 150°C

‡ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and
functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not
implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input negative voltage rating may be exceeded if the input clamp current rating is observed.

recommended operating conditions

		SN54BCT29862B			SN74BCT29862B			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V_{CC}	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH}	High-level input voltage	2			2			V
V_{IL}	Low-level input voltage			0.8			0.8	V
I_{IK}	Input clamp current			−18			−18	mA
I_{OH}	High-level output current			−12			−24	mA
I_{OL}	Low-level output current			24			48	mA
T_A	Operating free-air temperature	−55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		SN54BCT29862B			SN74BCT29862B			UNIT
				MIN	TYP†	MAX	MIN	TYP†	MAX	
V_{IK}		$V_{CC} = 4.5$ V,	$I_I = -18$ mA			-1.2			-1.2	V
V_{OH}		$V_{CC} = 4.5$ V	$I_{OH} = -12$ mA	2.4	3.3		2.4	3.3		V
			$I_{OH} = -24$ mA				2	3.1		
V_{OL}		$V_{CC} = 4.5$ V	$I_{OL} = 24$ mA		0.35	0.5				V
			$I_{OL} = 48$ mA					0.35	0.5	
I_I		$V_{CC} = 5.5$ V,	$V_I = 5.5$ V			0.1			0.1	mA
I_{IH}	Control inputs	$V_{CC} = 5.5$ V,	$V_I = 2.7$ V			20			20	µA
	A or B port‡					70			70	
I_{IL}	Control inputs	$V_{CC} = 5.5$ V,	$V_I = 0.5$ V			-0.2			-0.2	mA
	A or B port‡					-0.2			-0.2	
$I_{IO(off)}$§		$V_{CC} = 0$,	$V_O = 2.7$ V			0.1			0.1	mA
I_{OS}¶		$V_{CC} = 5.5$ V,	$V_O = 0$	-75		-250	-75		-250	mA
I_{CC}		$V_{CC} = 5.5$ V	Outputs high		20	32		20	32	mA
			Outputs low		29	46		29	46	
			Outputs disabled		6.5	11		6.5	11	
C_i		$V_{CC} = 5$ V,	$V_I = 0.5$ V or 2.5 V		6			6		pF
C_o		$V_{CC} = 5$ V,	$V_I = 0.5$ V or 2.5 V		8			8		pF

† All typical values are at $V_{CC} = 5$ V, $T_A = 25°C$.
‡ For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current.
§ $I_{O(off)}$ = Power-off bus leakage current
¶ Not more than one output should be shorted at a time and duration of the short circuit should not exceed one second.

switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5$ V, $C_L = 50$ pF, $R1 = 500\ \Omega$, $R2 = 500\ \Omega$, $T_A = 25°C$			$V_{CC} = 4.5$ V to 5.5 V, $C_L = 50$ pF, $R1 = 500\ \Omega$, $R2 = 500\ \Omega$, T_A = MIN to MAX#				UNIT
			′BCT29862B			SN54BCT29862B		SN74BCT29862B		
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t_{PLH}	A or B	B or A	1	3.6	5.2	1	6.5	1	6.1	ns
t_{PHL}			0.5	2.4	4.6	0.5	7.5	0.5	4.8	
t_{PZH}	$\overline{OE}$	A or B	2.5	5.3	7.2	2.5	9.1	2.5	8.4	ns
t_{PZL}			4.5	8	10.6	4.5	13.9	4.5	12.5	
t_{PHZ}	$\overline{OE}$	A or B	2.3	4.8	7.6	2.3	8.4	2.3	8.4	ns
t_{PLZ}			2.3	5.2	7	2.3	9.1	2.3	8.2	

For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating condtions.
NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

TEXAS INSTRUMENTS

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