### MAX4715/MAX4716

# 0.4Ω, Low-Voltage, Single-Supply SPST Analog Switches in SC70

## **General Description**

The MAX4715/MAX4716 are low on-resistance, low-voltage, single-pole/single-throw (SPST) analog switches that operate from a +1.6V to +3.6V single supply. The MAX4715 is normally open (NO), and the MAX4716 is normally closed (NC). These devices also have fast switching speeds ( $t_{ON}$  = 18ns max,  $t_{OFF}$  = 12ns max).

When powered from a +3V supply, the MAX4715/MAX4716 offer 0.4 $\Omega$  max on-resistance (R<sub>ON</sub>) with 0.1 $\Omega$  max R<sub>ON</sub> flatness. Their digital logic inputs are +1.8V CMOS compatible when using a single +3V supply.

The MAX4715 is pin compatible with the MAX4594, and the MAX4716 is pin compatible with the MAX4595. The MAX4715/MAX4716 are available in SC70-5 packages.

### **Applications**

- Power Routing
- Battery-Operated Equipment
- Audio and Video Signal Routing
- Low-Voltage Data-Acquisition Systems
- Communications Circuits
- PCMCIA Cards
- Cellular Phones
- Modems
- Hard Drives

#### **Benefits and Features**

- Low Ron
  - 0.4Ω max (+3V Supply)
  - 1.2Ω max (+1.8V Supply)
- 0.1Ω max R<sub>ON</sub> Flatness (+3V Supply)
- +1.6V to +3.6V Single-Supply Operation
- Available in 5-Pin SC70 Packages
- Fast Switching: t<sub>ON</sub> = 18ns max, t<sub>OFF</sub> = 12ns max
- +1.8V CMOS Logic Compatible (+3V Supply)
- Pin Compatible with MAX4594 (MAX4715)
  Pin Compatible with MAX4595 (MAX4716)

Ordering Information appears at end of data sheet.



## MAX4715/MAX4716

# $0.4\Omega$ , Low-Voltage, Single-Supply SPST Analog Switches in SC70

# **Absolute Maximum Ratings**

Voltages Referenced to GND
V+, IN0.3V to +4V
COM, NO, NC (Note 1)0.3V to (V+ + 0.3V)
Continuous Current NO, NC to COM±300mA
Peak Switch Current NO, NC to COM
(pulsed at 1ms, 10% duty cycle max)±600mA
Continuous Power Dissipation (T <sub>A</sub> = +70°C)
5-Pin SC70 (derate 3.1mW/°C above +70°C)247mW

Operating Temperature Range	
MAX471_EXK	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Note 1: Signals on NO, NC, or COM exceeding V+ or GND are clamped by internal diodes.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## **Package Information**

#### **5-PIN SC70**

Outline Number	21-0076
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For the latest package outline information and land patterns (footprints), go to <a href="www.maximintegrated.com/packages">www.maximintegrated.com/packages</a>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

# **Electrical Characteristics—Single +3V Supply**

(V+ = +2.7V to +3.6V,  $V_{IH}$  = +1.4V,  $V_{IL}$  = +0.5V,  $T_A$  =  $T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted. Typical values are at V+ = +3.0V and  $T_A$  = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V <sub>COM</sub> , V <sub>NO</sub> , V <sub>NC</sub>			0		V+	V
On-Resistance (Note 6)	R <sub>ON</sub>	V+ = 2.7V, I <sub>COM</sub> = 100mA,	+25°C		0.3	0.4	Ω
(	1.014	V <sub>NO</sub> or V <sub>NC</sub> = 1.5V	T <sub>MIN</sub> to T <sub>MAX</sub>			0.45	
On-Resistance Flatness	R <sub>FLAT(ON)</sub>	V+ = 2.7V, I <sub>COM</sub> = 100mA,	+25°C		0.05	0.09	Ω
(Note 4)	· FLAT(ON)	$V_{NO}$ or $V_{NC} = 0.6, 1.5V, 2.1V$	T <sub>MIN</sub> to T <sub>MAX</sub>			0.1	
NO, NC Off-Leakage	I <sub>NO(OFF)</sub> or	$V = 3.3V, V_{COM} = 0.3V, 3V$	+25°C	-1	0.01	1	nA
Current	I <sub>NC(OFF)</sub> or	$V_{NO}$ or $V_{NC} = 3V$ , 0.3V	T <sub>MIN</sub> to T <sub>MAX</sub>	-10		10	
COM Off-Leakage Current	I <sub>COM(OFF)</sub>	V+ = 3.3V, V <sub>COM</sub> = 0.3V, 3V	+25°C	-1	0.01	1	nA
	'COM(OFF)	$V_{NO}$ or $V_{NC} = 3V$ , 0.3V	T <sub>MIN</sub> to T <sub>MAX</sub>	-10		10	
COM On-Leakage Current	I <sub>COM(ON)</sub>	$V = 3.3V, V_{COM} = 0.3V, 3V,$	+25°C	-2		2	nA
COM ON Leakage Carrent	(COM(ON)	$V_{NO}$ or $V_{NC}$ = 0.3V, 3V or open	T <sub>MIN</sub> to T <sub>MAX</sub>	-10		10	100
DYNAMIC							
Turn-On Time	t <sub>ON</sub>	$V_{NO}$ or $V_{NC}$ = 1.5V, $R_L$ = 50 $\Omega$ , +25°C $C_L$ = 35pF, Figure 1 $T_{MIN}$ to $T_{MAX}$	+25°C		12	18	ns
Turr on time	UN		T <sub>MIN</sub> to T <sub>MAX</sub>			20	115
Turn-Off Time	<b>+</b>	$V_{NO}$ or $V_{NC}$ = 1.5V, $R_L$ = 50 $\Omega$ , $C_L$ = 35pF, Figure 1	+25°C		6	12	ns
Turn-Oil Time	toff		T <sub>MIN</sub> to T <sub>MAX</sub>			15	113
Charge Injection	Q	V <sub>GEN</sub> = 0, R <sub>GEN</sub> = 0, C <sub>L</sub> = 1.0nF, <u>Figure 2</u>	+25°C		20		pC
Off-Isolation (Note 5)	V <sub>ISO</sub>	$f = 1MHz$ , $V_{COM} = 1V_{RMS}$ , $R_L = 50\Omega$ , $C_L = 5pF$ , Figure 3	+25°C		-54		dB
Total Harmonic Distortion	THD	f = 20Hz to $20kHz$ , $V_{COM} = 2V_{P-P}$ , $R_L = 32\Omega$	+25°C		0.01		%
NC or NO Off-Capacitance	C <sub>NO(OFF)</sub>	f = 1MHz, Figure 4	+25°C		55		pF
COM Off-Capacitance	C <sub>COM(OFF</sub> )	f = 1MHz, Figure 4	+25°C		55		pF
COM On-Capacitance	C <sub>COM(ON)</sub>	f = 1MHz, Figure 4	+25°C		80		pF
LOGIC INPUT							
Input Voltage Low	V <sub>IL</sub>					0.5	V
Input Voltage High	V <sub>IH</sub>			1.4			V
Input Leakage Current	I <sub>IN</sub>	V <sub>IN</sub> = 0 or V+		-1		1	μA
SUPPLY							
Power-Supply Range	V+			1.6		3.6	V
Decitive County Comment	l+	V00V/V 0 V	+25°C		0.04	0.2	
Positive Supply Current		$V+ = +3.6V$ , $V_{IN} = 0$ or $V+$	T <sub>MIN</sub> to T <sub>MAX</sub>			2	μA

## **Electrical Characteristics—Single +1.8V Supply**

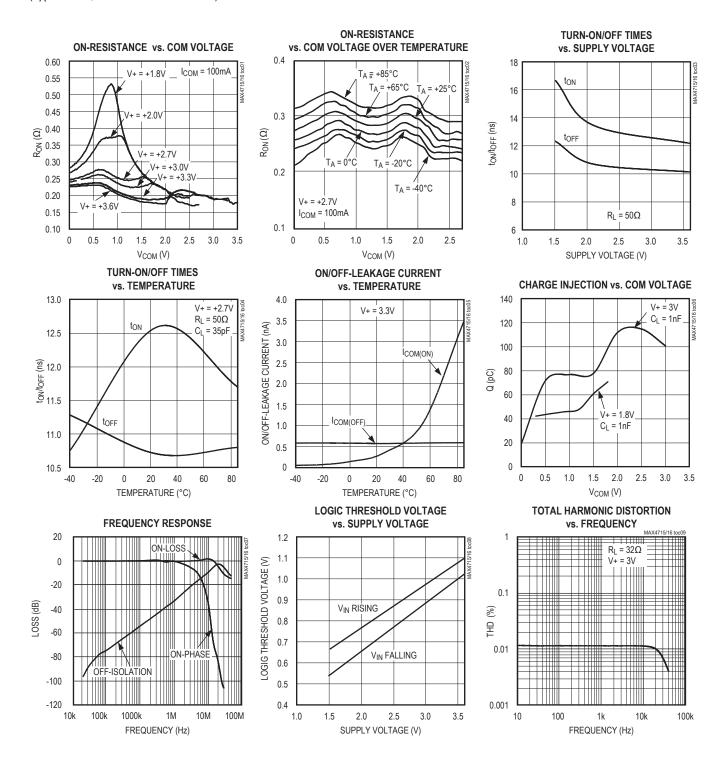
 $(V+=+1.8V,\,V_{IH}=+1V,\,V_{IL}=+0.4V,\,T_A=T_{MIN}\,to\,T_{MAX},\,unless\,otherwise\,noted.\,Typical\,values\,are\,at\,T_A=+25^{\circ}C.)\,(Notes\,2,\,3)$ 

PARAMETER	SYMBOL	CONDITIONS	T <sub>A</sub>	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V <sub>COM</sub> , V <sub>NO</sub> , V <sub>NC</sub>			0		V+	V
On-Resistance	В	I <sub>COM</sub> = 10mA,	+25°C		0.6	1.2	Ω
On-Resistance	R <sub>ON</sub>	$V_{NO}$ or $V_{NC} = 0.9V$	T <sub>MIN</sub> to T <sub>MAX</sub>			2.5	
NO or NC Off-Leakage	I <sub>NO(OFF)</sub> or	V <sub>COM</sub> = 0.3V, 1.5V,	+25°C	-1		1	nA
Current	I <sub>NC(OFF)</sub>	$V_{NO}$ or $V_{NC} = 1.5V$ , 0.3V	T <sub>MIN</sub> to T <sub>MAX</sub>	-10		10	
COM Off Lookaga Current	1	V <sub>COM</sub> = 0.3V, 1.5V,	+25°C	-1		1	n A
COM Off-Leakage Current	ICOM(OFF)	$V_{NO}$ or $V_{NC} = 1.5V$ , 0.3V	T <sub>MIN</sub> to T <sub>MAX</sub>	-10		10	nA
COM On Lookaga Current	1	V <sub>COM</sub> = 1.5V, 0.3V, V <sub>NO</sub> or	+25°C	-2		2	nA
COM On-Leakage Current	I <sub>COM(ON)</sub>	V <sub>NC</sub> = 1.5V, 0.3V, or open	T <sub>MIN</sub> to T <sub>MAX</sub>	-10		10	
DYNAMIC							
Turn-On Time	4	$V_{NO}$ or $V_{NC}$ = 1.5V, $R_L$ = 50 $\Omega$ ,	+25°C		18	25	ns
Turri-Ori Time	t <sub>ON</sub>	C <sub>L</sub> = 35pF, <u>Figure 1</u>	T <sub>MIN</sub> to T <sub>MAX</sub>			30	113
Turn-Off Time	t	$V_{NO}$ or $V_{NC}$ = 1.5V, $R_L$ = 50 $\Omega$ ,	+25°C		9	20	ns
Turri-Oir Time	toff	C <sub>L</sub> = 35pF, <u>Figure 1</u>	T <sub>MIN</sub> to T <sub>MAX</sub>			25	115
Charge Injection	Q	V <sub>GEN</sub> = 0, R <sub>GEN</sub> = 0, C <sub>L</sub> = 1nF, <u>Figure 2</u>	+25°C		40		pC
LOGIC INPUT							
Input Voltage Low	V <sub>IL</sub>					0.4	V
Input Voltage High	V <sub>IH</sub>			1			V
Input Leakage Current	I <sub>IN</sub>	V <sub>IN</sub> = 0 or V+				1	μA
SUPPLY							
Desitive Supply Current	l+	\\	+25°C		0.04	0.2	
Positive Supply Current		$V_{IN} = 0$ or $V+$	T <sub>MIN</sub> to T <sub>MAX</sub>			2	μA

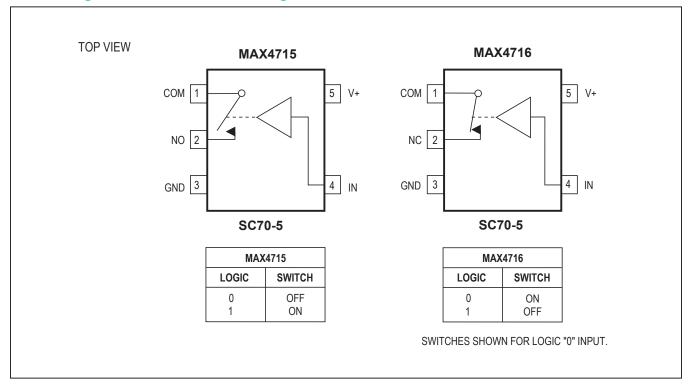
- **Note 2:** The algebraic convention, where the most negative value is a minimum and the most positive value a maximum, is used in this data sheet.
- **Note 3:** SC70-packaged parts are 100% tested at +25°C. Limits across the full temperature range are guaranteed by design and correlation.
- **Note 4:** Flatness is defined as the difference between the maximum and minimum values of on-resistance as measured over the specified analog signal range.
- Note 5: Off-Isolation =  $20log_{10} [V_{COM} / (V_{NC} \text{ or } V_{NO})], V_{COM} = \text{ output, } V_{NC} \text{ or } V_{NO} = \text{ input to off switch.}$
- Note 6: Guaranteed by design.

# **Typical Operating Characteristics**

 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$ 



# **Pin Configurations/Functional Diagrams/Truth Tables**



# **Pin Description**

BU	MP		FUNCTION	
MAX4715	MAX4716	NAME	FUNCTION	
1	1	COM	Analog Switch—Common	
2	_	NO	Analog Switch—Normally Open	
_	2	NC	Analog Switch—Normally Closed	
3	3	GND	Ground	
4	4	IN	Digital Control Input	
5	5	V+	Positive Supply Input	

# $0.4\Omega$ , Low-Voltage, Single-Supply SPST Analog Switches in SC70

## **Detailed Description**

The MAX4715/MAX4716 are low on-resistance ( $R_{ON}$ ), low-voltage, single-pole/single-throw (SPST) analog switches that operate from a +1.6V to +3.6V single supply. The MAX4715 is normally open (NO), and the MAX4716 is normally closed (NC).

When powered from a +3V supply, their  $0.4\Omega$  R<sub>ON</sub> allows high continuous currents to be switched in a variety of applications.

## **Applications Information**

### **Logic Inputs**

The MAX4715/MAX4716 logic inputs can be driven up to +3.6V regardless of the supply voltage. For example,

with a +3.3V supply, IN may be driven low to GND and high to +3.6V. Driving IN Rail-to-Rail  $^{\circledR}$  minimizes power consumption.

### **Analog Signal Levels**

Analog signals that range over the entire supply voltage (V+ to GND) can be passed with very little change in on-resistance (see the <u>Typical Operating Characteristics</u> section). The switches are bidirectional, so the NO, NC, and COM pins can be used as either inputs or outputs.

Rail-to-Rail is a registered trademark of Nippon Motorola Ltd.

## **Test Circuits/Timing Diagrams**

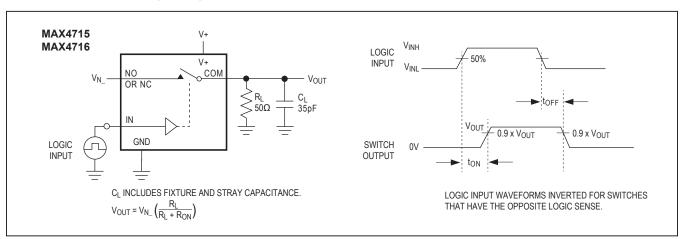


Figure 1. Switching Time

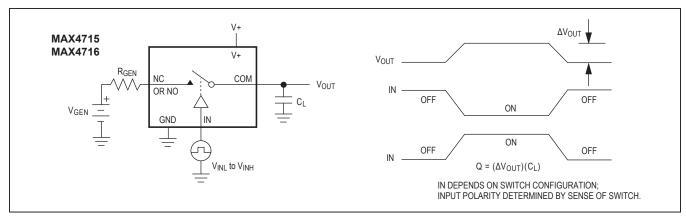


Figure 2. Charge Injection

# **Test Circuits/Timing Diagrams (continued)**

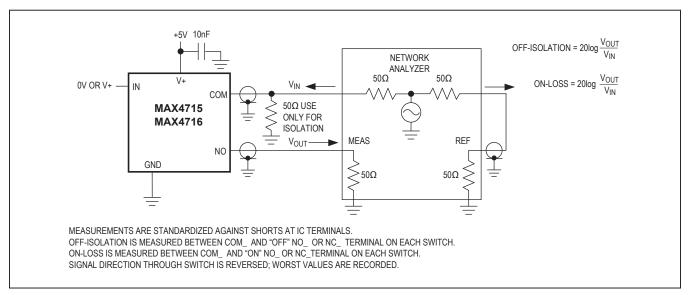


Figure 3. On-Loss and Off-Isolation

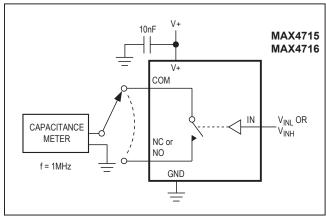


Figure 4. Channel Off/On-Capacitance

# **Ordering Information**

PART	TEMP. RANGE	PIN- PACKAGE	TOP MARK
MAX4715EXK+T	-40°C to +85°C	5 SC70-5	ACJ
MAX4716EXK+T	-40°C to +85°C	5 SC70-5	ACK

+ Denotes a lead(Pb)-free/RoHS-compliant package. T = Tape and reel.

# **Chip Information**

TRANSISTOR COUNT: 135

PROCESS: CMOS

## MAX4715/MAX4716

# $0.4\Omega$ , Low-Voltage, Single-Supply SPST Analog Switches in SC70

# **Revision History**

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	4/01	Initial release	_
1	3/20	Updated the Ordering Information table	8
2	2/21	Updated Pin 3 for MAX4715 in Pin Description.	6

For pricing, delivery, and ordering information, please visit Maxim Integrated's online storefront at https://www.maximintegrated.com/en/storefront/storefront.html.

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