

LTC6802-2 Battery Monitor

HARDWARE/SOFTWARE USERS GUIDE

DESCRIPTION

Evaluation circuit DC1393B is a Battery Monitoring System to demonstrate the functional operation of the **LTC6802-2** integrated circuit. The **LTC6802-2** is a complete battery monitoring IC that can measure as many as 12 series connected cells with a total voltage of 60V. A simple SPI serial interface is used to communicate battery state information to a system control processor.

Use of an isolated serial interface allows monitoring of completely separate battery cell packs or a series stack of cells. Stacking of cells permits the monitoring of each individual cell in systems requiring 100's of volts from the battery pack, such as those used in Hybrid Electric Vehicles (HEVs). Each device also has a built in supply voltage regulator.

In addition, each individual cell connection has a software controlled discharge transistor. This allows the implementation of precision cell charging algorithms including charge shedding of over-voltage cells.

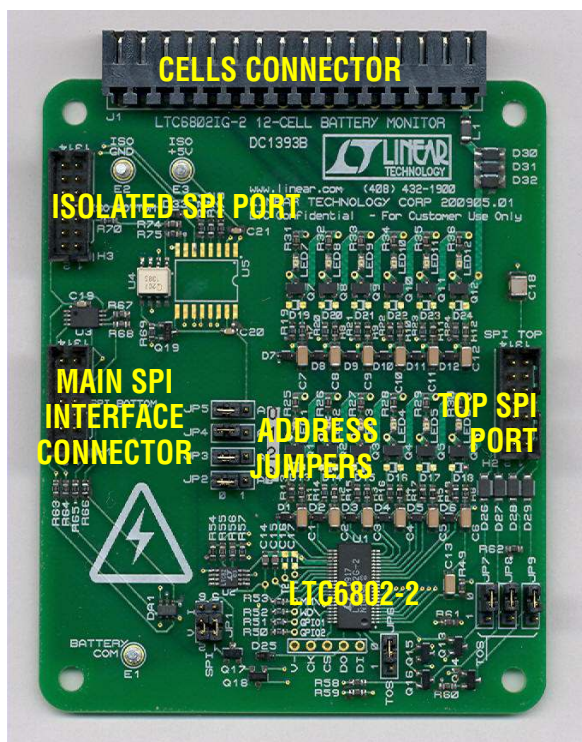
A group of up to ten DC1393B boards can be managed through the USB port of a PC using an on screen control panel available from LTC.

LTC6802-2 REV C/D FEATURES

- 12-bit accurate voltage measurement of 12 battery cells (60V maximum/board)
- 4-bit addressable serial interface for high cell count systems (16 boards x 12 cells = 192 Cells)
- New commands allow reading of individual or all cell voltages, open wire connections and temperature sensors
- Option to measure cell voltages and perform open wire detection with or without the cell discharge transistors conducting
- Functionality self test commands
- Thermal Shutdown built in and flagged
- Silicon revision code provided

DC1393B DEMO FEATURES

- New Graphical User Interface (GUI) for demonstrating all new Rev C/D features
- Optional isolated main SPI communication port
- External discrete circuitry for daisy-chained current mode SPI communication up and down a stack of monitors
- ESD and fault protection circuitry added



GETTING STARTED WITH ONE BOARD CONNECTED

SINGLE BOARD CONNECTION TO PC AND GUI

Step 1. Set jumpers on DC1393B to the default positions indicated in Table 1.

TABLE 1. JUMPER FUNCTIONS

JUMPER	FUNCTION	DEFAULT POSITION	“1” POSITION	“0” POSITION
JP1	Two Jumper Block to Select Voltage or Current Mode for Bottom SPI Port	Voltage Mode	Labeled “V” on board. Sets bottom port for voltage mode SPI communication.	Labeled “I” on board. Sets Top and Bottom ports for Current Mode SPI communication using external daisy-chain current source circuitry.
JP6,JP7,JP8,JP9	SPI Daisy Chain Configuration Jumpers: (All Four Jumpers Must be Moved Together)	1, 1, 1, 1 Top of Stack (TOS) Setting	For V Mode communication to all boards set all four jumpers to the 1 position. For SPI daisy chain communication this setting is required for only the board at the top of a cell stack. These settings connect the top daisy chain current source transistors to the top cell at the top of the cell stack.	For SPI daisy-chain communication set all four jumpers to the 0 setting. This enables the top port on each board for current mode communication up the stack. Use these settings also for the bottom board in a daisy chain stack which should be set to V mode (JP1) for the bottom port communication to a system controller.
JP2,JP3,JP4,JP5	Board Address Setting (0000 to 1111) JP2 is MSB JP5 is LSB	0, 0, 0, 0 (for address 0000)	Logic ‘1’ setting	Logic ‘0’ setting

Step 2. Connect the DC590 Quick Eval USB cable to PC/Laptop USB port. Connect a ribbon cable from DC590 to the Main connector of DC1393B (H1).

Step 3. Before running the DC1393 GUI, a DC590 driver must be installed. To do this, install the Quick Eval Software. Quick Eval can be downloaded from:

<http://www.linear.com/designtools/software/>

After installation **close the DC590 driver program.** Launch the GUI control program:

DC1393_LTC6802-2_GUI_V_25_mmddyyyy.exe

The month/day/year addition to the file name identifies the version as changes are made. When the DC590 Quick Eval board recognizes the String ID code from the DC1393B board, the program will open and present the control screen. **The program will not open unless DC590 is connected to the computer and the DC1393B board.**

Step 4. Connect the cells to be monitored to the cells connector J1. This connector is in two pieces. The setscrew piece can be unplugged to make it easier to attach a wiring harness from a four to twelve cell battery stack. The **LTC6802-2** is intended to measure from four to twelve individual cells with a total stack voltage of 10V to 60V.

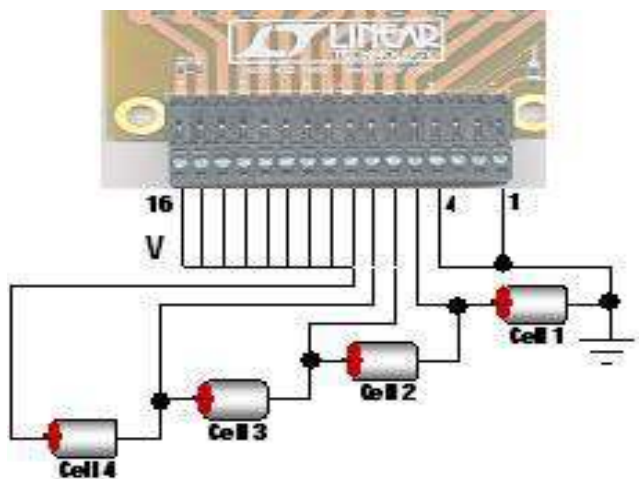
With fewer than 12 cells to be monitored, the bottom cell of the stack should always be connected as Cell 1 between terminals J1-5(+cell contact) and J1-4(-cell contact). Terminals J1-4 and J1-1 are the ground reference point for the battery cell stack and for the DC1393B board. The second cell on the stack connects between terminals J1-6(+cell contact) and J1-5(-cell contact). All higher numbered terminals on J1 not used for cell connections should be shorted together. The V+ positive supply for the DC1393B is the potential at terminal J1-16 and therefore must connect to the top of the battery

stack. Figure 1 illustrates a connection for fewer than 12 cells.

Step 5. Apply power.

Inserting the setscrew piece into connector J1 will apply power to the board from the battery cell stack.

Figure 1. Connection of four cells (cell voltages at least 2.5V)



THE CONTROL PROGRAM

THE GRAPHICAL USER INTERFACE (GUI) SCREENS

Figure 2 shows the control panel that appears on the computer screen. Controls on this panel are used to communicate with the **LTC6802-2**. Commands are issued and information is retrieved and displayed on this screen. This panel is useful not only for demonstrating the operation of the **LTC6802-2**, but also for software developers to observe the Hex codes exchanged with the device.

Figure 2. PC GUI Control Panel

Hex Code Display for Configuration, System Flags and CRC Check

The screenshot displays the DC1393 LTC6802-2 MULTICELL ADDRESSABLE BATTERY MONITOR GUI. It features a grid of cell voltage readings for Board 4, a status panel with color-coded indicators, and various control buttons and configuration registers.

Cell Voltage Readings for Selected Board:

Cell	Voltage	Mask	DCC
CELL12	1.895V	<input type="checkbox"/>	<input type="checkbox"/>
CELL6	1.892V	<input type="checkbox"/>	<input type="checkbox"/>
CELL11	1.890V	<input type="checkbox"/>	<input type="checkbox"/>
CELL5	1.895V	<input type="checkbox"/>	<input type="checkbox"/>
CELL10	1.893V	<input type="checkbox"/>	<input type="checkbox"/>
CELL4	1.895V	<input type="checkbox"/>	<input type="checkbox"/>
CELL9	1.896V	<input type="checkbox"/>	<input type="checkbox"/>
CELL3	1.895V	<input type="checkbox"/>	<input type="checkbox"/>
CELL8	1.893V	<input type="checkbox"/>	<input type="checkbox"/>
CELL2	1.835V	<input type="checkbox"/>	<input type="checkbox"/>
CELL7	1.893V	<input type="checkbox"/>	<input type="checkbox"/>
CELL1	1.833V	<input type="checkbox"/>	<input checked="" type="checkbox"/>

Color Coded Status Panel for All Cells Connected:

Cell	Board 1	Board 2	Board 3	Board 4	PEC
CELL12	Green	Green	Green	Green	C12
CELL11	Green	Green	Green	Green	C11
CELL10	Green	Green	Green	Green	C10
CELL9	Green	Green	Green	Green	C9
CELL8	Green	Green	Green	Green	C8
CELL7	Green	Green	Green	Green	C7
CELL6	Green	Green	Green	Green	C6
CELL5	Green	Green	Green	Green	C5
CELL4	Green	Green	Green	Green	C4
CELL3	Green	Green	Green	Green	C3
CELL2	Green	Green	Green	Green	C2
CELL1	Green	Green	Green	Green	C1

Board Selection Tabs:

- BRD# 4 (0x83) - Selected
- BRD# 3 (0x82)
- BRD# 2 (0x81)
- BRD# 1 (0x80)

SET VOLTAGE LIMITS:

- ITMP Only: GLOBAL VUV, VOV
- CUSTOM BOARD # 4: Enter VOV: 2.487V, 2.496V, 0.000V to 6.120V
- Enter VUV: 0.815V, 1.896V, 0.000V to 6.120V
- ETMP2Only: 2.333V

SET I/O MODE CFGRO:

- WDTEN bit7 1 = ON, 0 = OFF
- GPIO2 bit6 1 = Hi-Z, 0 = Out Low.
- GPIO1 bit5 1 = Hi-Z, 0 = Out Low.
- LVLPL bit4 1 = Level, 0 = Toggle Poll.
- 10CELLS bit3 1 = 10, 0 = 12 CELLS

SELF TEST START A/D:

- CELL VOLT: CELL DCC: OPEN WIRE: OPEN DCC: ALL TEMP:
- 0x555: READCELL: READTEMP:
- 0xAAA: HEX VIEW: HEX VIEW:

Configuration Registers:

CONFIGURATION READ FROM LTC6802: FLGR0 | 55, FLGR1 | 55, FLGR2 | 54

CONFIGURATION WRITTEN TO LTC6802: ICFGR0 | E1, ICFGR1 | 01, ICFGR2 | 00, ICFGR3 | 00, ICFGR4 | 4F, ICFGR5 | 68

Packet Error Code: 0x02 == 0x02

Command: 06 RDFLG

Buttons: START OPEN WIRE, WRITE CONFIG, BOARD# 4 ONLY READ CONFIG, BROADCAST, START CELL VOLT, BOARD# 4 ONLY READCELL VOLT, BROADCAST, START TEMP, BOARD# 4 ONLY READ TEMP, BOARD# 4 ONLY READ FLAG, DCC PERMITTED, BROADCAST, START CELL HOLD DCC, BROADCAST, START OPEN HOLD DCC, PEC Match Only, LOW CURRENT STBY, STOP CONTINUOUS READ CELLS

Set Individual Board Addresses

Temperature Readings

Programmable Over and Under Voltage Thresholds

Configure I/O and Operating Modes

Continuous Reading of All Cells Function

OPERATING THE CONTROL SCREEN

FIRST THINGS FIRST – ONLY ONE BOARD CONNECTED

The GUI start-up screen appears when the program is launched and the DC590 Quick Eval interface card recognizes that the DC1393B board is connected. Once power is supplied to the board from a stack of cells or a power supply, the communication between the PC and the board can be checked.

1: SET BOARD ADDRESS

A feature of the LTC6802-2 is that each device/board is given a unique 4-bit address. Communication between the GUI and a board will occur only when the address set on the board with jumpers J2 through J5 matches the address set in the GUI for that board. The default selected board and address on power up is Board #1 with the address of 0x80. The actual address byte for each board is 8-bits long, but only the 4 LSBs are programmable. Set the jumpers to the address on the GUI or change the GUI to match the board (using the Set Hex Address box in the lower left corner of the GUI) to enable communication with the DC1393B.

2: READ CONFIGURATION

Click the command button labeled **READ CONFIG**. If all is properly connected and operating the start-up default configuration of the **LTC6802-2** (standby mode) will be read from the board. The Hex codes for the six bytes of configuration setting will appear in the **CONFIGURATION REGISTERS** section in the boxes labeled **CONFIGURATION READ FROM LTC6802-2**. The initial configuration bytes should be 0xE0 for configuration register 0 (CNFRG0) and 0x00 for the other five bytes.

In addition the **LTC6802-2** calculates a Packet Error Code, PEC, and appends it to the data stream each time it sends out data. For the six bytes sent by this command and received by the GUI, the control program calculates a PEC in the same manner. This byte is compared with the appended byte to check that the data transmission was properly executed. Both PEC bytes are displayed in the top section labeled **PACKET ERROR CODE CHECK** and both bytes should match.

The oval located at the top of the color-coded status panel for the one board will turn green if the PEC bytes match. Data transmission errors will produce red warning indications if the PEC bytes do not match.

3: PROGRAM THE CELL MONITORING VOLTAGE THRESHOLDS

In the section labeled **SET VOLTAGE LIMITS** click on the boxes and enter voltage values for the over-voltage and under-voltage thresholds required for the cells being monitored. The voltage value entered will be rounded to the actual value used by the **LTC6802-2** and displayed in the box. The voltage ranges for these thresholds is 0V to 6.12V and the program will not allow the 'under' to be greater than the 'over' voltage threshold.

These monitor thresholds can be applied globally to each and every cell in the system or customized for the cells connected to an individual board by clicking the desired option button. Individual boards are selected for programming by the left hand tabs in multiple board systems.

4: SELECT AN OPERATING MODE

The power-on operating mode for the LTC6802-2 is Standby. The GUI initializes to this state also to serve as a reminder that the device must be configured before starting operation. Select one of the seven CDC (Comparator Duty Cycle) options from the scroll box in the **SET I/O MODE** section at the bottom of the screen.

5: WRITE CONFIGURATION

Nothing is changed in the LTC6802-2 until the Write Configuration command is executed. Clicking the **WRITE CONFIG** command button does this. When the command is sent, the six Hex bytes shown in the **CONFIGURATION REGISTERS** section in the boxes labeled **CONFIGURATION WRITTEN TO LTC6802-2** will become **bold** type. Software developers can note the exact hex values required by the LTC6802-2 for specific conditions in these boxes to facilitate their control program development.

Clicking the **READ CONFIG** button can provide confirmation that the configuration change was actually made. The six bytes read back should match the six

bytes sent and the PEC/CRC check bytes should be a match (green PEC oval above the stack status display).

IMPORTANT NOTE

No configuration changes take effect until the WRITE CONFIG button is clicked. A flashing backlight serves as a reminder that this needs to be implemented.

Consult the LTC6802-2 data sheet for detailed information concerning the variety of software configurable functions possible. The following is a summary of these functions:

- Discharging individual cells
- Changing Over/Under voltage thresholds
- Preventing UV/OV interrupt flags from modifying system operation through masking.
- Systems status polling type; level or toggle polling
- Conversion time for measuring all cells on a stack.
- Monitor comparator time interval
- GPIO pin functions -Inputs or Outputs
- 10cell/12cell conversion mode
- Standby Mode-low current drain

All of these can be implemented from the control panel when selected from various points on the screen then followed by a **WRITE CONFIG** button click.

6: READ TEMPERATURE

The LTC6802-2 has three ADC channels dedicated to measuring temperature. The temperature indications are for the internal die temperature of the LTC6802-2 and two externally connected thermistors. The display returns a voltage measurement.

The internal die temperature sensor produces a voltage that changes at a rate of 8mV/°C relative to absolute zero. To convert the voltage reading to degrees Centigrade, divide the voltage by 8mV then subtract 273°C. For example, 25°C is a reading of 2.384V.

For external temperature measurements connect thermistors across cells connector terminals J1-3 to J1-1 and J1-2 to J1-1. Alternatively thermistors can be connected to the board as resistors R35 and R40 located near connector J1. A thermistor with a 25°C value of 10KΩ will produce a half scale voltage reading at 25°C. Any thermistor value may be used but scaling the vol-

tage measurement may require changing the values of resistors R29 and R30 on the DC1393B circuit board.

To take a temperature reading simply click the **START TEMP** command button to make the LTC6802-2 ADC conversion followed by clicking the **READ TEMP** command button to download the data from the board and display the voltage readings for temperature.

7: READ CELL VOLTAGES

The essential function of the LTC6802-2 is to measure and report the voltage on each battery cell when commanded. Once again this is accomplished from the control screen with two command button clicks. First click on the **START CELL VOLT** button. This commands an A/D conversion of all 12-cell voltages in the time configured from the selected Operating Mode (Set CDC option) in the **SET I/O MODE** box.

NOTE: All 12 cell voltages are converted every time regardless of the number of cells actually connected to the LTC6802-2. Unused cell connections, which should be shorted together, will return voltage readings of 0.000 volts.

The actual cell voltage measurements are not displayed until the **READ CELL VOLT** command button is clicked.

8: READ FLAGS

When any cell in a stack exceeds the programmed over or under voltage threshold limit, one of two flag bits is set in an internal register for that cell to serve as a warning. This is important feedback for battery charging algorithms to know simply when to start or stop charging. To read the state of these warning flags at any time is a simple click of the **READ FLAG** command button. The Hex code for the three flag bytes appears in the **FLAG REG** section of the control panel at the top of the screen.

One of the configuration options is to mask these flags from appearing in the register bytes that are read from the LTC6802-2. This feature can be used to prevent or allow these flags to interrupt the flow of the control algorithm. A check box is provided for each cell in a stack to select the mask interrupts option for that cell. To implement the masking requires checking the box

and then writing the new configuration with a **WRITE CONFIG** button push.

9: DISCHARGE CELLS

Another major feature of the LTC6802-1 is the ability to remove charge from individual cells. This can help to distribute the cell charge evenly over a stack of batteries. DC1393B contains a P channel Mosfet in series with a 15Ω resistor across each cell connection. When enabled, a cell is shorted and charge is pulled from the cell with energy dissipated in the switch and resistor.

A check box is provided for each cell to be discharged. Checking this box and then writing the new configuration with a **WRITE CONFIG** button push will short the cell.

IMPORTANT NOTE: The discharge transistors are automatically turned off momentarily while the A/D converter is measuring the cell voltage. This prevents any voltage drop errors caused by the discharge current flowing through the cell inter-connection wiring. An accurate indication of the true state of charge of the cells is then obtained.

The command set and GUI offer the option of keeping the discharge transistors on while measuring the cell voltages. This is done by using the STARTCELL hold DCC command button. A blue indicator is illuminated when this command is in effect. The lower voltage reading due to the discharging condition is the actual cell voltage contribution to the stack.

OTHER CONTROL FEATURES

Three additional command buttons are provided on the control screen. The **POLL ADC** and **POLL INT** command buttons are used to test if the ADC is busy making conversion and to test if any of the LTC6802-2 devices in a system have an interrupt condition respectively. The result of these commands can be observed by monitoring the serial data output line of the SPI interface at pin 5 of the Main connector, Hi. There is no indication provided on the control screen.

The **START OPENWIRE** command button connects the built in open wire detection circuitry to all cells. A **START CELL VOLT** and **READ CELL VOLT** command button click sequence must follow to determine the

result. Ideally an open wire connection to any cell will be indicated by an abnormally high voltage measurement for the cell above the open wire and a near 0V measurement for the cell with the open wire.

CONTINUOUS OPERATION

For convenience, the control panel allows for continuous operation of the DC1393B board. The command button labeled **START CONTINUOUS READ CELLS** can be clicked and the board control is placed in a continuous loop executing the following commands automatically in the following sequence:

- Start cell voltage
- Read cell voltage
- Start temp
- Read temp
- Read flags

All values are updated continually. While running, the configuration can be changed on the fly. Simply changing a configuration item (Discharge cells for example) and clicking the **WRITE CONFIG** button will implement the new configuration and return to continuous operation.

A green box in the lower right hand corner indicates that the system is running continuously. A red box means that the system is stopped and waiting for a new command to be sent.

DISPLAYING VALID DATA TRANSFERS ONLY

Each time data is transferred from the LTC6802-2 by the four READ commands (Cell Voltage, Configuration, Flag Status and Temperature), a Packet Error Code, PEC, is calculated based on the data stream sent. The control program also calculates a PEC value based on the data it receives. If the calculated PEC matches the transmitted value the data transfer is assumed to be error free and therefore the data is valid.

If the two PEC values do not match, the transmitted data stream has been somehow corrupted. This type of data error becomes more of a concern when boards are stacked and the transmit data stream is lengthened. The transmitted and calculated PEC values are displayed on the GUI and turn red when a mismatch occurs.

A small checkbox located in the lower right corner of the GUI, called **PEC Match Only**, is provided as a system control option. When left unchecked the control of the system is normal and all information read from the LTC6802-2 is displayed, even if the transmitted data was corrupted and a PEC mismatch occurred.

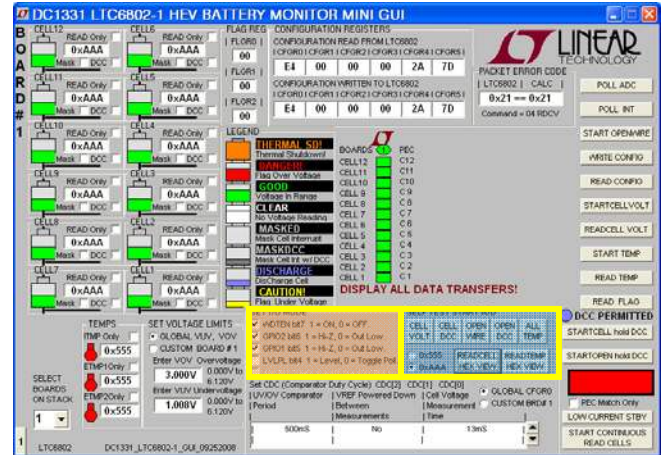
Checking this box instructs the control program to automatically retry the READ commands. The only data displayed is that which was transmitted without error. **This setting masks all transmission errors and makes for a smooth running, error free demonstration.**

The state of the PEC Match Only option is indicated below the color-coded status panel.

LOW CURRENT STANDBY

An important system consideration is the ability to put the monitoring circuitry into a low current drain condition. This is done by setting the LTC6802-2 into its standby configuration. A command button in the lower right corner of the screen is provided to facilitate this function. Once pushed all data and configuration settings are reset and the screen goes white on all indicators.

SELF TEST FUNCTIONS



Revision C of the LTC6802-2 has built in self test functions. These commands apply a test signal to the ADC to check that the internal cell voltage and temperature connections are functioning. The cell voltage and open wire test signals can be applied with or without the discharge transistors active. Checking the functionality of each bit in the internal data registers for cell voltages and temperatures can also be seen by choosing which test code (0x555 or 0xAAA) to expect to be returned from the device when a self test command is issued.

OTHER CONFIGURATION OPTIONS

The SET I/O MODE group of checkboxes can be used to adjust other features of the LTC6802-2. Enabling the Watchdog timer, configuring the general purpose I/O pins and setting the type of activity polling scheme can be select and configured with a WRITE CONFIG button push.

ADDING BOARDS TO MEASURE MORE CELLS

An important function of the LTC6802-2 is the ability to communicate with devices connected to up to 16 individually addressable battery cell stacks. Likewise 16 DC1393B boards, monitoring up to 12 cells each, can be controlled and read individually. **The control GUI is limited to only 10 boards (120 cells) maximum.** The battery cell stacks can be completely separate from each other, or stacked on top of each other with an isolated digital control interface. To control more than one board requires the following hardware and software modifications:

HARDWARE ADJUSTMENTS

1. Each board needs a unique 4-bit binary address between 0 (0000) and 15 (1111). The LSB of the address is A0 and is set with jumper J5 (1 or 0). Bits A1 through A3 (the MSB), are set with jumpers J4, J3 and J2. Any address can be used with any board so long as no two are the same. The GUI can also be programmed to match the address of each board.
2. Refer to the following configuration drawings (A through E) for connecting to multiple battery stacks. Just a three board example is shown for simplicity; more boards will require individual address settings.

EXTERNAL DIGITAL ISOLATOR SUPPLY

The DC590 interface is electrically isolated from the controlling computer power. This can allow communication from a PC with full isolation from the main AC power. Several battery stacks which have a common ground connection can be controlled by a single bottom port SPI bus as shown in configuration B.

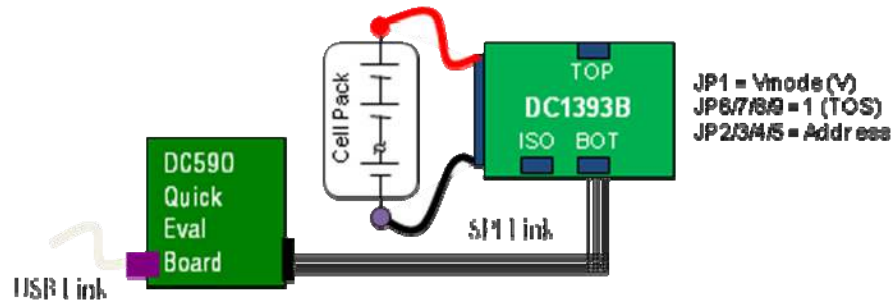
To communicate with multiple boards connected to stacked cells requires the addition of an isolator circuit on each DC1393B board for device U5 and connection to the alternative bottom port isolated connector, H3. Power for the isolators can be provided through the DC590 connector at H3 up to 30mA total. If the operat-

ing current required by all of the isolators exceeds the available current from this supply, an external 5V power supply connected to terminals E3 and E2 (ground) will ensure proper data communication. Make sure to disconnect the power supplied by DC590 to pin 2 of connector H3. An external supply is shown in the isolated configuration schematics but is not required if the DC590 isolated port and supply are used.

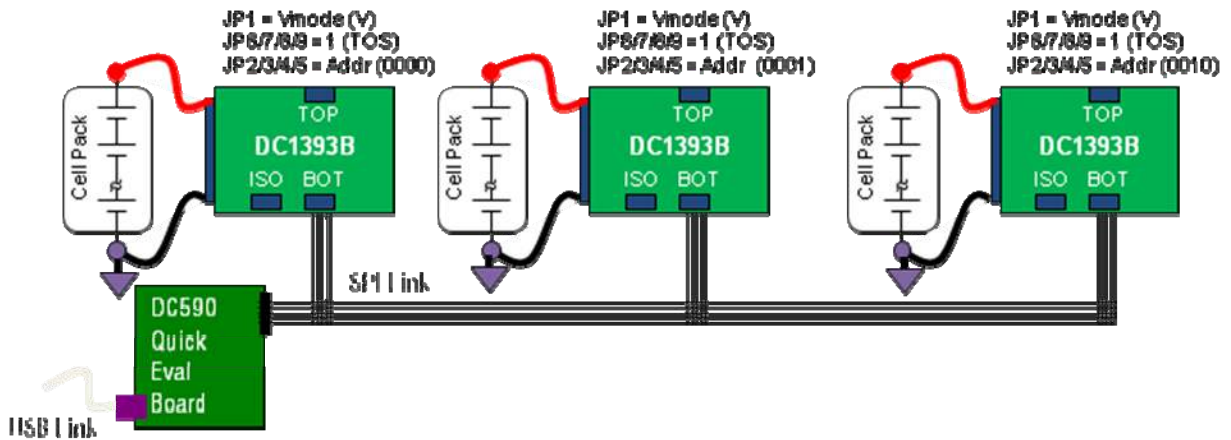
3. Pay close attention to the jumper settings for each board in a stack depending on the configuration chosen. It is essential that the current/voltage I/V Communication Mode and Top-of-Stack (TOS) settings be correct for communication to work properly.

CAUTION! CAUTION! CAUTION!

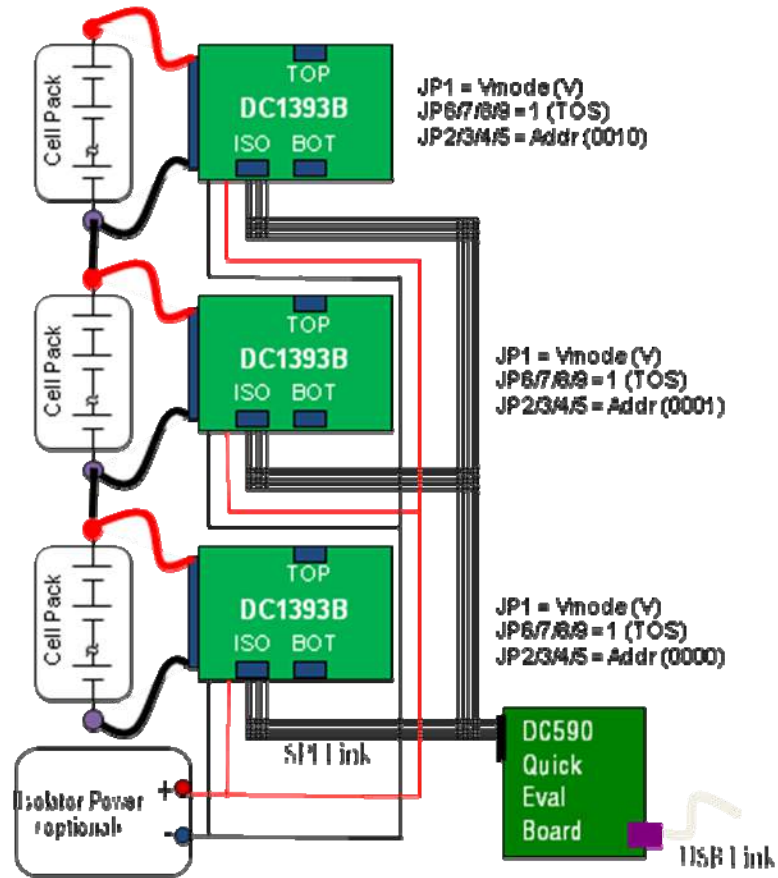
As battery cells are stacked on top of each other, great care must be taken to prevent damage and personal injury from the very high voltage potentials that may be present. Do not allow short circuit connections, whether electrical or human, between a high voltage point and the system or chassis ground at the bottom of the stack. Be very careful and respect the potential danger of high voltage!



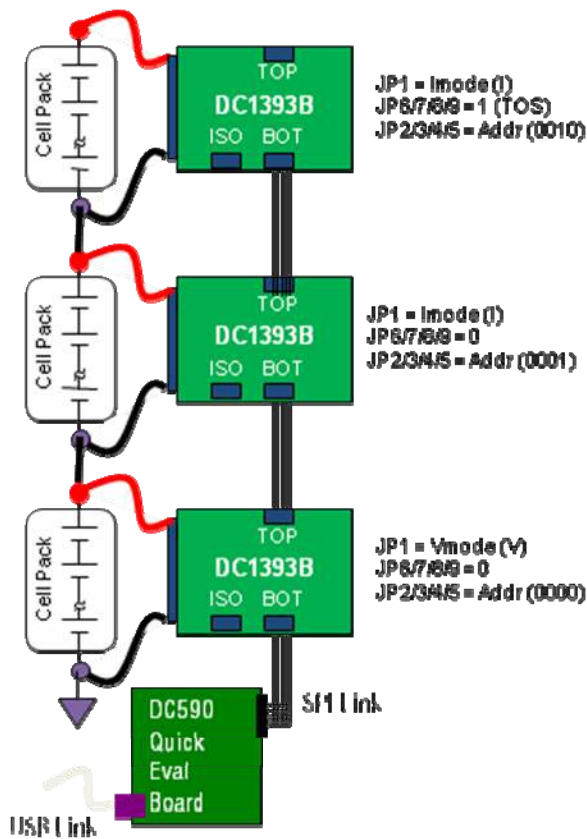
Configuration A. Single Cell Stack Monitor



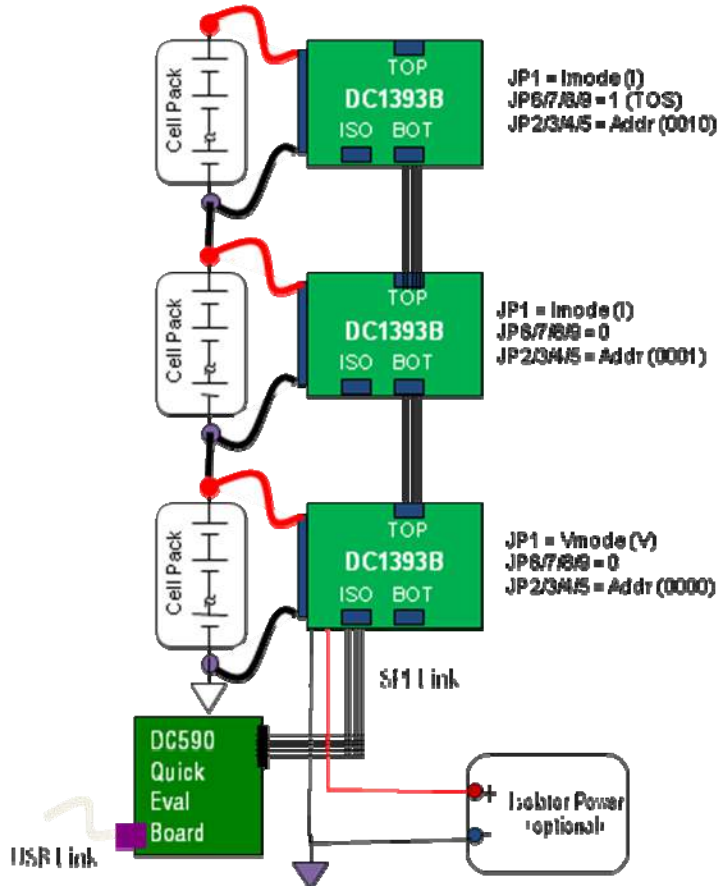
Configuration B. Multiple (16 Maximum) Cell Pack Monitors. Each pack must be connected to a common ground.



Configuration C. Isolated Multiple (16 Maximum) Stacked Cells Monitor. Each board requires an isolator device and isolator power.



Configuration D. Daisy Chain SPI Communication to Each Board on a Stack. Only the Top board is set to Top of Stack (TOS) and only bottom board is set to Vmode.



Configuration E. Isolated Daisy Chain SPI Communication to Each Board on a Stack. Only the Top board is set to Top of Stack (TOS) and only bottom board is set to Vmode. Bottom board also requires installation of an isolator and power for it.

INITIAL SOFTWARE ADJUSTMENTS

The GUI program can control up to ten boards.

Select the number of boards connected from the list box located in the lower left corner of the screen.

A tab will appear on the left edge of the control panel for each board on the stack.

Click each board tab separately and set the following parameters for the selected board:

1. Select the address for each board to match its jumper-set value. Values range from 0 to F. Make sure that each board has a unique address.
2. Select whether the Over/Under voltage thresholds for each board are to be the same (GLOBAL) or different (CUSTOM) and set the voltage limits accordingly. If Global, the limits need to be entered only once.
3. Since the default power up state of the LTC6802-2 is Standby, the GUI starts up this way to serve as a reminder that the device must be configured before any other operations. Select an operating mode from the Set CDC scroll box at the bottom.
4. Click **WRITE CONFIG** to configure the board for basic operation.
5. Click **READ CONFIG** to verify that the proper word was written to the board.
6. Select another board tab on the left and repeat steps 1 to 5.

BROADCAST COMMANDS-BE CAREFUL

Each LTC6802-2 controlling command has the option to be broadcast to all boards in the system simultaneously. This can be quite convenient, but it can also make unintended changes to some of the boards. When the **WRITE CONFIG** command B'Cast check box is checked, the settings currently visible on the screen will be sent to all boards and will change their configuration for voltage monitoring threshold and operating mode. If unpredictable results are seen from a board it could be due to the configuration change. Making a **READ CONFIG** on that board will indicate the present configuration.

CONTINUOUS READING OF ALL CELLS IN A SYSTEM

When put into continuous mode each board is exercised in succession. The moving LTC logo indicates which board is being measured. The cell voltage and temperature readings are for the Tab selected board only. When stopped, all readings are cleared.

DC 1393B

COLOR CODED STATUS PANEL

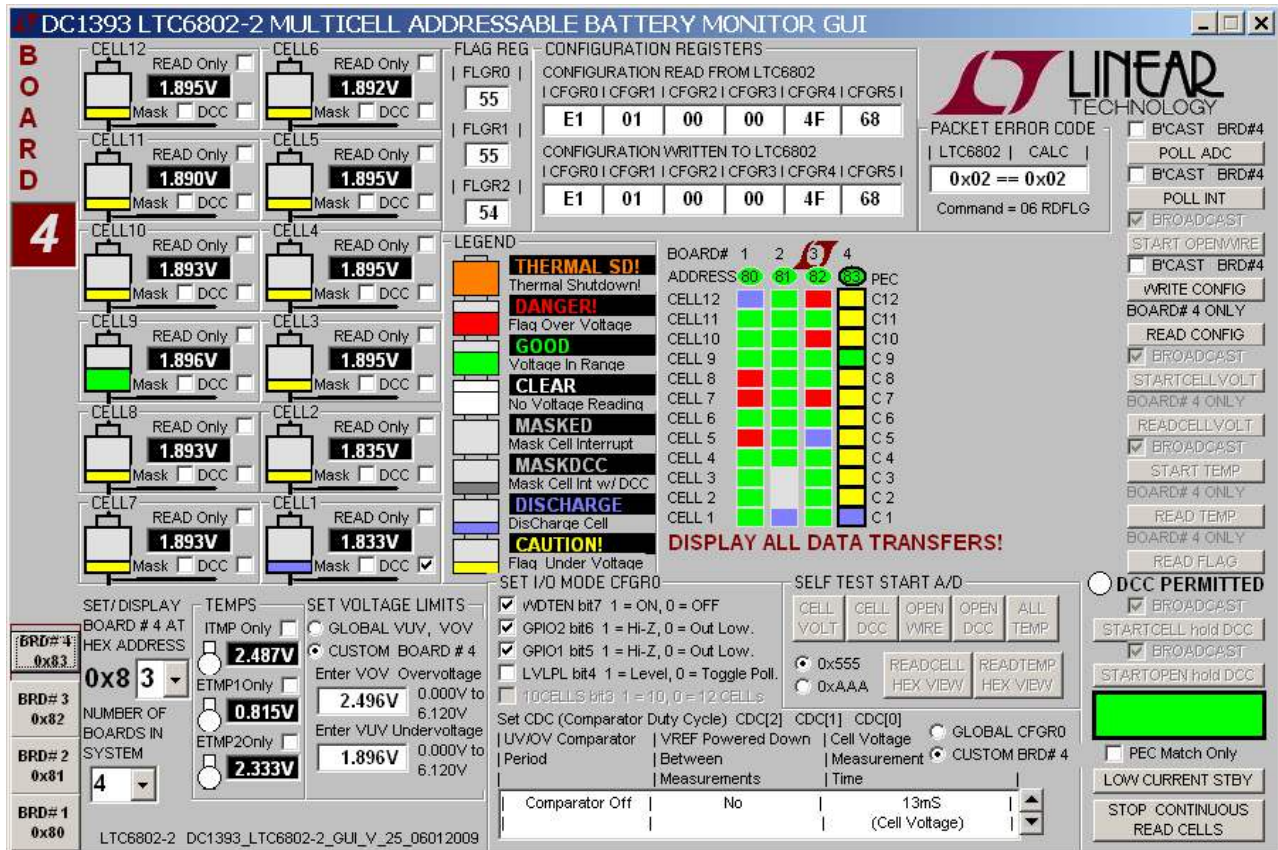
The color-coded status panel will expand to include all boards connected in a stack. Each small square in this array represents an individual battery in the stack of boards. The intent of this display is to provide a way to see the status of all cells at a glance. The meaning of the colors used is explained in the legend on the screen.

Any grayed box indicates that the cell's interrupt flag has been masked so the LTC6802-2 is no longer reporting this status. The cell voltage value measured for this cell however is still accurate.

The GUI is quite sophisticated with many adjustable variables. As items are changed displayed values may become stale or inaccurate. These will be flagged by either being cleared or grayed. If an item looks different, repeat the desired command to refresh the displayed information.

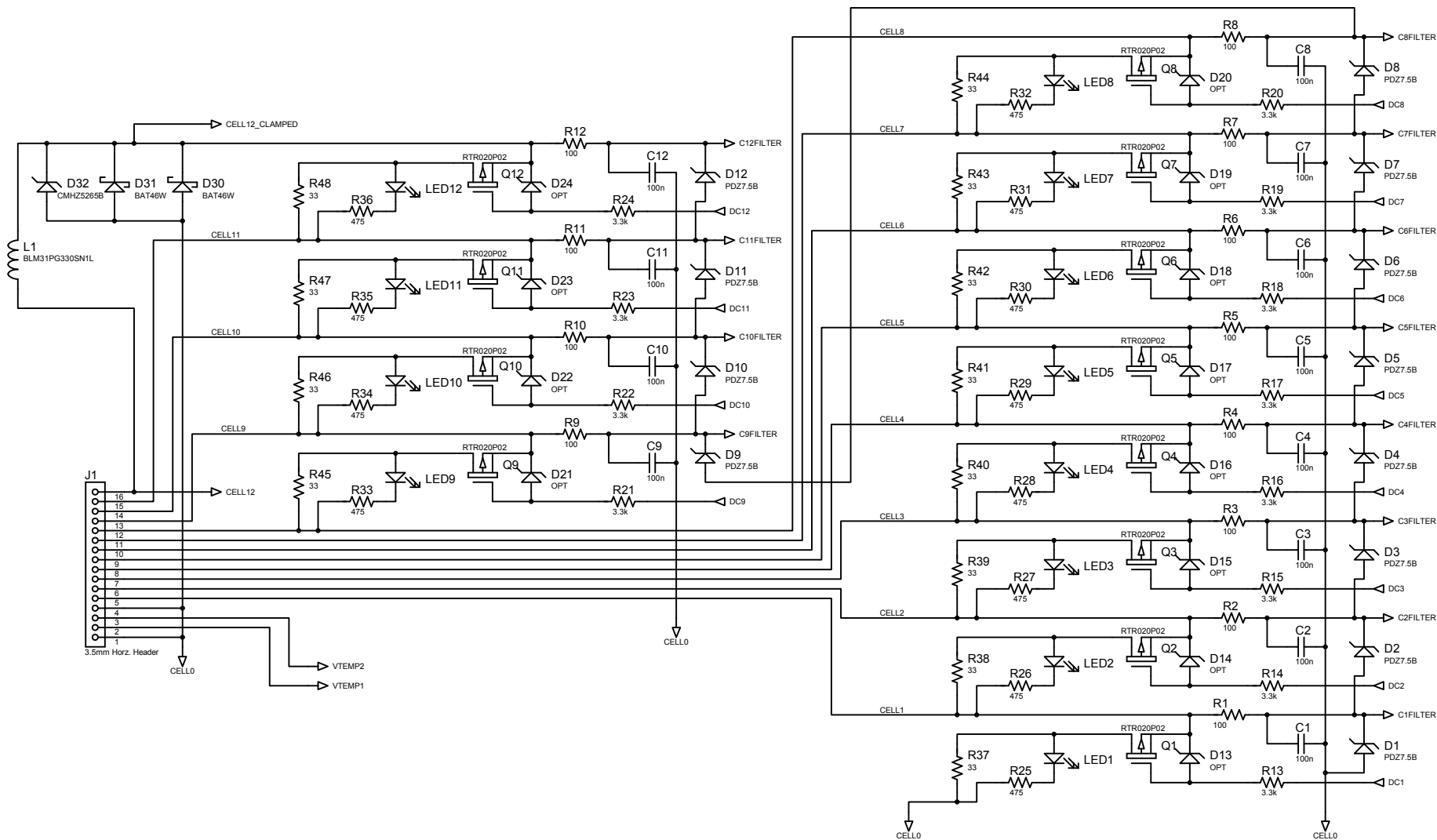
The schematics for DC1393B follow. Consult the data sheet for detailed information on the operation of the LTC6802-2 battery monitoring system.

Figure 3. GUI screenshot showing 4 boards being continuously monitored. Cell voltages and Temperature readings are for board 4 only. Color-coded cell status is for all boards connected.



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS
2. ALL CAPACITORS ARE IN FARADS
3. INSTALL SHUNTS AT JP1-JP9 AS SHOWN



Customer Notice:

Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customer's responsibility to verify proper and reliable operation in the actual application. Component substitution and printed circuit layout may significantly affect circuit performance or reliability. Contact Linear Technology Applications Engineering for assistance.

This circuit is proprietary to Linear Technology and supplied for use with Linear Technology parts.



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DC1393B DEMO BOARD SCHEMATIC

LTC6802IG-2 12-CELL BATTERY MONITOR

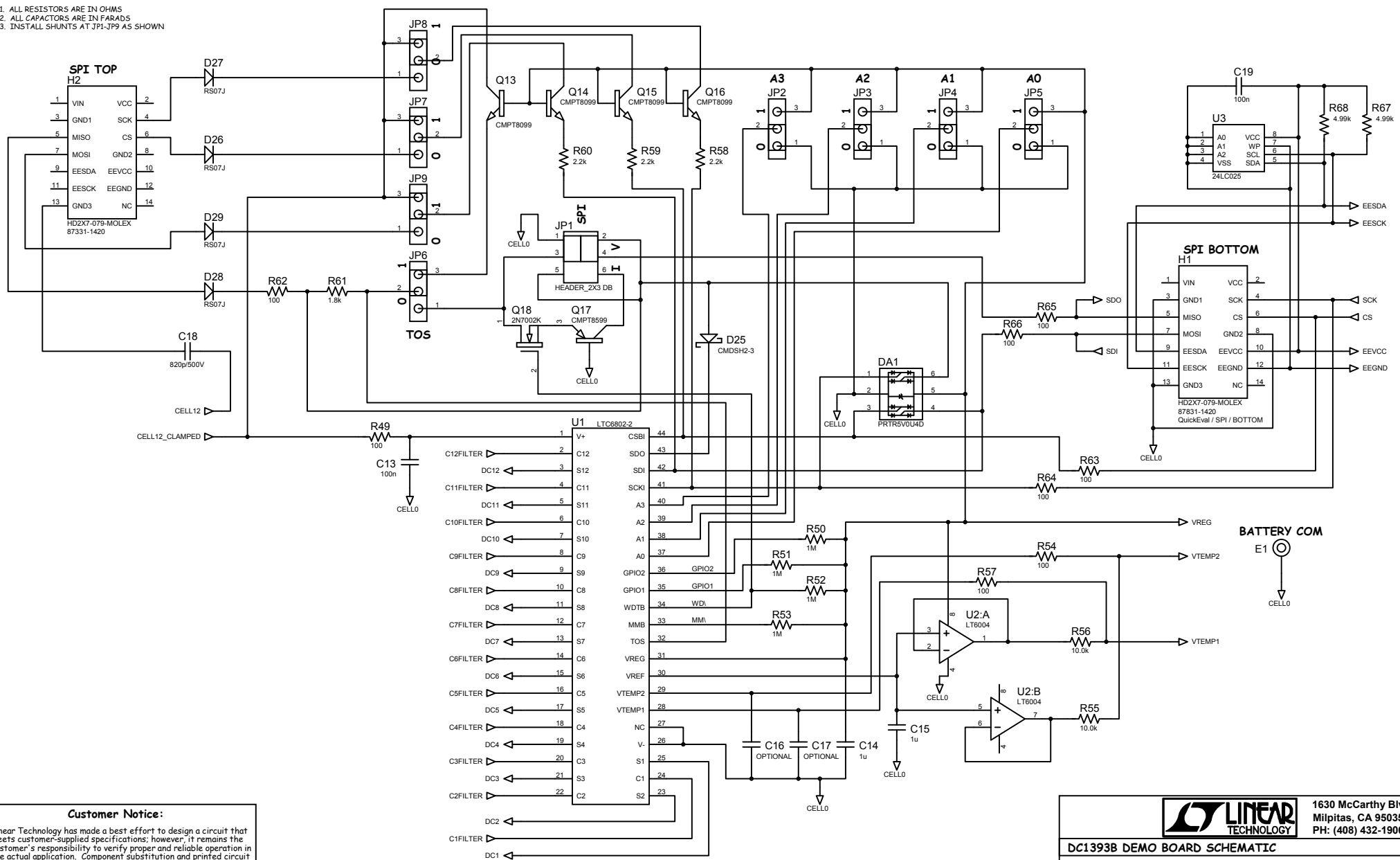
Sourcefile: DC1393B.DSN

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SH 1 of 3

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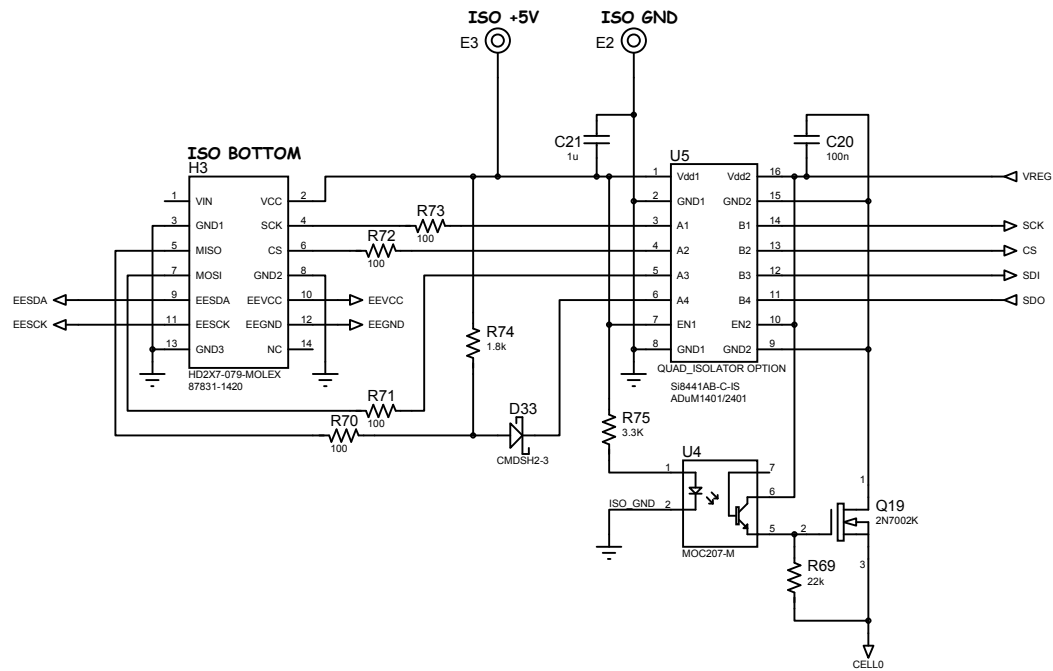
Sourcefile: DC1393B.DSN

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SH 2 of 3

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