

FJA4210

PNP Epitaxial Silicon Transistor

- Audio Power Amplifier
- High Current Capability : $I_C = -10A$
- High Power Dissipation
- Wide S.O.A
- Complement to FJA4310



Absolute Maximum Ratings* $T_a = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{CBO}	Collector-Base Voltage	-200	V
V_{CEO}	Collector-Emitter Voltage	-140	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current (DC)	-10	A
I_B	Base Current (DC)	-1.5	A
P_C	Collector Dissipation ($T_C = 25^\circ\text{C}$)	100	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature	-55 ~ 150	$^\circ\text{C}$

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Electrical Characteristics* $T_a = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
BV_{CBO}	Collector-Base Breakdown Voltage	$I_C = -5\text{mA}, I_E = 0$	-200			V
BV_{CEO}	Collector-Emitter Breakdown Voltage	$I_C = -50\text{mA}, R_{BE} = \infty$	-140			V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_E = -5\text{mA}, I_C = 0$	-6			V
I_{CBO}	Collector Cut-off Current	$V_{CB} = -200\text{V}, I_E = 0$			-10	μA
I_{EBO}	Emitter Cut-off Current	$V_{EB} = -6\text{V}, I_C = 0$			-10	μA
h_{FE}	* DC Current Gain	$V_{CE} = -4\text{V}, I_C = -3\text{A}$	50		180	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -5\text{A}, I_B = -0.5\text{A}$			-0.5	V
C_{ob}	Output Capacitance	$V_{CB} = -10\text{V}, f = 1\text{MHz}$		400		pF
f_T	Current Gain Bandwidth Product	$V_{CE} = -5\text{V}, I_C = -1\text{A}$		30		MHz

* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

h_{FE} Classification

Classification	R	O	Y
h_{FE}	50 ~ 100	70 ~ 140	90 ~ 180

Typical Characteristics

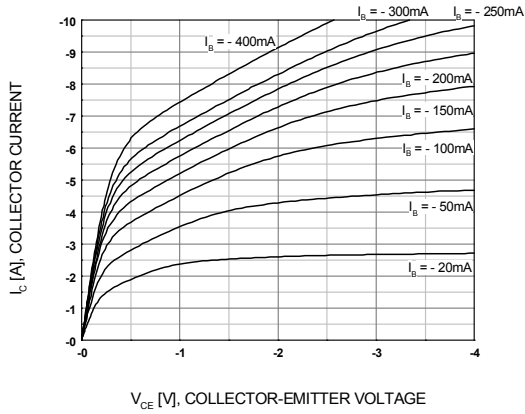


Figure 1. Static Characteristic

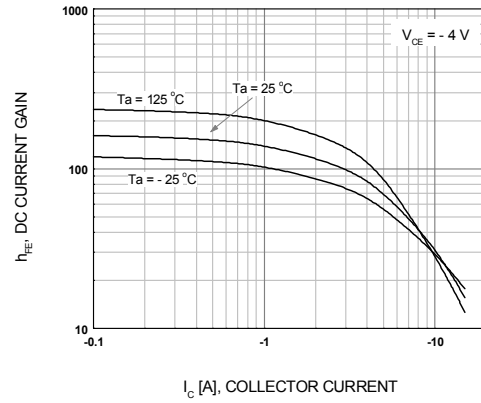


Figure 2. DC current Gain

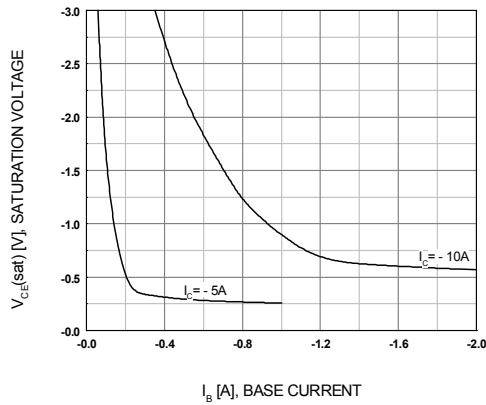


Figure 3. $V_{CE(sat)}$ vs. I_B Characteristics

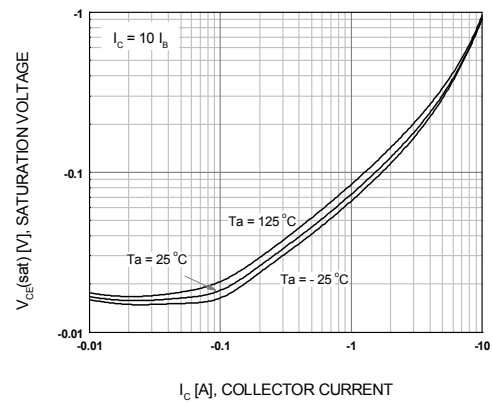


Figure 4. Collector-Emitter Saturation Voltage

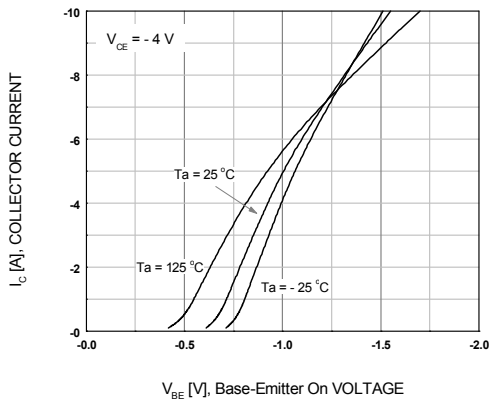


Figure 5. Base-Emitter On Voltage

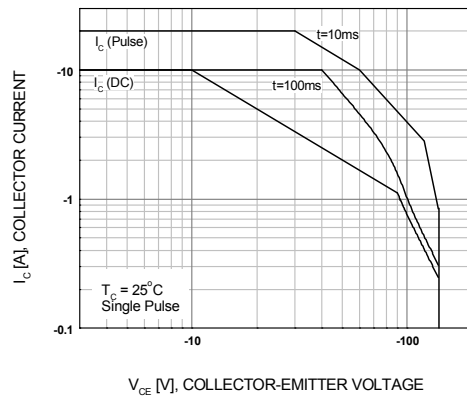


Figure 6. Forward Bias Safe Operating Area

Typical Characteristics (Continued)

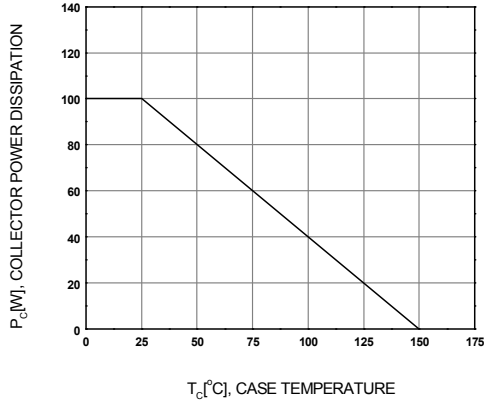
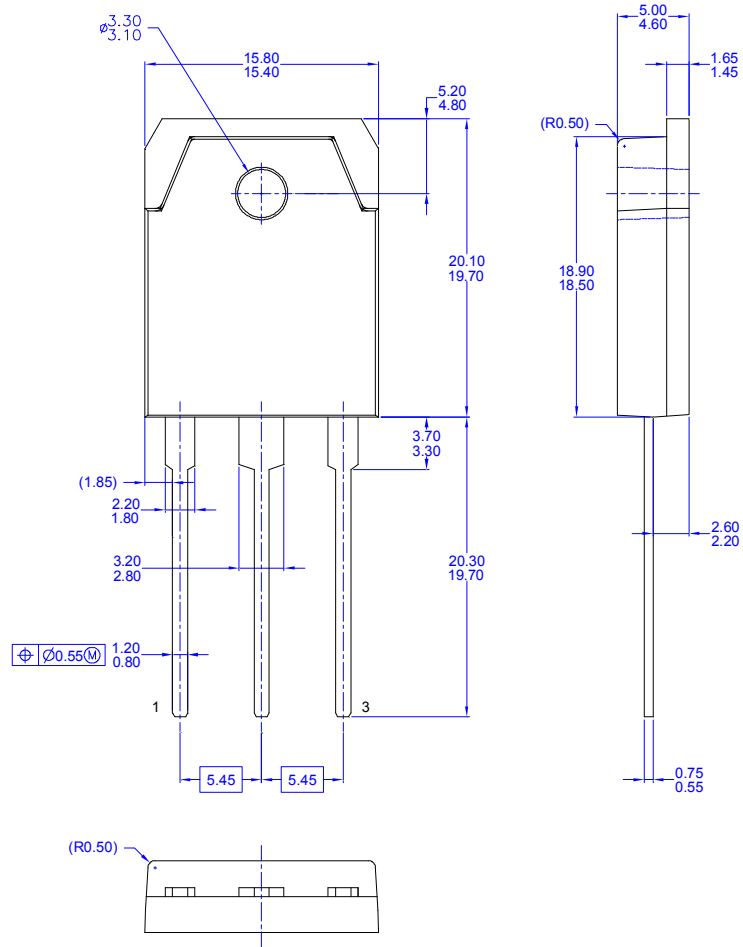


Figure 7. Power Derating

Package Dimension (TO-3P)






NOTES:

- A) THIS PACKAGE CONFORMS TO EIAJ SC-65 PACKAGING STANDARD.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONING AND TOLERANCING PER ASME14.5 1973.
- D) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- E) DRAWING FILE NAME: TO3P03AREV2.



TRADEMARKS

The following are registered and unregistered trademarks and service marks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEX®	Green FPS™	Power247®	SuperSOT™-8
Build it Now™	Green FPS™ e-Series™	POWEREDGE®	SyncFET™
CorePLUS™	GTO™	Power-SPM™	The Power Franchise®
CROSSVOLT™	i-Lo™	PowerTrench®	
CTL™	IntelliMAX™	Programmable Active Droop™	TinyBoost™
Current Transfer Logic™	ISOPLANAR™	QFET®	TinyBuck™
EcoSPARK®	MegaBuck™	QS™	TinyLogic®
 Fairchild®	MICROCOUPLER™	QT Optoelectronics™	TINYOPTO™
Fairchild Semiconductor®	MicroFET™	Quiet Series™	TinyPower™
FACT Quiet Series™	MicroPak™	RapidConfigure™	TinyPWM™
FACT®	MillerDrive™	SMART START™	TinyWire™
FAST®	Motion-SPM™	SPM®	µSerDes™
FastvCore™	OPTOLOGIC®	STEALTH™	UHC®
FPS™	OPTOPLANAR®	SuperFET™	UniFET™
FRFET®	 PDP-SPM™	SuperSOT™-3	VCX™
Global Power Resource SM	Power220®	SuperSOT™-6	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.