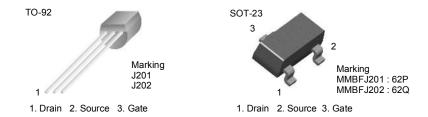


January 2008

# J201 - J202 / MMBFJ201 - MMBFJ203 **N-Channel General Purpose Amplifier**

- · This device is designed primarily for low level audio and general purpose applications with high impedance signal sources.
- Sourced from Process 52.



## Absolute Maximum Ratings \* Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	40	V
$V_{GS}$	Gate-Source Voltage	-40	V
I <sub>GF</sub>	Forward Gate Current	50	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired. NOTES:

## Thermal Characteristics\* Ta=25°C unless otherwise noted

Symbol	Parameter	Va	Units	
		J201 - J202	MMBFJ201 - MMBFJ203	
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	W mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

<sup>\*</sup> Device mounted on FR-4 PCB 1.6" x 1.6" x 0.06"

<sup>1)</sup> These ratings are based on a maximum junction temperature of 150°C.

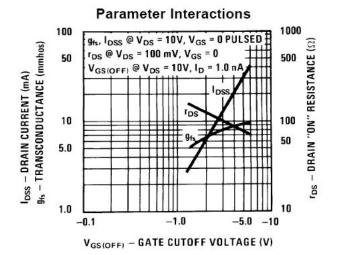
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

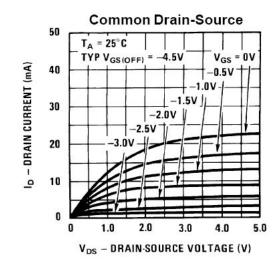
# **Electrical Characteristics \*** $T_C = 25^{\circ}C$ unless otherwise noted

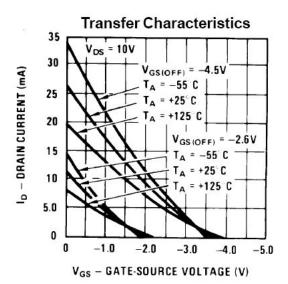
Symbol	Parameter	Conditions		Min.	Max	Units
Off Charact	Off Characteristics					
$V_{(BR)GSS}$	Gate-Source Breakdwon Voltage	$I_G = -1 \mu A, V_{DS} = 0$		-40		V
I <sub>GSS</sub>	Gate Reverse Current	V <sub>GS</sub> = -20V, V <sub>DS</sub> = 0			-100	pA
V <sub>GS</sub> (off)	Gate-Source Cutoff Voltage	V <sub>DS</sub> = 20V, I <sub>D</sub> = 10nA	201 202 203	-0.3 -0.8 -2	-1.5 -4 -10	V
On Charact	On Characteristics					
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current *	$V_{DS} = 20V, I_{GS} = 0$	201 202 203	0.2 0.9 4	1.0 4.5 20	mA
Small Signal Characteristics						
y <sub>FS</sub>	Forward Transfer Admittance	V <sub>DS</sub> = 20V, f = 1.0kHz	201 202 203	500 1000 1500		μmhos

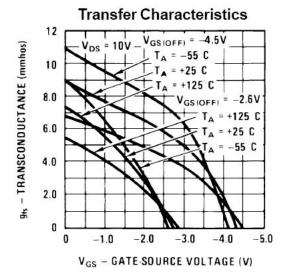
<sup>\*</sup> Pulse Test: Pulse Width  $\leq$  300ms, Duty Cycle  $\leq$  2.0%

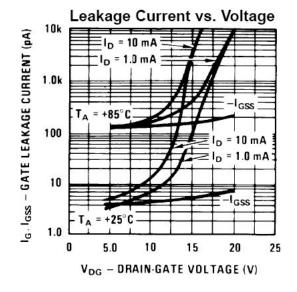
## **Typical Characteristics**

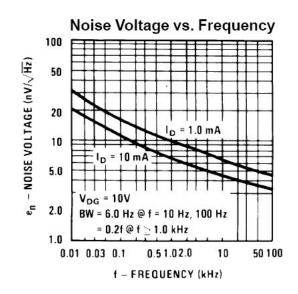




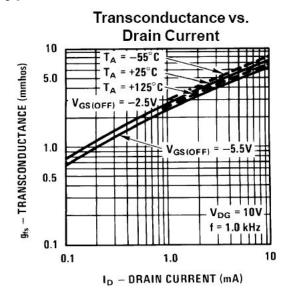


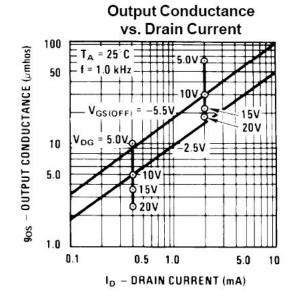


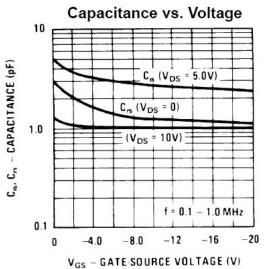


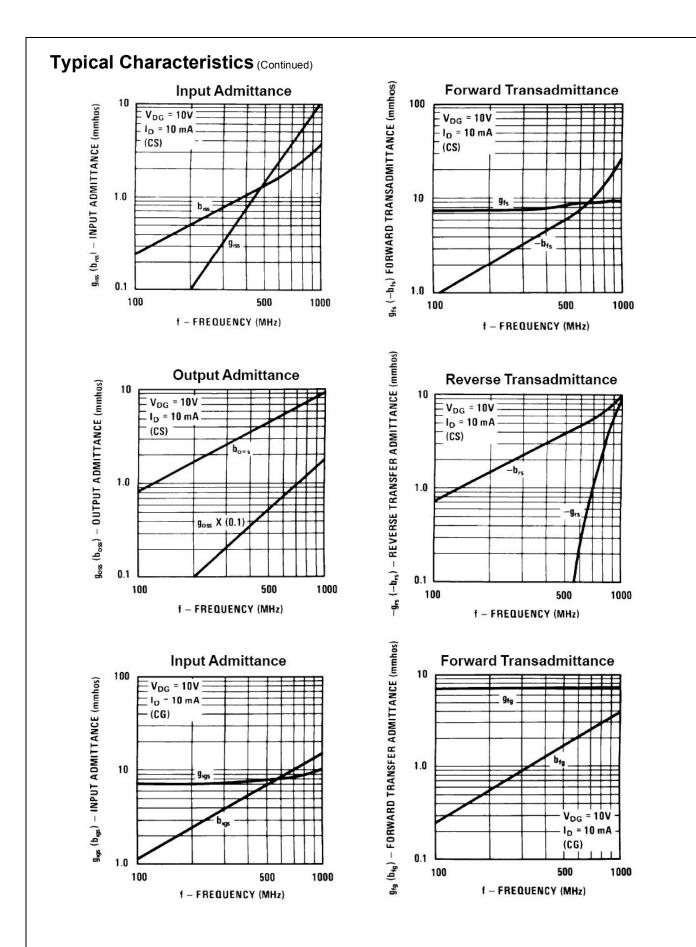


## Typical Characteristics (Continued)



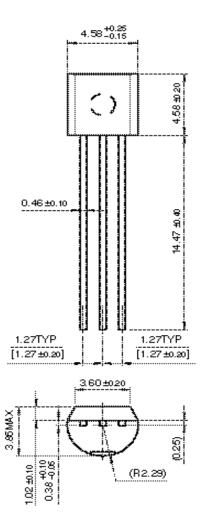


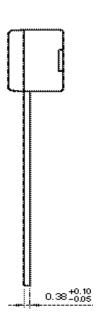




### **Mechanical Dimensions**

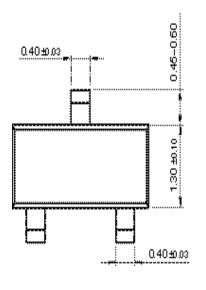
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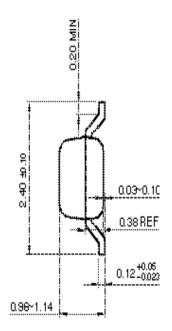


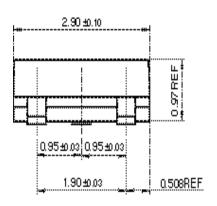


### **Mechanical Dimensions**

# SOT-23







Dimensions in Millimeters





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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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