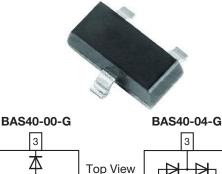
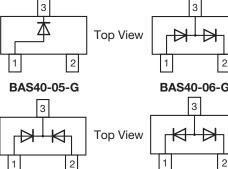
# BAS40-00-G to BAS40-06-G

**Vishay Semiconductors** 

# Small Signal Schottky Diodes, Single and Dual



www.vishay.com



### **DESIGN SUPPORT TOOLS** click logo to get started



# BAS40-06-G

### • These devices are protected by a PN junction guardring against excessive voltage, such as

and fast switching

**FEATURES** 

• AEC-Q101 qualified available (part number on request)

electrostatic discharges

- Base P/N-G3 green, commercial grade
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

· These diodes feature very low turn-on voltage

### **MECHANICAL DATA**

Case: SOT-23

Weight: approx. 8.1 mg

### Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAS40-00-G	BAS40-00-G3-08 or BAS40-00-G3-18	Single	43G	Tape and reel	
BAS40-04-G	BAS40-04-G3-08 or BAS40-04-G3-18	Dual serial	44G		
BAS40-05-G	BAS40-05-G3-08 or BAS40-05-G3-18	Common cathode	45G		
BAS40-06-G	BAS40-06-G3-08 or BAS40-06-G3-18	Common anode	46G		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Repetitive peak reverse voltage		$V_{RRM} = V_{RWM} = V_{R}$	40	V	
Forward continuous current <sup>(1)</sup>		IF	200	mA	
Surge forward current <sup>(1)</sup>	t <sub>p</sub> < 1 s	I <sub>FSM</sub>	600	mA	
Power dissipation <sup>(1)</sup>		P <sub>tot</sub>	200	mW	

### Note

<sup>(1)</sup> Device on fiberglass substrate, see layout on next page

<b>THERMAL CHARACTERISTICS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)					
PARAMETER	TEST CONDITION	T CONDITION SYMBOL		UNIT	
Thermal resistance junction to ambient air <sup>(1)</sup>		R <sub>thJA</sub>	500	K/W	
Junction temperature		Тj	125	°C	
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C	
Operating temperature range		T <sub>op</sub>	-55 to +125	°C	

### Note

<sup>(1)</sup> Device on fiberglass substrate, see layout on next page

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RoHS

COMPLIANT HALOGEN

FREE

<u>GREEN</u>

(5-2008)

# BAS40-00-G to BAS40-06-G



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# **Vishay Semiconductors**

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I <sub>R</sub> = 10 μA (pulsed)	V <sub>(BR)</sub>	40			V
Leakage current	V <sub>R</sub> = 30 V	I <sub>R</sub>		20	100	nA
Forward voltage	I <sub>F</sub> = 1 mA	V <sub>F</sub>			380	mV
Forward voltage <sup>(1)</sup>	I <sub>F</sub> = 40 mA	V <sub>F</sub>			1000	mV
Diode capacitance	$V_{R} = 0 V, f = 1 MHz$	CD		4	5	pF
Reverse recovery time	$I_F = I_R = 10$ mA, $i_R = 1$ mA, $R_L = 100$ $\Omega$	t <sub>rr</sub>			5	ns

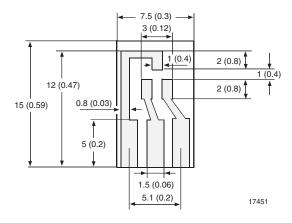
### Note

<sup>(1)</sup> Pulse test  $t_p < 300 \ \mu s$ 

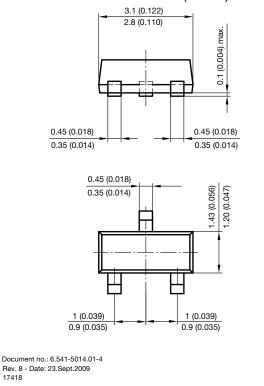
## LAYOUT FOR R<sub>thJA</sub> TEST

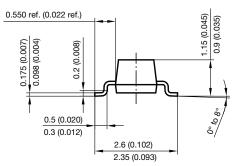
### Thickness:

Fiberglass 1.5 mm (0.059 inches) Copper leads 0.3 mm (0.012 inches)

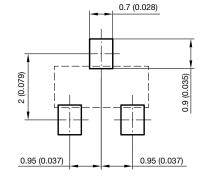


### PACKAGE DIMENSIONS in millimeters (inches): SOT-23





Foot print recommendation:



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Document Number: 85156

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



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