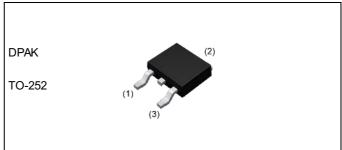


$V_{DSS}$	800V
R <sub>DS(on)</sub> (Max.)	8.7Ω
I <sub>D</sub>	±1.0A
$P_D$	36W

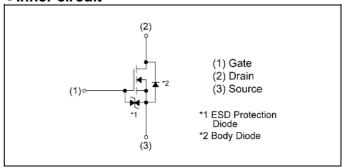
### Outline



### Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Drive circuits can be simple
- 4) Pb-free plating; RoHS compliant
- 5) AEC-Q101 qualified

## Inner circuit



Packaging specifications

or ackaging specifications							
	Packing	Embossed Tape					
	Reel size (mm)	330					
Туре	Tape width (mm)	16					
	Quantity (pcs)	2500					
	Taping code	TL					
	Marking	R8001CND3					

## Application

**Switching Power Supply** 

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	800	V
Continuous drain current (T <sub>c</sub> = 25°C)	I <sub>D</sub> *1	±1.0	А
Pulsed drain current	I <sub>DP</sub> *2	±4.0	А
Gate - Source voltage	V <sub>GSS</sub>	±30	V
Avalanche current, single pulse	I <sub>AS</sub> *3	0.5	А
Avalanche energy, single pulse	E <sub>AS</sub> *3	0.066	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub> *4	36	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

### ●Thermal resistance

Dougnoston	Currele e l	Values			1.1
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *4	-	-	3.44	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *5	-	-	100	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		1	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 800V, V <sub>GS</sub> = 0V	-	-	100	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 25V, V_{DS} = 0V$	-	-	±10	μA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	3.5	-	5.5	V
Static drain - source on - state resistance	R <sub>DS(on)</sub> *6	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.5A	-	6.7	8.7	Ω
Gate resistance R <sub>G</sub> f = 1MHz, open drain		-	7.2	-	Ω	

## ● Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	60	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 25V	-	70	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	9	-	
Turn - on delay time	t <sub>d(on)</sub> *6	V <sub>DD</sub> ≈ 400V, V <sub>GS</sub> = 10V	-	20	-	
Rise time	t <sub>r</sub> *6	I <sub>D</sub> = 0.5A	-	21	-	
Turn - off delay time	t <sub>d(off)</sub> *6	$R_L \simeq 800\Omega$	-	33	-	ns
Fall time	t <sub>f</sub> *6	$R_G = 10\Omega$	-	137	-	

## ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*6}$	V <sub>DD</sub> ≈ 400V	-	7.2	-	
Gate - Source charge	Q <sub>gs</sub> *6	I <sub>D</sub> = 1.0A	-	2.4	-	nC
Gate - Drain charge	${\sf Q_{gd}}^{*6}$	V <sub>GS</sub> = 10V	-	3.9	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> ≈ 400V, I <sub>D</sub> = 1.0A	-	8.2	-	V

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L  $\simeq$  500 $\mu$ H, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25 $\Omega$ , starting T<sub>j</sub> = 25°C Fig.3-1,3-2

<sup>\*4</sup> T<sub>c</sub>=25°C

<sup>\*5</sup> Mounted on an epoxy PCB FR4 (20×20×0.8mm)

<sup>\*6</sup> Pulsed

## ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Source current	I <sub>S</sub> *1	T - 25°C	1	-	1.0	Α	
Pulsed source current	I <sub>SP</sub> *2	T <sub>C</sub> = 25°C	1	-	4.0	Α	
Source-Drain voltage	V <sub>SD</sub> *6	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1.0A	-	-	1.5	V	
Reverse recovery time	t <sub>rr</sub> *6		-	460	-	ns	
Reverse recovery charge	erse recovery charge $Q_{rr}^{*6}$ $I_S = 1.0A$ $di/dt = 100A/\mu s$	-	1.6	-	μC		
Peak reverse recovery current	<sub>rr</sub> *6	·	-	7.1	-	Α	

Fig.1 Power Dissipation Derating Curve

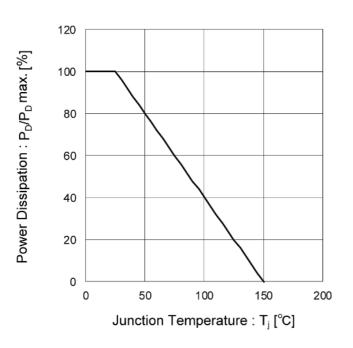


Fig.2 Drain Current Derating Curve

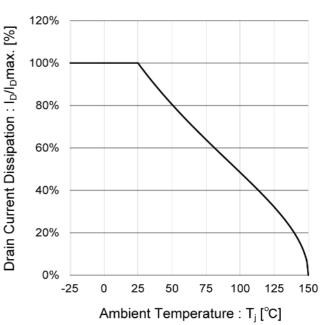


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

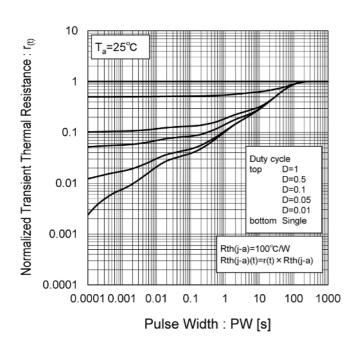


Fig.4 Maximum Safe Operating Area

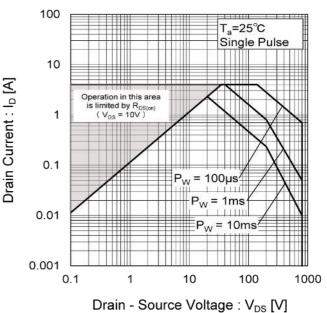


Fig.5 Avalanche Energy Derating Curve

120 Avalanche Energy: EAS / EAS max [%] 100 80 60 40 20 0 0 25 50 75 100 125 150 175 Junction Temperature : T<sub>j</sub> [°C]

Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

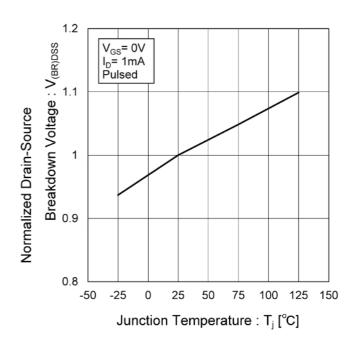


Fig.7 Output Characteristics(I)

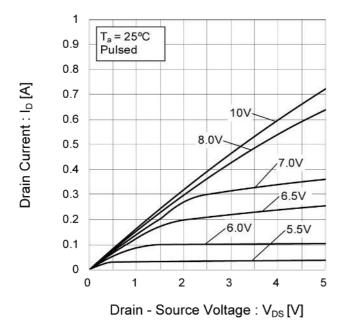
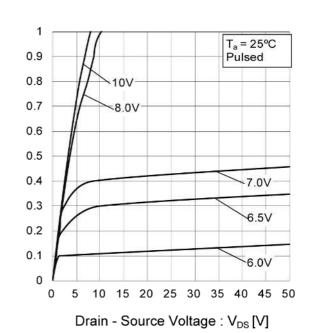


Fig.8 Output Characteristics(II)



Drain Current: Ip [A]

Fig.9 Gate Threshold Voltage vs. Drain Current

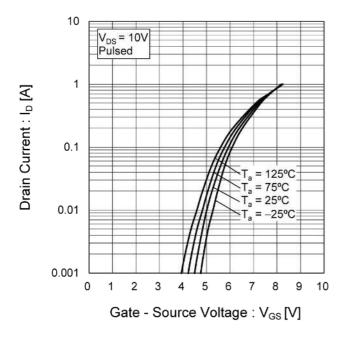


Fig.10 Normalized Gate Threshold

Voltage vs. Junction Temperature

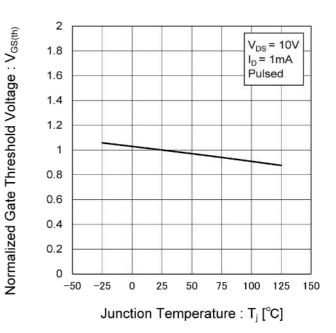


Fig.11 Static Drain - Source On - State Resistance vs. Drain Current

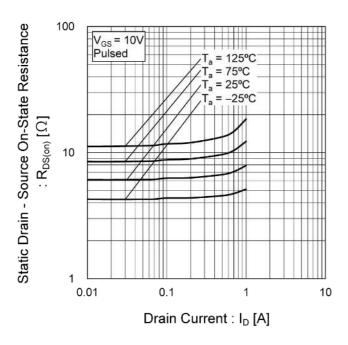


Fig.12 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

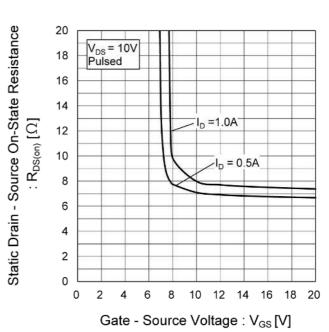


Fig.13 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

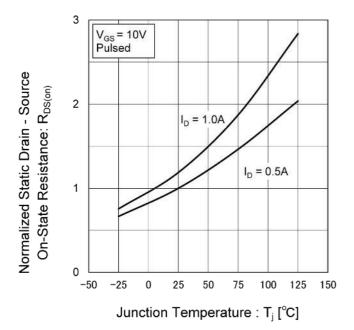


Fig.14 Capacitances

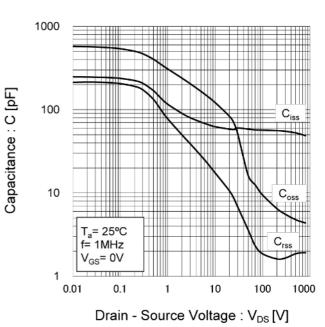


Fig.15 Switching Times

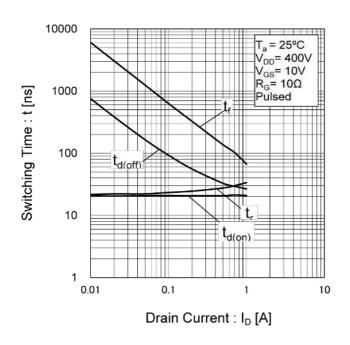
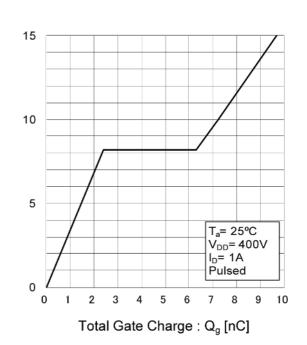


Fig.16 Gate Charge



Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.17 Source Current vs. Source - Drain Voltage

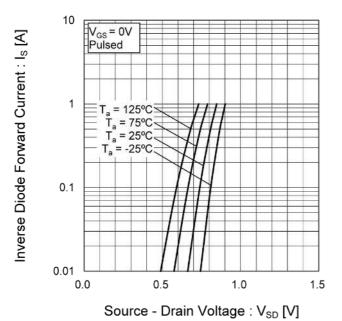
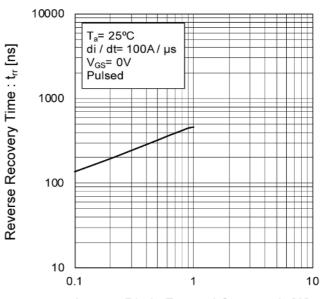


Fig.18 Reverse Recovery Time vs. Source Current



Inverse Diode Forward Current : I<sub>S</sub> [A]

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

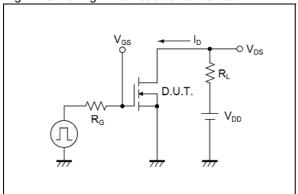


Fig.2-1 Gate Charge Measurement Circuit

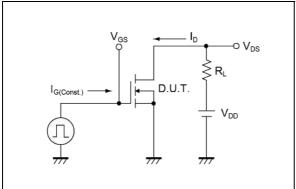


Fig.3-1 Avalanche Measurement Circuit

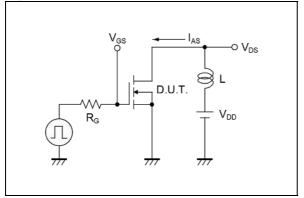


Fig.4-1 trr Measurement Circuit

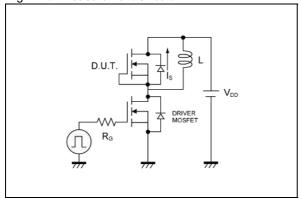


Fig.1-2 Switching Waveforms

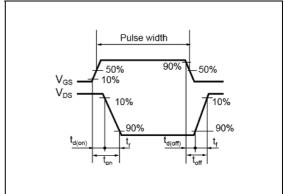


Fig.2-2 Gate Charge Waveform

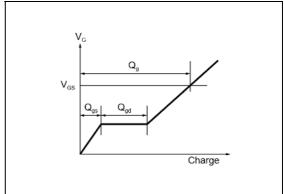


Fig.3-2 Avalanche Waveform

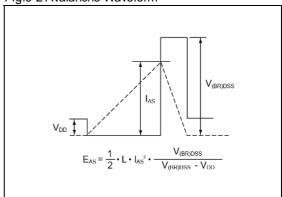
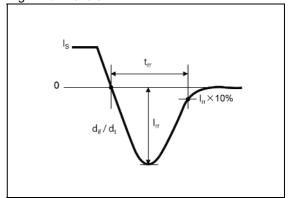
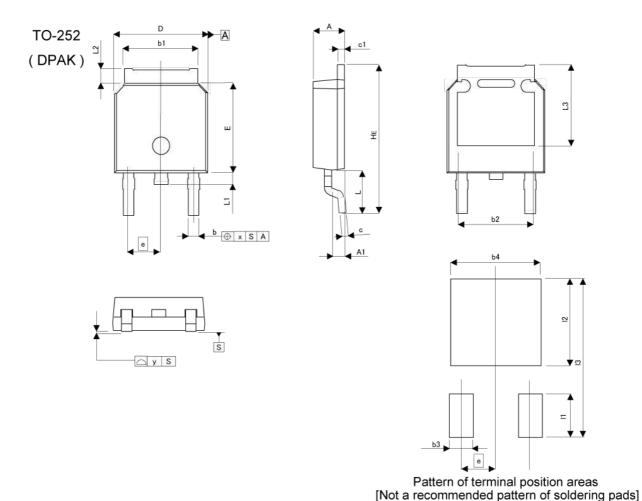


Fig.4-2 trr Waveform



## Dimensions



**MILIMETERS INCHES** DIM MIN MAX MIN MAX 0.083 2.10 2.30 0.091 Α A1 0.70 1.10 0.028 0.043 b 0.65 0.85 0.026 0.033 0.213 5.10 0.201 5.40 b1 b2 5.10 0.201 0.40 0.60 0.016 0.024 C 0.016 0.40 0.60 0.024 c1 0.252 D 6.40 6.80 0.268 е 6.00 0.236 0.252 6.40 E HE 9.50 10.50 0.374 0.413 0.114 0.70 0.028 0.035 L1 0.90 0.70 0.028 L2 1.30 0.051 L3 0.10 0.004 X 0.10 0.004

DIM -	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
b3	25	1.10	7/27	0.043
b4	+	5.40	3,53	0.213
11	20	2.90	Nay	0.114
12	*	5.50	0.50	0.217
13	<u>~</u>	10.50	1524	0.413

Dimension in mm/inches

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	JAPAN	USA	EU	CHINA
	CLASSⅢ	CLASSⅢ	CLASSIIb	CLASSⅢ
	CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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  - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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