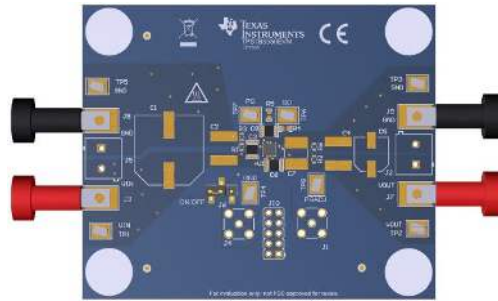


# TPS7B8550EVM Evaluation Module



**Figure 1. TPS7B8550EVM Evaluation Module**

This user's guide describes the operational use of the TPS7B8550EVM evaluation module (EVM) as a reference design for engineering demonstration and evaluation of the TPS7B8550 low-dropout linear regulator (LDO). Included in this user's guide are setup and operating instructions, thermal and layout guidelines, a printed-circuit board (PCB) layout, a schematic diagram, and a bill of materials (BOM).

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## 1 Introduction

Texas Instruments' TPS7B8550EVM helps design engineers evaluate the operation and performance of the TPS7B8550 family of linear regulators for possible use in their own circuit application. This particular EVM configuration contains a single wide input voltage, small size, linear regulator for a wide range of applications. The regulator is capable of delivering up to 150 mA to the load with low  $V_{IN}$  to  $V_{OUT}$  dropout voltage. For stability, use a minimum derated capacitance of 1- $\mu$ F (or larger) output capacitor.

## 1.1 Before You Begin

The following warnings and cautions are noted for the safety of anyone using or working close to the TPS7B8550EVM. Observe all safety precautions.

**Warning**

Warning Hot surface. Contact may cause burns. Do not touch.

**CAUTION**

The circuit module can be damaged by overtemperature. To avoid damage, monitor the temperature during evaluation and provide cooling, as needed, for your system environment.

**CAUTION**

Some power supplies can be damaged by application of external voltages. If you are using more than one power supply, check your equipment requirements and use blocking diodes or other isolation techniques, as needed, to prevent damage to your equipment.

**CAUTION**

The circuit module is not a finished product or electrical appliance. The module does not contain current or voltage thresholds for circuit protection. It must be used by qualified personnel with additional equipment for evaluation only.

## 1.2 EVM Setup

This section describes how to properly connect and set up the TPS7B8550EVM, including the jumpers and connectors on the EVM board.

### 1.2.1 Inputs/Outputs Connectors and Jumper Descriptions

#### 1.2.1.1 J1 – OUT\_S

Regulated output voltage sense

#### 1.2.1.2 J2 – OUT

Regulated output power supply voltage screw terminal.

#### 1.2.1.3 J3 – IN

Input voltage banana connector

#### 1.2.1.4 J4 – IN\_S

Input voltage sense

#### 1.2.1.5 J5 –IN\_ST

Input supply voltage screw terminal. Twist together the positive input lead and ground return lead from the input power supply, and keep them as short as possible to minimize input inductance.

#### 1.2.1.6 J6 – EN

Output voltage enable. To enable the output voltage, connect the jumper to short VI to EN.

#### 1.2.1.7 J7 – OUT

Regulated output voltage banana connector

#### 1.2.1.8 J8 – GND

Input supply ground return banana connector

#### 1.2.1.9 J9 – GND

Regulated output voltage return banana connector

#### 1.2.1.10 J10 – Test Hookup

Intended for test purposes only

#### 1.2.1.11 TP1 – IN

Input voltage test point

#### 1.2.1.12 TP2 – OUT

Regulated output voltage test point

#### 1.2.1.13 TP3 – GND

Ground test point

**1.2.1.14 TP4 – GND**

Ground test point

**1.2.1.15 TP5 – GND**

Ground test point

**1.2.1.16 TP6 – SO**

Sense output enable test point

**1.2.1.17 TP7 – PG**

Power good test point

**1.2.1.18 TP8 – PGADJ**

Power-good, threshold-adjustment test point

**1.2.2 Soldering Guidelines**

To avoid damaging the integrated circuit (IC), use a hot-air system for any solder rework to modify the EVM for the purpose of repair or other application reasons.

**1.2.3 Equipment Connection**

Connect the equipment as shown in the following steps:

1. Set the input power supply up to 40 V (max), and turn them off.
2. Connect the positive voltage lead from the input power supply one to V1 at the J3 connector of the EVM.
3. Connect the ground lead from the input power supply to GND at the J8 connector of the EVM.
4. Connect a 0-A to 150-mA load between VO and GND.
5. Disable output voltage by connecting J6 pin 2 (EN) to J6 pin 1 (GND).

## 2 PCB Layout

Figure 2 to Figure 6 illustrate the PCB layout for this EVM.

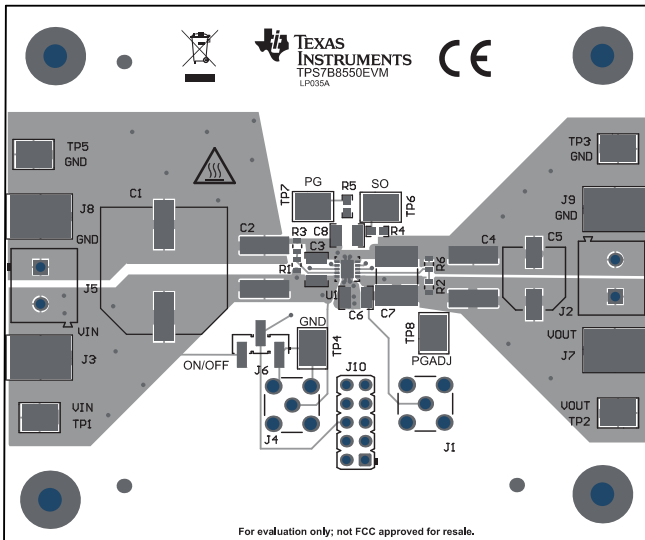


Figure 2. Assembly Layer

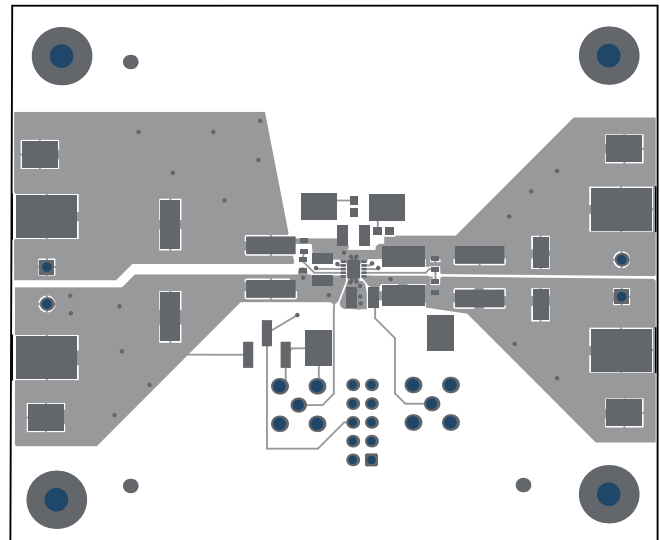


Figure 3. Top Layer Routing

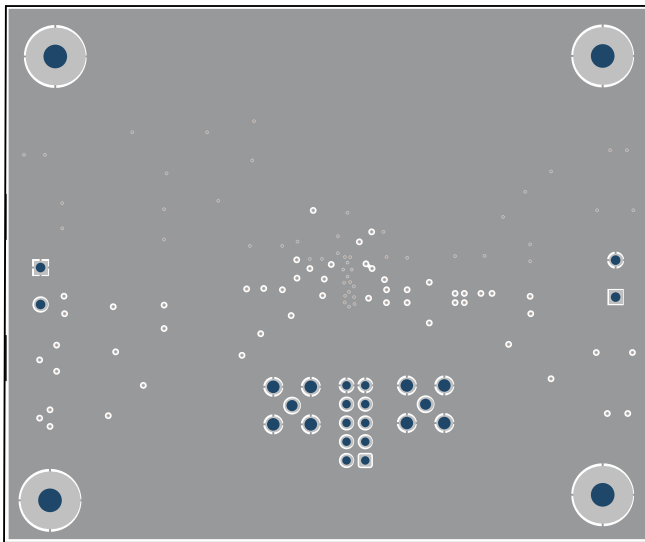


Figure 4. First Middle Layer

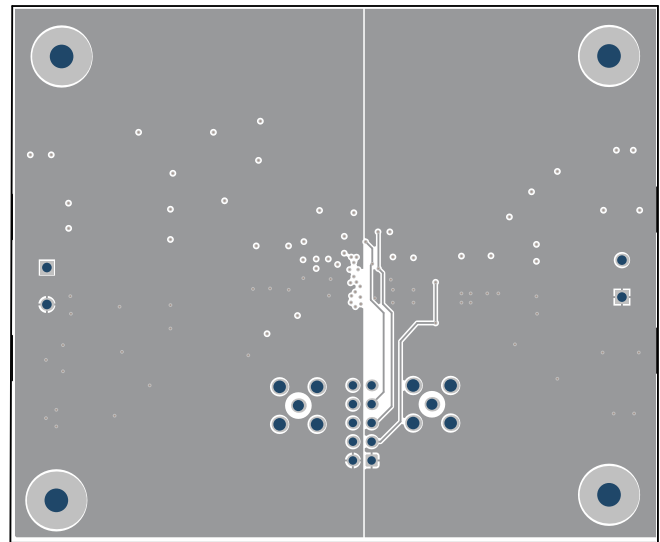


Figure 5. Second Middle Layer

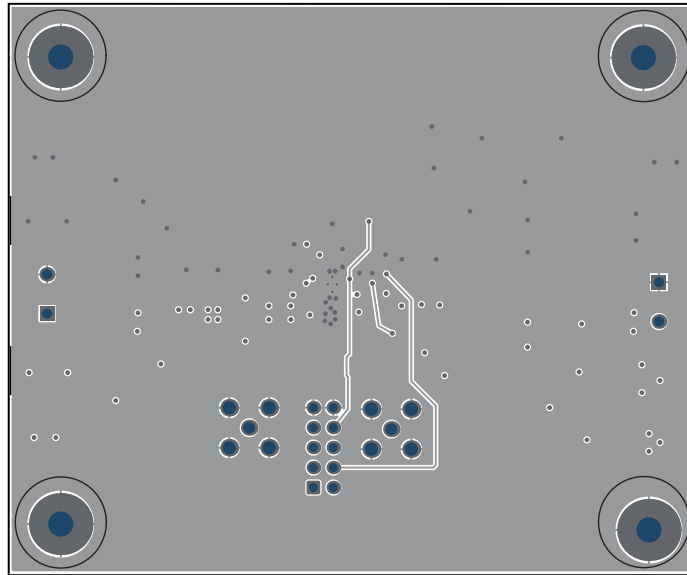


Figure 6. Bottom Layer Routing

### 3 Schematic

Figure 7 shows the schematic for the EVM.

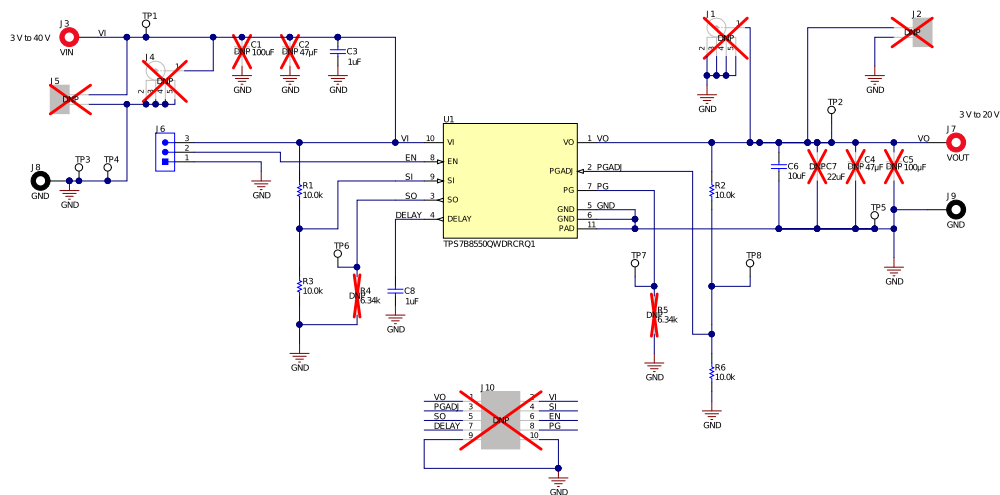


Figure 7. TPS7B8550EVM Schematic

## 4 Bill of Materials

Table 1 shows the BOM for this EVM.

**Table 1. TPS7B8550EVM BOM<sup>(1)(2)(3)(4)</sup>**

Designator	QTY	Value	Description	Package Reference	Part Number	Manufacturer	Alternate Part Number	Alternate Manufacturer
!PCB1	1		Printed Circuit Board		LP035A	Any		
C3, C8	2	1 $\mu$ F	CAP, CERM, 1 $\mu$ F, 100 V, $\pm$ 10%, X7R, 1210	1210	C3225X7R2A105K200AA	TDK		
C6	1	10 $\mu$ F	CAP, CERM, 10 $\mu$ F, 50 V, $\pm$ 10%, X7R, 1210	1210	GRM32ER71H106KA12L	MuRata		
H9, H10, H11, H12	4		Bumpon, Hemisphere, 0.44 x 0.20, Clear	Transparent Bumpon	SJ-5303 (CLEAR)	3M		
J3, J7	2		Standard Banana Jack, Insulated, Red	6091	6091	Keystone		
J6	1		Header, 2.54 mm, 3 x 1, Gold, SMT	Header, 2.54 mm, 3 x 1, SMT	TSM-103-01-L-SV-P-TR	Samtec		
J8, J9	2		Standard Banana Jack, Insulated, Black	6092	6092	Keystone		
R1, R2, R3, R6	4	10.0 k $\Omega$	RES, 10.0 k, 0.1%, 0.1 W, AEC-Q200 Grade 1, 0603	0603	TNPW060310K0BEEA	Vishay-Dale		
SH-J1	1	1 x 2	Shunt, 100-mil, Gold plated, Black	Shunt	SNT-100-BK-G	Samtec	969102-0000-DA	3M
TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8	8		Test Point, Compact, SMT	Testpoint_Keystone_Compact	5016	Keystone		
U1	1		150-mA, wide VIN, low IQ, low-dropout regulator, DRC0010R (VSON-10)	DRC0010R	TPS7B8550QWDRCRQ1	Texas Instruments		
C1	0	100 $\mu$ F	CAP, AL, 100 $\mu$ F, 100 V, $\pm$ 20%, 0.17 $\Omega$ , AEC-Q200 Grade 2, SMD	SMT Radial J16	EEV-FK2A101M	Panasonic		
C2, C4	0	47 $\mu$ F	CAP, TA, 47 $\mu$ F, 50 V, $\pm$ 10%, 0.24 $\Omega$ , SMD	6.2 x 6 mm	597D476X9050Z2T	Vishay-Sprague		
C5	0	100 $\mu$ F	CAP, AL, 100 $\mu$ F, 35 V, $\pm$ 20%, 0.2 $\Omega$ , AEC-Q200 Grade 1, SMD	D8xL10.2 mm	EEE-TC1V101P	Panasonic		
C7	0	22 $\mu$ F	CAP, CERM, 22 $\mu$ F, 50 V, $\pm$ 20%, X7R, Stacked 2220	Stacked 2220	KCM55WR71H226MH01K	MuRata		
FID1, FID2, FID3	0		Fiducial mark. There is nothing to buy or mount.	N/A	N/A	N/A		
H1, H2, H3, H4	0		Machine Screw, Round, #4-40 x 1/4, Nylon, Philips panhead	Screw	NY PMS 440 0025 PH	B&F Fastener Supply		
H5, H6, H7, H8	0		Standoff, Hex, 0.5"L #4-40 Nylon	Standoff	1902C	Keystone		
J1, J4	0		SMA Straight Jack, Gold, 50 $\Omega$ , TH	SMA Straight Jack, TH	901-144-8RFX	Amphenol RF		
J2, J5	0		Terminal Block, 5 mm, 2 x 1, Tin, TH	Terminal Block, 5 mm, 2 x 1, TH	691 101 710 002	Würth Elektronik		
J10	0		Header, 2.54 mm, 5x2, Gold, TH	Header, 2.54 mm, 5 x 2, TH	61301021121	Würth Elektronik		
R4, R5	0	6.34 k $\Omega$	RES, 6.34 k $\Omega$ , 1%, 0.1 W, 0603	0603	RC0603FR-076K34L	Yageo		

<sup>(1)</sup> These assemblies are ESD sensitive, observe ESD precautions.

<sup>(2)</sup> These assemblies must be clean and free from flux and all contaminants. Use of no-clean flux is not acceptable.

<sup>(3)</sup> These assemblies must comply with workmanship standards IPC-A-610 Class 2.

<sup>(4)</sup> Unless otherwise noted in the *Alternate Part Number* or *Alternate Manufacturer* columns, all parts can be substituted with equivalents.

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  - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.
  - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

### **WARNING**

**Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.**

**User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.**

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.



### 3 Regulatory Notices:

#### 3.1 United States

##### 3.1.1 Notice applicable to EVMs not FCC-Approved:

**FCC NOTICE:** This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

##### 3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

#### **CAUTION**

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### **FCC Interference Statement for Class A EVM devices**

*NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.*

#### **FCC Interference Statement for Class B EVM devices**

*NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:*

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

#### 3.2 Canada

##### 3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

#### **Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### **Concernant les EVMs avec appareils radio:**

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

#### **Concerning EVMs Including Detachable Antennas:**

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

#### 3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see [http://www.tij.co.jp/lstds/ti\\_ja/general/eStore/notice\\_01.page](http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page) 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。  
[http://www.tij.co.jp/lstds/ti\\_ja/general/eStore/notice\\_01.page](http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page)

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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#### 3.4 European Union

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

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#### 4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

#### 4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

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8. *Limitations on Damages and Liability:*

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