### Features

- Internal Level-Setting DACs
- Heat Sink Included for Safe Operation
- Windows 2000-, Windows XP-, and Windows Vista (32-Bit)-Compatible Software
- USB-PC Connection (Cable Included)
- Proven PCB Lavout

DESIGNATION

C53, C55,

C61-C68

C57, C58

D1

J1–J18

J20, J22, J24

J21

J23

JU1, JU2, JU3, JU15, JU16,

JU18–JU23. JU24 JU4–JU11

L1

Fully Assembled and Tested

### **Ordering Information**

PART	ТҮРЕ	
MAX19000EVKIT+	EV Kit	
+Denotes lead(Ph)-free and	RoHS compliant	

QTY

10

2

1

18

3

1

1

12

8

1

### **Component List**

DESCRIPTION 1µF ±10%, 16V X5R ceramic

Murata GRM188R61C105K 10pF ±5%, 50V C0G ceramic

Murata GRM1885C1H100J

Banana jacks, uninsulated

capacitors (0603)

capacitors (0603)

Red LED (0603)

receptacles

(panel jack)

2-pin headers

3-pin headers Ferrite bead

SMA end-launch jack

24-pin header (2 x 12) USB type-B right-angle

PC-mount receptacle

DESIGNATION	QTY	DESCRIPTION		
C1-C12	12	10nF ±10%, 25V X7R ceramic capacitors (0402) Murata GRM155R71E103K		
C13	1	10µF ±10%, 25V X5R ceramic capacitor (1206) Murata GRM31CR61E106K		
C14, C15, C16, C18, C20, C23, C24, C27	8	0.1µF ±10%, 25V X7R ceramic capacitors (0603) Murata GRM188R71E104K		
C17, C19, C21, C22, C25, C26	6	1μF ±10%, 25V X7R ceramic capacitors (0805) Murata GRM21BR71E105K		
C43, C54, C56	3	10µF ±20%, 16V X5R ceramic capacitors (1206) Murata GRM31CR61C106M		
C44, C45	2	22pF ±5%, 50V C0G ceramic capacitors (0603) Murata GRM1885C1H220J		
C46	1	0.033µF ±10%, 16V X5R ceramic capacitor (0603) Taiyo Yuden EMK107BJ333KA		
C47–C52, C59, C60	8	0.1µF 10%, 16V X7R ceramic capacitors (0603) TDK C1608X7R1C104K		
Windowo Window	VD on	d Windowa Vista ara ragistarad		

Wine trademarks of Microsoft Corp.

### **NVIXIVN**

Maxim Integrated Products 1

TDK MMZ1608R301A (0603)  $10\mu$ H  $\pm 10\%$ , 340m $\Omega$  inductors

C1-C12	12	10nF ±10%, 25V X7R ceramic capacitors (0402) Murata GRM155R71E103K
C13	1	10μF ±10%, 25V X5R ceramic capacitor (1206) Murata GRM31CR61E106K
4, C15, C16, 8, C20, C23, C24, C27	8	0.1µF ±10%, 25V X7R ceramic capacitors (0603) Murata GRM188R71E104K
17, C19, C21, 22, C25, C26	6	1µF ±10%, 25V X7R ceramic capacitors (0805) Murata GRM21BR71E105K
13, C54, C56	3	10μF ±20%, 16V X5R ceramic capacitors (1206) Murata GRM31CR61C106M
C44, C45	2	22pF ±5%, 50V C0G ceramic capacitors (0603) Murata GRM1885C1H220J
C46	1	0.033µF ±10%, 16V X5R ceramic capacitor (0603) Taiyo Yuden EMK107BJ333KA
17–C52, C59, C60	8	0.1µF 10%, 16V X7R ceramic capacitors (0603) TDK C1608X7R1C104K
dows, Windows	s XP, an	d Windows Vista are registered

**General Description** 

The MAX19000 evaluation kit (EV kit) is a fully assembled

and tested PCB that evaluates the MAX19000 dual-chan-

nel, high-performance pin electronics driver/comparator/

load (DCL) with built-in level-setters. The EV kit includes

SMA connections for the high-speed digital I/Os and

the MAX19000 pin-driver outputs. The MAX19000 EV kit is connected to the PC through the universal serial bus

(USB) port. The EV kit also includes Windows® 2000-,

Windows XP®-, and Windows Vista®-compatible software that provides a simple graphical user interface

(GUI) for exercising the features of the MAX19000.

L2, L3 2 (1210)Panasonic ELJ-EA100KF

valuates: MAX19000

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

Evaluates: MAX19000

	r	1
DESIGNATION	QTY	DESCRIPTION
R1, R2	2	1k $\Omega$ ±5% resistors (0603)
R3	1	$280\Omega \pm 1\%$ resistor (0603)
R4	1	2.26k $\Omega$ ±1% resistor (0603)
R5, R8	2	$402\Omega \pm 1\%$ resistors (0603)
R6	1	$75\Omega \pm 1\%$ resistor (0603)
R7	1	249 $\Omega$ ±1% resistor (0603)
R9, R18–R22	0	Not installed, resistors—shorted with PCB trace (0603)
R10, R11	2	$27\Omega \pm 5\%$ resistors (0603)
R12	1	1.5k $\Omega$ ±5% resistor (0603)
R13	1	$470\Omega \pm 5\%$ resistor (0603)
R14	1	2.2kΩ ±5% resistor (0603)
R15	1	10k $\Omega$ ±5% resistor (0603)
R16	1	169k $\Omega$ ±1% resistor (0603)
R17	1	100k $\Omega$ ±1% resistor (0603)
R23	1	$200\Omega$ SMT cermet trimmer, 9 to 15 turns
R24	1	220Ω ±5% resistor (0603)
R26–R29	4	243 $\Omega$ ±1% resistors (0603)
R32	1	1.5k $\Omega$ ±1% resistor (0603)
TMPSNS, TP1, TP2, TP3, TP12, 9 TP14–TP17		Miniature test points, black
U1	1	Dual din driver (64 TQFP-EP*) Maxim MAX19000BECB+
U2	1	2.5V voltage reference Maxim MAX6126AASA25+

### **Component List (continued)**

DESIGNATION	QTY	DESCRIPTION
U3	1	LDO regulator (5 SC70) Maxim MAX8511EXK25+T
U4	1	USB-to-UART converter (32 TQFP)
U5	1	93C46 type 3-wire EEPROM (8 SO)
U6	1	Microcontroller (68 QFN-EP*) Maxim MAXQ2000-RAX+
U7	1	Adjustable output LDO regulator (5 SC70) Maxim MAX8512EXK+T
U8–U11	4	Level translators (10 µMAX <sup>®</sup> ) Maxim MAX1840EUB+
U14–U17	4	LDOs (3 TO263)
Y1	1	16MHz crystal Hong Kong X'tals SSM16000N1HK188F0-0
Y2	1	6MHz crystal Hong Kong X'tals SSL60000N1HK188F0-0
Y3	0	Not installed
_	1	Heatpad
	1	Heat sink
	20	Shunts
_	1	PCB: MAX19000 EVALUATION KIT+

\*EP = Exposed pad.

### **Component Suppliers**

SUPPLIER	PHONE	WEBSITE
Hong Kong X'tals Ltd.	852-35112388	www.hongkongcrystal.com
Murata Electronics North America, Inc.	770-436-1300	www.murata-northamerica.com
Panasonic Corp.	800-344-2112	www.panasonic.com
Taiyo Yuden	800-348-2496	www.t-yuden.com
TDK Corp.	847-803-6100	www.component.tdk.com

Note: Indicate that you are using the MAX19000 when contacting these component suppliers.

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### MAX19000 EV Kit Files

FILE	DESCRIPTION
INSTALL.EXE	Installs the EV kit files on your computer
MAX19000.EXE	Application program
FTD2XX.INF	USB device driver file
UNINST.INI	Uninstalls the EV kit software
USB_Driver_Help.PDF	USB driver installation help file

### \_\_Quick Start

#### **Required Equipment**

- MAX19000 EV kit (USB cable included)
- User-supplied Windows 2000, Windows XP, or Windows Vista PC with a spare USB port
- +15V/0.5A DC power supply (VHH)
- -5.25V/0.5A DC power supply (VEE)
- LVDS source (RCV0/NRCV0)
- Differential output pulse generator
- High-speed oscilloscope
- Digital voltmeter (DVM)

**Note:** In the following sections, software-related items are identified by bolding. Text in **bold** refers to items directly from the EV kit software. Text in **bold and under-lined** refers to items from the Windows operating system.

#### Procedure

The MAX19000 EV kit is fully assembled and tested. Follow the steps below to verify board operation. **Caution: Do not turn on the power supply until all connections are completed.** 

- Visit <u>www.maxim-ic.com/evkitsoftware</u> to download the latest version of the EV kit software, 19000Rxx.ZIP. Save the EV kit software to a temporary folder and uncompress the ZIP file.
- Install the EV kit software on your computer by running the INSTALL.EXE program inside the temporary folder. The program files are copied and icons are created in the Windows <u>Start I Programs</u> menu.
- 3) Make sure the shunts of all jumpers are in the default positions, as shown in Tables 1 and 2.
- 4) Verify that the heat sink is installed and flush on the top of the MAX19000 IC.
- 5) Verify the correct polarity, voltage, and current limit of all power supplies.

- Set the differential pulse generator to output ±200mV centered at +1.2V common-mode voltage. Ensure the outputs are disabled (high impedance). Set the pulse frequency to 20MHz, 50% duty cycle.
- Connect the +15V power supply between the J20 (VHH) and J22 (GND) banana jacks on the MAX19000 EV kit.
- Connect the -5.25V power supply between the J22 (GND) and J24 (VEE) banana jacks on the MAX19000 EV kit. Connect all power-supply grounds to a single ground terminal.
- Connect the positive input of the DVM to TMPSNS and the negative input to ground.
- 10) Connect the differential pulse generator to the DATA0 and NDATA0 SMA connectors on the MAX19000 EV kit with SMA cables of equal length.
- 11) Set the RCV0/NRCV0 to a differential logic-low with  $V_{RCV0} = +1V$  and  $V_{NRCV0} = +1.4V$  to disable the three-state output mode.
- 12) Connect the DUT0 SMA connector on the MAX19000 EV kit with a short SMA cable to the high-speed oscilloscope. Set the scope input impedance to  $50\Omega$ .
- 13) Turn on the power supplies in the following order, VHH, VEE. By default, the MAX19000 starts in low-leakage mode.
- 14) Enable the differential pulse generator.
- 15) Connect the USB cable from the PC to the EV kit board. A <u>New Hardware Found</u> window pops up when installing the USB driver for the first time. If a window is not seen that is similar to the one described above after 30s, remove the USB cable from the board and reconnect it. Administrator privileges are required to install the USB device driver on Windows.
- 16) Follow the directions of the Found New Hardware window to install the USB device driver. Manually specify the location of the device driver to be C:\Program Files\MAX19000 (default installation directory) using the Browse button. During device driver installation, Windows may show a warning message indicating that the device driver Maxim uses does not contain a digital signature. This is not an error condition and it is safe to proceed with installation. Refer to the USB\_Driver\_Help.PDF document included with the software for additional information.

- 17) Start the MAX19000 EV kit software by opening its icon in the **Start I Programs** menu. The EV kit software main window appears, as shown in Figure 1. The MAX19000 EV kit software places the MAX19000 in drive mode.
- 18) Select the DCL/Channel 0 tab.
- 19) Set VCH to 6.2V by entering **6.2** into the **VCH** edit box located inside the **Voltage** group box and press the Enter key on the keyboard.
- 20) Set VCL to -2.2V by entering **-2.2** into the **VCL** edit box located inside the **Voltage** group box and press the Enter key on the keyboard.
- Set VDH to 1V by entering 1 into the VDH edit box located inside the Voltage group box and press the Enter key on the keyboard.
- 22) Set VDL to 0V by entering **0** into the **VDL** edit box located inside the **Voltage** group box and press the Enter key on the keyboard.

### Table 1. Digital Interface Jumper Settings

- 23) This places channel 0 into the drive-high mode.
- 24) Select the **DCL/Channel 1** tab and repeat steps 19–22 to place the channel 1 into the drive-high mode.
- 25) The TMPSNS test point monitors the MAX19000 junction temperature. Verify that the voltmeter does not read higher than +4.2V (junction temperature < +150°C).
- 26) Set the oscilloscope to  $10M\Omega$  high-impedance mode.
- 27) Set the oscilloscope to trigger on the OUT0 channel, with the trigger level set to +0.5V. Set the time base to 20ns per division. A 0 to +1V square wave of 20MHz appears on the oscilloscope.

JUMPER	SHUNT POSITION	DESCRIPTION
JU4	1-2*	Connects the DOUT pin of the MAX19000 to the on-board MAXQ2000 microcontroller through a level translator
	2-3	Connects the external DOUT signal to the J21-1 pin of the J21 connector
JU5	1-2*	Connects the DIN pin of the MAX19000 to the on-board MAXQ2000 microcontroller through a level translator
	2-3	Connects the external DIN signal to the J21-3 pin of the J21 connector
JU6	1-2*	Connects the $\overline{\text{CS}}$ pin of the MAX19000 to the on-board MAXQ2000 microcontroller through a level translator
	2-3	Connects the external $\overline{\text{CS}}$ signal to the J21-7 pin of the J21 connector
JU7	1-2*	Connects the SCLK pin of the MAX19000 to the on-board MAXQ2000 microcontroller through a level translator
	2-3	Connects the external SCLK signal to the J21-5 pin of the J21 connector
JU8	1-2*	Connects the LLEAKPO pin of the MAX19000 to the on-board MAXQ2000 microcontroller through a level translator
	2-3	Connects the external LLEAKPO signal to the J21-13 pin of the J21 connector
JU9	1-2*	Connects the LLEAKP1 pin of the MAX19000 to the on-board MAXQ2000 microcontroller through a level translator
	2-3	Connects the external LLEAKP1 signal to the J21-15 pin of the J21 connector
JU10	1-2*	Connects the $\overline{\text{RST}}$ pin of the MAX19000 to the on-board MAXQ2000 microcontroller through a level translator
	2-3	Connects the external RST signal to the J21-11 pin of the J21 connector
JU11	1-2*	Connects the $\overline{\text{LOAD}}$ pin of the MAX19000 to the on-board MAXQ2000 microcontroller through a level translator
	2-3	Connects the external LOAD signal to the J21-9 pin of the J21 connector

### Table 1. Digital Interface Jumper Settings (continued)

JUMPER	SHUNT POSITION	DESCRIPTION		
11.11.0	1-2*	Connects the DGS pin of the MAX19000 to the ground		
JU 18	Open	Disconnects the DGS pin of the MAX19000 from the ground		
11.100	1-2*	Connects the GNDDAC0 pin of the MAX19000 to the ground		
JU23	Open	Disconnects the GNDDAC0 pin of the MAX19000 from the ground		
11.12.4	1-2*	Connects the GNDDAC1 pin of the MAX19000 to the ground		
JU24	Open	Disconnects the GNDDAC1 pin of the MAX19000 from the ground		

\*Default position.

### **Table 2. Power Supplies Jumper Settings**

JUMPER	SHUNT POSITION	DESCRIPTION		
JU1	1-2	Connects the ADJ pin of the regulator (U15) to ground through a $280\Omega$ resistor (R3) in parallel with a $402\Omega$ resistor (R5)		
	Open*	Disconnects the ADJ pin of the regulator from R3		
1112	1-2	Connects the ADJ pin of the regulator (U15) to ground through a 2.26k $\Omega$ resistor (R4)		
JU2	Open*	Disconnects the ADJ pin of the regulator from R4		
11.10	1-2*	Connects the VT_C to the on-board regulator (U17)		
303	Open	Disconnects the 1.25V (VT_C) from the on-board regulator (U17)		
JU15	1-2	Connects the ADJ pin of the regulator (U16) to ground through a $75\Omega$ resistor (R6) in parallel with a $402\Omega$ resistor (R8)		
	Open*	Disconnects the ADJ pin of the regulator from R6		
JU16	1-2*	Connects the ADJ pin of the regulator (U16) to ground through a $249\Omega$ resistor (R7) in parallel with a $402\Omega$ resistor (R8)		
	Open	Disconnects the ADJ pin of the regulator from R7		
11.110	1-2*	Connects VCC to the on-board regulator (U14)		
3019	Open	Disconnects VCC from the on-board regulator (U14)		
11.120	1-2*	Connects VDD to the on-board regulator (U15)		
3020	Open	Disconnects VDD from the on-board regulator (U15)		
11.101	1-2*	Connects VT_DT to the on-board regulator (U16)		
3021	Open	Disconnects VT_DT from the on-board regulator (U16)		
11.100	1-2*	Connects VEE to the negative power supply input jack		
0022	Open	Disconnects VEE from the negative input power supply		

\*Default position.

VDH [ VDH [ VDL [ VCH [ VCH [ VCPL [ VCPL [ VCPL [ VCOM [		<ul> <li>4915</li> </ul>	Calibration Gain I	<ul> <li>31</li> &lt;</ul>	Offset	<ul> <li>127</li> </ul>	Voltage -0.00012 -0.00012 -0.00012 -0.00012 -0.00012 -0.00012 -0.00012 -0.00012 -0.00012
VLDL	• Read	A915	EN_TEMP ALARM EN_OV_ALARM LDCAL LDDIS TMSel LLeak SC1 SC0 ecked = Logic High checked = Logic Low	▶     31       1000     ▼       000     ▼       000     ▼       000     ▼       000     ▼       000     ▼	R0 HYST CDRP_DRV_S CDRP_DRV_L CDRP_CMP_S CDRP_CMP_L	Control Sig	0.00012 nals P1 P0 Ext Temp Sense

DCL/Cha	annel 0 DCL/Cha	annel 1					
VDU			Calibration Gain		Offset		Voltage
		4915		▶ 31		127	0.00012
VDT		• 4915		• 31		• 127 • 127	0.00012
VCH		► 4915		► 31		127	-0.00012
VCL		4915		31		127	-0.00012
VCPH		▶ 4915		• 31		127	-0.00012
VCPL		• 4915	•	• 31	•	127	-0.00012
VCOM	•	▶ 4915	•	• 31	•	▶ 127	-0.00012
VLDH	•	▶ 4915	•	▶ 31	•	127	-0.00012
VLDL	•	▶ 4915	•	▶ 31	•	▶ 127	-0.00012
−Status ▼ A	Auto Read		EN_OV_ALARM   LDCAL   LDDIS   TMSel   LLeak   SC1   SC0   ecked = Logic High   ichecked = Logic Low	1000 ▼ 000 ▼ 000 ▼ 000 ▼ 000 ▼	R0 HYST CDRP_DRV_S CDRP_DRV_L CDRP_CMP_S CDRP_CMP_L		
<b>//</b> irdware C	Connected					LOAD	RST



Evaluates: MAX19000

\_\_\_\_Detailed Description of Software User-Interface Panel The GUI contains two DCL tab sheets for channels 0 and 1 for all local projects, and control signal actings

and 1 for all level, register, and control-signal settings. The **File** menu item has save and load functions for all of the settings.

**Driver/Comparator/Load (DCL) Settings DCL/Channel 0** and **DCL/Channel 1** tab sheets (Figures 1 and 2) are identical and are for channels 0 and 1, respectively. These tab sheets contain **Level Settings** and **DCL Register** group boxes.

#### Level Settings

The Level Settings group box contains registers for signal level, calibration, and gain settings for VDH, VDL, VDT, VCH, VCL, VCPH, VCPL, VCOM, VLDH, and VLDL. Each setting is controlled by a scrollbar with a value label to its right. Finer adjustments can be made by clicking on the arrows on each side of the scrollbar. The scrollbars in the Level group box have 16,384 steps corresponding to 14 bits. In the Calibration group box, the Offset scrollbars have 256 steps corresponding to 8 bits, and the Gain scrollbars have 64 steps corresponding to 6 bits. The corresponding voltage levels are also calculated and shown in the edit boxes inside the Voltage group box. The value, calibration, and gain selections become effective immediately after an adjustment without having to press the LOAD button. The value can also be typed into the edit boxes inside the **Voltage** group box. Pressing the Enter key on the keyboard loads the values to the device.

**DCL Register** The **DCL Register** group box contains eight checkboxes to control the DCL register. A checked checkbox represents a logic-high and an unchecked checkbox represents a logic-low. Any change in state of the checkbox immediately sends the new bit setting to the device.

The **DCL Register** group box also has six drop-down lists that control the driver output impedance, comparator hysteresis, and driver/comparator cable-droop compensation.

#### **Save/Load Settings**

All settings specified by the GUI can be saved to a file by selecting the **File I Save Setup** menu item located at the front of the menu bar. Settings saved in a file can also be loaded to the GUI and the MAX19000 using the same procedure of selecting the **File I Load Setup** menu item. Use the save-and-load feature to save many different setups, which can be recalled at a later time.

#### **Advanced User Interface**

There are two methods for communicating with the MAX19000. The first is through the windows shown in Figures 1 and 2. The second is through the **Advanced User Interface** window. The **Advanced User Interface** window, available by selecting the **Option I Interface** (**Advanced User**) menu item, allows execution of serial commands manually.

The **Advanced User Interface** window can also be used as a debug tool since it is capable of manually reading and writing to every register of the MAX19000.

### **Detailed Description of Hardware**

The MAX19000 evaluation kit (EV kit) is a fully assembled and tested PCB that evaluates the MAX19000 dual pin driver. The EV kit includes SMA connections for the highspeed digital I/Os and the MAX19000 pin-driver outputs. The MAX19000 EV kit is connected to the PC through the universal serial bus (USB) port.

#### **Power Supplies**

Connect the power supplies using the high-current-rated banana jacks, J24 (-5.25V) and J20 (+15V). Common to all power supplies should be the GND banana jack, J22 (GND) on the MAX19000 EV kit. All power supplies should be within the range specified on the MAX19000 IC data sheet. The MAX19000 needs only two supplies attached to the board. All other supplies are generated through regulators on the EV kit board.

Advanced User Interface Options Help	I _ □ ×	
Connection Bit Set/Clear 2-wire interface 3-wire interface Logging		
Connection K10 Clock (SCK) (SCLK)	Configuration ✓ Send & receive MSB first CPOL=1 (clock idles high) CPHA=1 (sample 2nd edge)	
K12 🗨 Data from master to slave (MOSI) (DIN)	MOSI Data Inverted Logic     MISO Data Inverted Logic	
K11 🔽 Data from slave to master (MISO) (DOUT)	CS is active high, idle low	
K9 Chip-select (CS) for data framing	8.0 V × 1 MHz V	
Use standard connections for high-speed SPI	Get Speed Set Speed	
Send and Receive Data Data bytes to be written:		
0x55, 0xAA Send Now repeat 1		
?		
Maxim CMAXQUSB V01.04.32 > USB:0 @ 460800 bau	ud Speed query	

Figure 3. Advanced User Interface Window (3-Wire Interface Tab)

TEST POINT	SIGNAL	FUNCTION
TMPSNS	TEMPSNS	MAX19000 die temperature indicator
TP1	DIN	Serial-data input
TP2	LLEAKPO	Low-leakage enable, channel 0
TP3	LLEAKP1	Low-leakage enable, channel 1
TP12	DOUT	Serial-data output
TP14	SCLK	Serial clock
TP15	CS	Chip select
TP16	LOAD	Load
TP17	RST	Serial reset

### **Table 3. Test Points and Their Functions**

#### **High-Speed Digital I/Os**

The top and bottom edges of the PCB are populated with end-launch SMA connectors, and are the high-speed digital I/Os of the MAX19000. The inputs are terminated internally to the MAX19000 IC. The outputs require termination (nominally  $50\Omega$ ) at the end of the attached cable.

The board power supply (VRCV) is the voltage used to terminate the comparator outputs on the MAX19000 IC. Setting VRCV to +1.2V makes the high-speed digital I/Os compatible with LVDS levels.

The high-speed digital inputs (DATA0/NDATA0, RCV0/ NRCV0, DATA1/NDATA1, and RCV1/NRCV1) are intended for use with a high-speed differential signal source such as LVDS, LVPECL, etc. If only a single-ended stimulus source is available, a converter consisting of a 1:1 ratio transformer can be used to produce a differential pair of inputs for DATA0/NDATA0 or DATA1/NDATA1. A tri-resistor network can be used to produce a differential logic level for RCV0/NRCV0 or RCV1/NRCV1 inputs.

The high-speed digital outputs (CL0/NCL0, CH0/NCH0, CL1/NCL1, and CH1/NCH1) are intended for use with a high-speed differential logic analyzer. These outputs are internally pulled up to the VCTV voltage through internal 50 $\Omega$  resistors. These outputs can be double-terminated at the measurement source by external 50 $\Omega$  resistors.

#### **Pin-Driver Outputs**

The two pin-driver outputs (DUT0 and DUT1) are through end-launch SMA connectors on the right-edge of the PCB. The outputs have a typical output impedance of 50 $\Omega$ , but this can be adjusted by software.

#### **Test Points**

There are nine test points on the MAX19000 EV kit to facilitate performance analysis and circuit modification. These test points are listed in Table 3.

#### **Device Ground Sense**

The MAX19000 IC has the ability to sense the ground potential at the device under test (DUT). The MAX19000 EV kit is preconfigured to have the device ground sense (DGS) pin connected to the ground plane. If remote sensing is desired, remove the shunt on jumper JU18 and connect the pin 1 of JU18 to the remote DUT ground.

#### **Temperature Sensing**

The MAX19000 EV kit provides the means to determine the MAX19000 IC's die temperature through the TMPSNS test point. During operation, TMPSNS should be continuously monitored to ensure that the junction temperature does not exceed  $+150^{\circ}$ C, which corresponds with +4.2V. During normal operation, a voltage of +3V to +3.6V is typical.



Figure 4a. MAX19000 EV Kit Schematic (Sheet 1 of 4)



Figure 4b. MAX19000 EV Kit Schematic (Sheet 2 of 4)



Figure 4c. MAX19000 EV Kit Schematic (Sheet 3 of 4)



Figure 4d. MAX19000 EV Kit Schematic (Sheet 4 of 4)



Figure 5. MAX19000 EV Kit Component Placement Guide—Top Side



Figure 6. MAX19000 EV Kit PCB Layout—Component Side



Figure 7. MAX19000 EV Kit PCB Layout—Layer 2



Figure 8. MAX19000 EV Kit PCB Layout—Layer 3



Figure 9. MAX19000 EV Kit PCB Layout—Bottom Side

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