

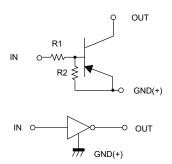
Features

- Built-In Bias Resistors Enable the Configuration of an Inverter Circuit Without Connecting External Input Resistors
- The Bias Resistors Consist of Thin-Film Resistors With Complete Isolation to Allow Negative Biasing of the Input. They Also Have the Advantage of Almost Completely Eliminating Parasitic Effects
- Only The On/Off Conditions Need to Be Set For Operation, Making Device Design Easy
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant.See Ordering Information)

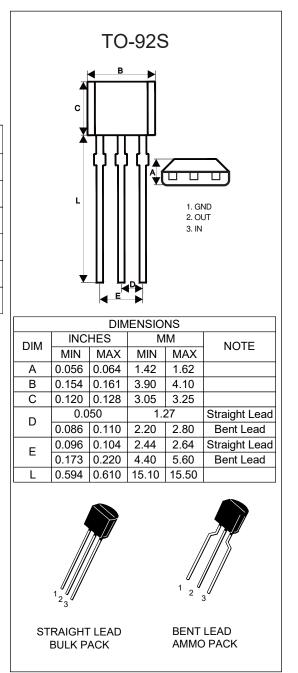
Maximum Ratings @ 25°C Unless Otherwise Specified

Parameter	Symbol	Min	Тур	Max	Unit
Supply Voltage	V _{cc}		-50		V
Input Voltage	V _{IN}	-40		10	V
Output Current	I _o		-50	-100	mA
Power Dissipation	P _D		300		mW
Junction Temperature	TJ			150	°C
Storage Temperature	T _{stg}	-55		150	°C

Internal Structure



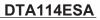
PNP Digital Transistor





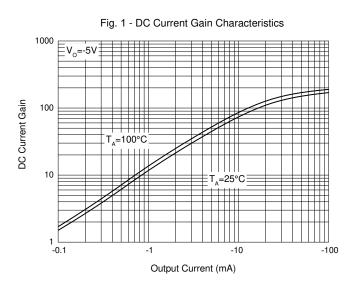
Electrical Characteristics @ 25°C Unless Otherwise Specified

Parameter	Symbol	Min	Тур	Max	Unit	Conditions
Input Voltage	V _{I(off)}	-0.5			V	V _{CC} =-5V, I _O =-100µA
	V _{I(on)}			-3.0	V	V _o =-0.3V, I _o =-10mA
Output Voltage	V _{O(on)}			-0.3	V	I _o =-10mA,I _I =-0.5mA
Input Current	I _I			-0.88	mA	V _I =-5V
Output Current	I _{O(off)}			-0.5	μA	V _{CC} =-50V, V _I =0
DC Current Gain	Gı	30				V _o =-5V, I _o =-5mA
Input Resistance	R ₁	7.0	10	13	KΩ	
Resistance Ratio	R ₂ /R ₁	0.8	1.0	1.2		
Transition Frequency	f _T		250		MHz	V _{CE} =-10V, I _E =5mA, f=100MHz

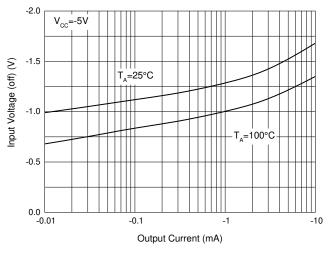




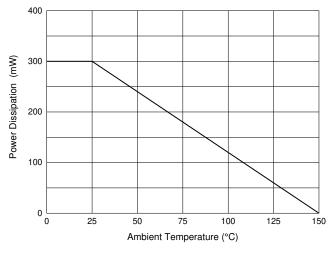
Curve Characteristics











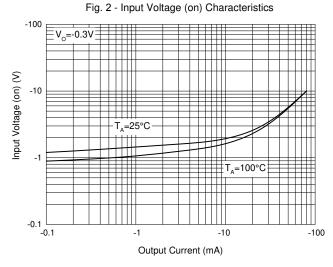
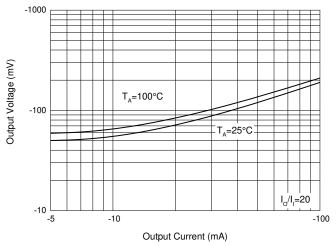


Fig. 4 - Output Voltage Characteristics





Ordering Information

Device	Packing		
Part Number-AP	Ammo Packing: 30Kpcs/Carton		
Part Number-BP	Bulk: 100Kpcs/Carton		

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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