



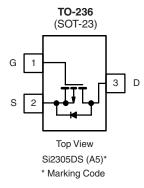
# P-Channel 1.25-W, 1.8-V (G-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
	$0.052$ at $V_{GS} = -4.5 \text{ V}$	± 3.5		
- 8	0.071 at V <sub>GS</sub> = - 2.5 V	± 3		
	0.108 at V <sub>GS</sub> = - 1.8 V	± 2		

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFETs: 1.8 V Rated





Ordering Information: Si2305DS-T1

Si2305DS-T1-E3 (Lead (Pb)-free)

Si2305DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_A = 25  ^{\circ}C$ , unles	ss otherwise not	ed		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 8	V	
Gate-Source Voltage		$V_{GS}$	± 8	v	
Continuous Drain Current (T,I = 150 °C)	T <sub>A</sub> = 25 °C	1	± 3.5		
Continuous Diam Current (1) = 150 °C)	T <sub>A</sub> = 70 °C	I <sub>D</sub>	± 2.8		
Pulsed Drain Current		I <sub>DM</sub>	± 12	A	
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		I <sub>S</sub> - 1.6			
w. D. D ah	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.25	w	
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 70 °C	LD	0.8		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian Installanta Ambricant	t ≤ 5 s	R <sub>thJA</sub>		100	°C/W	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' 'thJA	130			

#### Notes:

a. Surface Mounted on FR4 board.

b.  $t \le 5$  s.

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply.

## Si2305DS

# Vishay Siliconix



SPECIFICATIONS T <sub>J</sub> = 25	SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted						
-			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = -10 \mu\text{A}$	- 8			V	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	- 0.45		- 0.8	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zoro Cata Voltago Drain Current	I <sub>DSS</sub>	$V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μΑ	
Zero Gate Voltage Drain Current		V <sub>DS</sub> = - 8 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10		
	1	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 6			А	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -2.5 \text{ V}$	- 3				
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.5 A		0.044	0.052	Ω	
Drain-Source On-Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 3 A		0.060	0.071		
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 2 A		0.087	0.108		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 3.5 A		8.5		S	
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> = - 1.6 A, V <sub>GS</sub> = 0 V			- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			10	15		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 4 V, $V_{GS}$ = - 4.5 V, $I_D$ $\cong$ - 3.5 A		2		nC	
Gate-Drain Charge	Q <sub>gd</sub>			2			
Input Capacitance	C <sub>iss</sub>			1245			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -4 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		375		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			210			
Switching <sup>b</sup>							
Turn-On Time	t <sub>d(on)</sub>			13	20		
	t <sub>r</sub>	$V_{DD}$ = - 4 V, $R_L$ = 4 $\Omega$		25	40	no	
Turn-Off Time	t <sub>d(off)</sub>	$I_D\cong$ - 1.0 A, $V_{GEN}$ = - 4.5 V, $R_G$ = 6 $\Omega$		55	80	ns	
Turr-On Time	t <sub>f</sub>			19	35		

- a. For DESIGN AID ONLY, not subject to production testing.
- b. Pulse test: PW  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- c. Switching time is essentially independent of operating temperature.

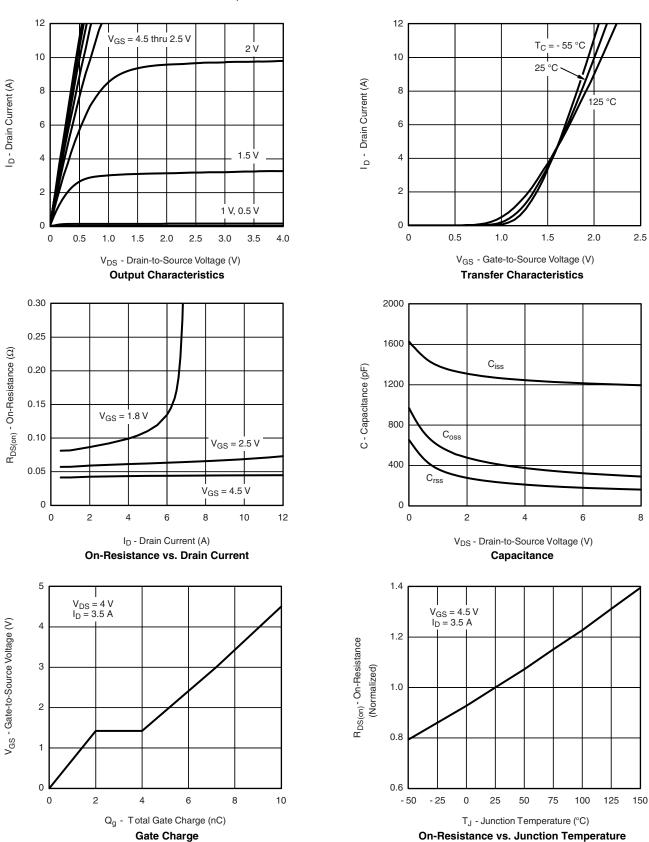
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







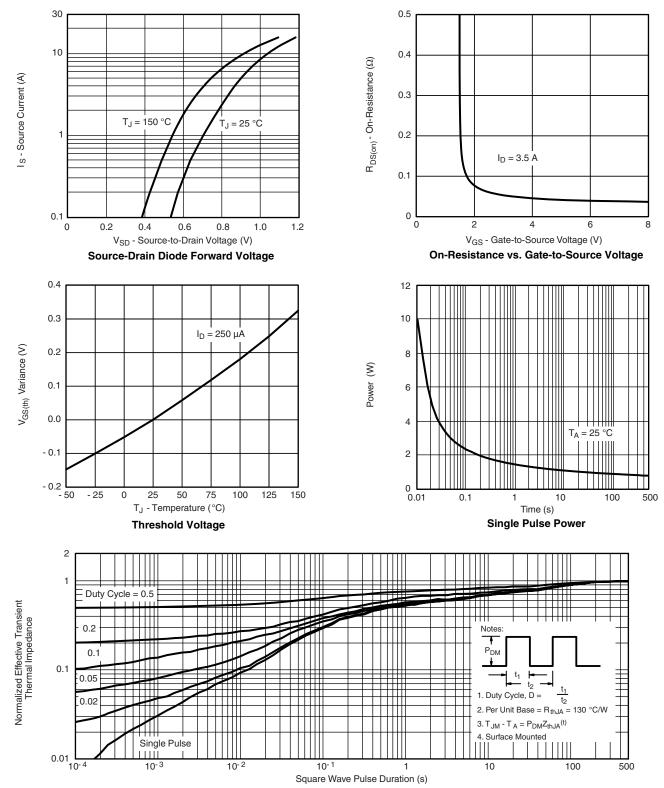
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwse noted



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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwse noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

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