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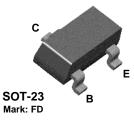
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## BCV26



# **PNP** Darlington Transistor

This device is designed for applications requiring extremely high current gain at currents to 800 mA. Sourced from Process 61.

#### **Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	30	V
V <sub>CBO</sub>	Collector-Base Voltage	40	V
V <sub>EBO</sub>	Emitter-Base Voltage	10	V
I <sub>C</sub>	Collector Current - Continuous	1.2	А
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

### NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

### Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Мах	Units
		*BCV26	
PD	Total Device Dissipation	350	mW
	Derate above 25°C	2.8	mW/∘C
$R_{\thetaJA}$	Thermal Resistance, Junction to Ambient	357	°C/W

\*Device mounted on FR-4 PCB 40 mm X 40 mm X 1.5 mm.

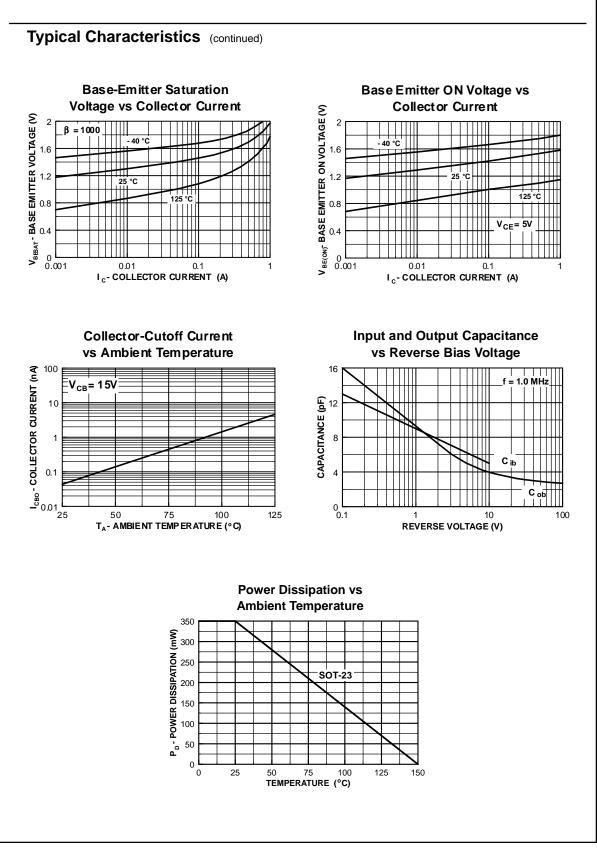
## PNP Darlington Transistor (continued)

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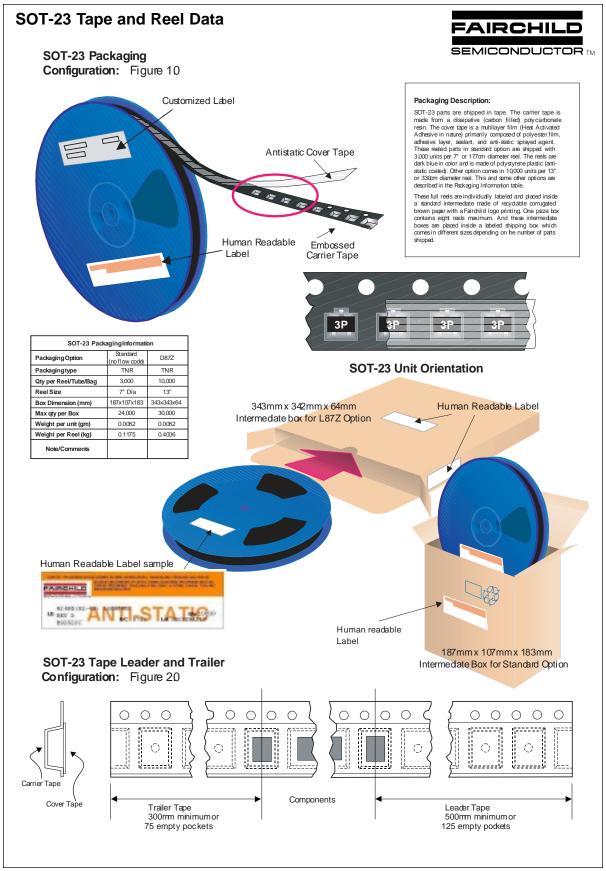
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
OFF CHA	RACTERISTICS					
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0$	30			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_{\rm C} = 10 \ \mu \text{A}, I_{\rm E} = 0$	40			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_{\rm E} = 100 \text{ nA}, I_{\rm C} = 0$	10			V
I <sub>CBO</sub>	Collector-Cutoff Current	$V_{CB} = 30 \text{ V}, I_E = 0$			0.1	μA
I <sub>EBO</sub>	Emitter-Cutoff Current	$V_{EB} = 10 \text{ V}, \text{ I}_{C} = 0$			0.1	μΑ
ON CHAR	ACTERISTICS					
FE	DC Current Gain $I_c = 1.0 \text{ mA}, V_{ce} = 5.0 \text{ V}$		4,000			
		$I_{C} = 10 \text{ mA}, V_{CE} = 5.0 \text{ V}$	10,000			
1	Collector-Emitter Saturation Voltage	$I_{C} = 100 \text{ mA}, V_{CE} = 5.0 \text{ V}$ $I_{C} = 100 \text{ mA}, I_{B} = 0.1 \text{ mA}$	20,000		1.0	V
CE(sat)	Base-Emitter Saturation Voltage	$I_{\rm C} = 100$ mA, $I_{\rm B} = 0.1$ mA			1.5	V
BE(sat)	Dase-Emilier Saluration Voltage	$I_{\rm C} = 100$ mA, $I_{\rm B} = 0.1$ mA			1.5	v
~						
-	Collector Capacitance ages (V) and currents (A) are negative polarity for PNP	$V_{CB} = 30$ V, $I_E = 0$ , f = 1.0 MHz transistors.		3.5		pF
	ages (V) and currents (A) are negative polarity for PNP	transistors.	Emitter		ation	pF
NOTE: All volta	ages (V) and currents (A) are negative polarity for PNP	transistors.		Satur		pF

PNP Darlington Transistor (continued)

# BCV26

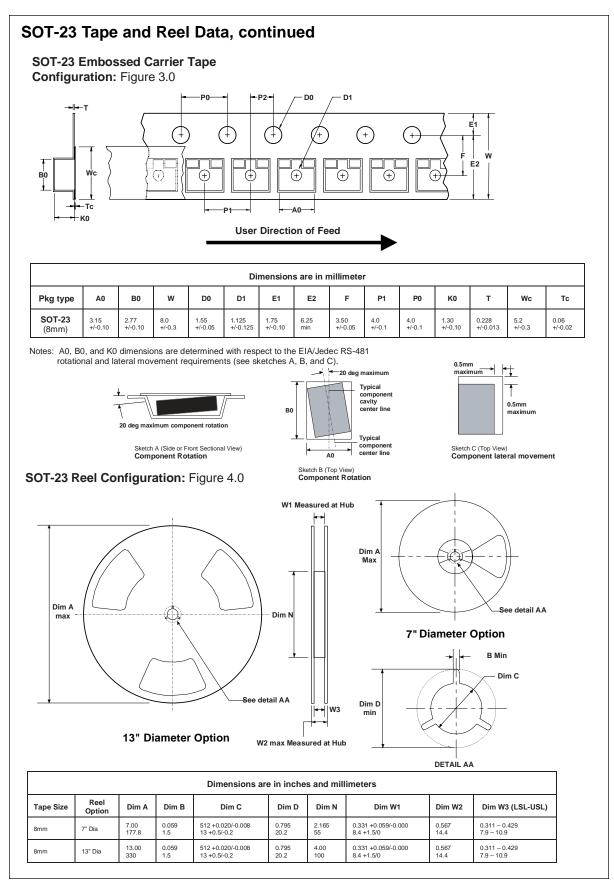


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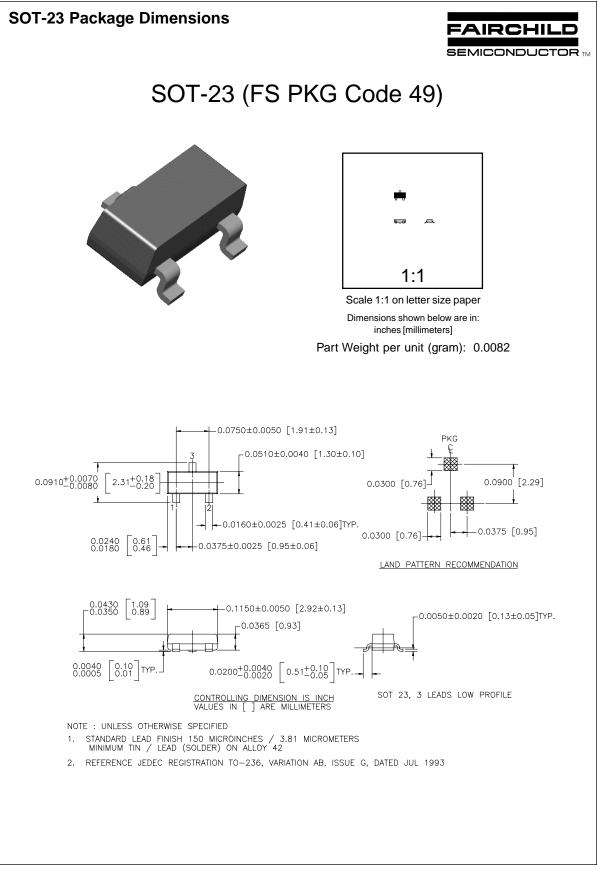


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