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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# 2SK1401, 2SK1401A

## Silicon N Channel MOS FET

REJ03G0941-0200

(Previous: ADE-208-1281)

Rev.2.00 Sep 07, 2005

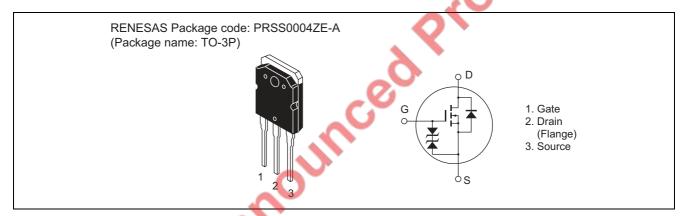
### **Application**

High speed power switching

#### **Features**

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

#### **Outline**



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item		Symbol	Ratings	Unit
Drain to source voltage 2SK1401		$V_{DSS}$	300	V
	2SK1401A		350	
Gate to source voltage		$V_{GSS}$	±30	V
Drain current		I <sub>D</sub>	15	Α
Drain peak current		I <sub>D(pulse)</sub> *1	60	Α
Body to drain diode reverse dr	ain current	I <sub>DR</sub>	15	Α
Channel dissipation		Pch* <sup>2</sup>	100	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1.  $PW \le 10 \infty$ , duty cycle  $\le 1\%$ 

2. Value at  $T_C = 25^{\circ}C$ 

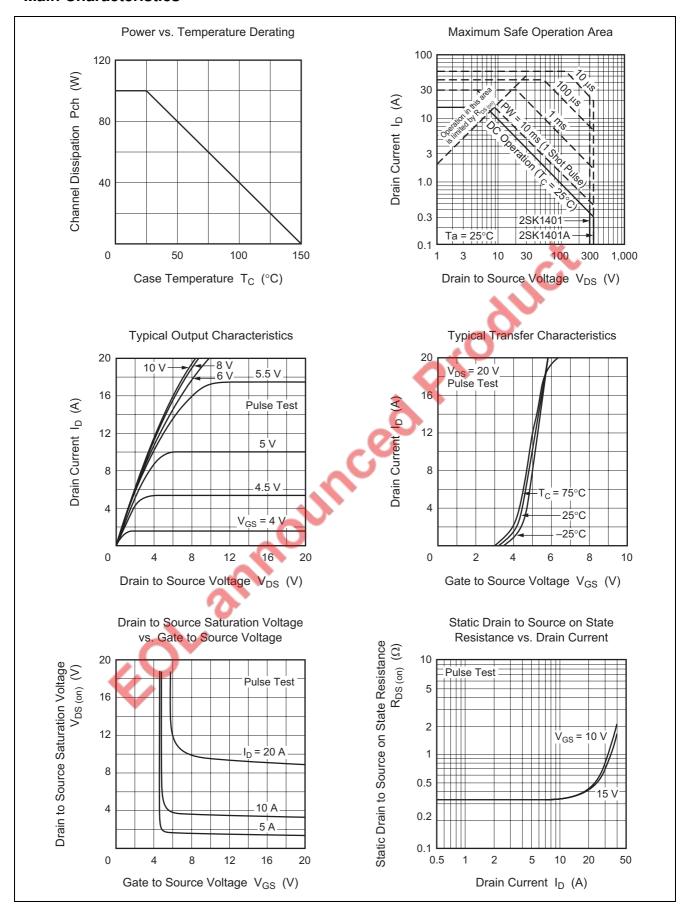
### **Electrical Characteristics**

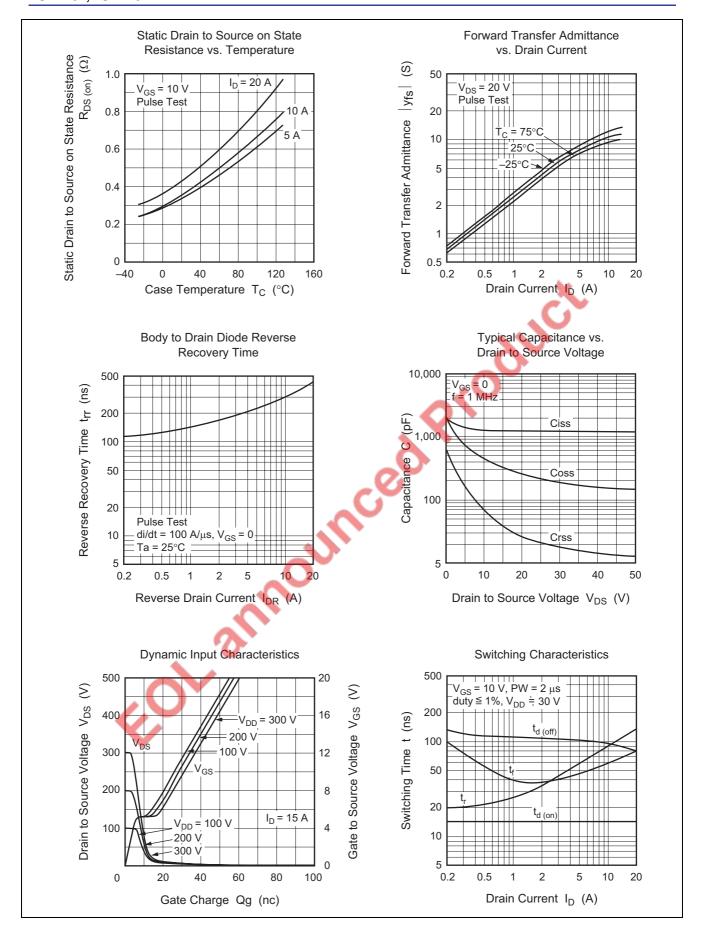
 $(Ta = 25^{\circ}C)$ 

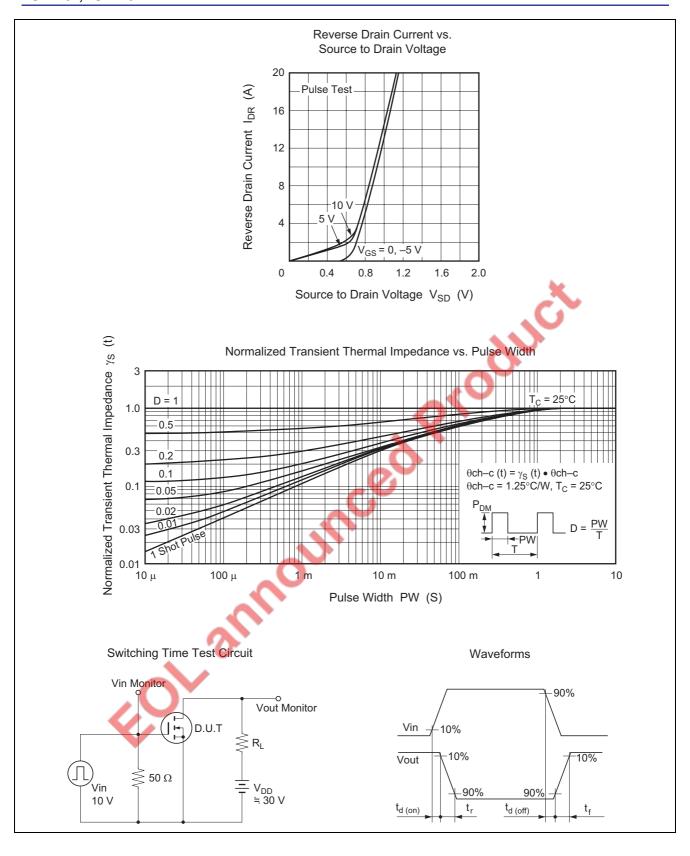
Item		Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source	2SK1401	$V_{(BR)DSS}$	300		-	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
breakdown voltage	2SK1401A		350		_		
Gate to source breakdown voltage		$V_{(BR)GSS}$	±30		<b>P</b>	V	$I_G = \pm 100 \propto A, V_{DS} = 0$
Gate to source leak curre	ent	I <sub>GSS</sub>	_	_	±10	≪A	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain	2SK1401	I <sub>DSS</sub>	_		250	∞A	$V_{DS} = 240 \text{ V}, V_{GS} = 0$
current	2SK1401A						$V_{DS} = 280 \text{ V}, V_{GS} = 0$
Gate to source cutoff vol-	tage	$V_{GS(off)}$	2.0	2	3.0	٧	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on	2SK1401	R <sub>DS(on)</sub>	_	0.25	0.35	Ω	$I_D = 8 A, V_{GS} = 10 V^{*3}$
state resistance	2SK1401A		<del>.</del>	0.30	0.40		
Forward transfer admittance		yfs	6	9.5		S	$I_D = 8 A, V_{DS} = 10 V^{*3}$
Input capacitance		Ciss		1250	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance		Coss		420		рF	f = 1 MHz
Reverse transfer capacitance		Crss	-	70		рF	
Turn-on delay time	4	t <sub>d(on)</sub>		15		ns	$I_D = 8 A, V_{GS} = 10 V,$
Rise time		t <sub>r</sub>		80		ns	$R_L = 3.75 \Omega$
Turn-off delay time		$t_{d(off)}$	_	100	_	ns	
Fall time	<b>A</b>	t <sub>f</sub>	_	55	_	ns	
Body to drain diode forwa	ard voltage	$V_{DF}$	_	1.05	_	V	$I_F = 15 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery		t <sub>rr</sub>	_	370	_	ns	$I_F = 15 A, V_{GS} = 0,$
time							di <sub>F</sub> /dt = 100 A/∞s

Note: 3. Pulse test

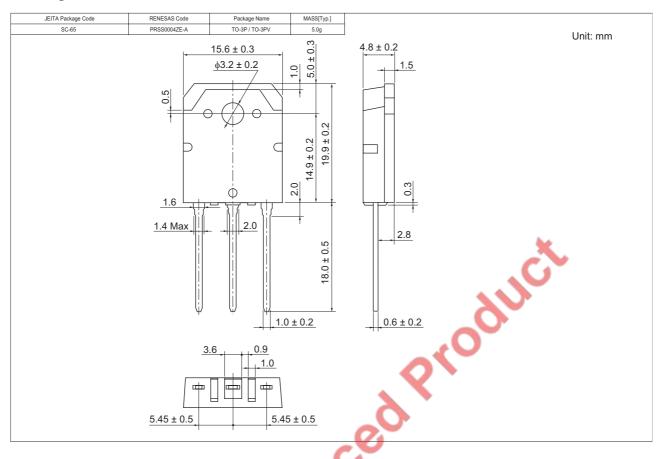
#### **Main Characteristics**







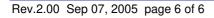
### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity		<b>3</b>	Shipping Container
2SK1401-E	360 pcs	7		Box (Tube)
2SK1401A-E	360 pcs			Box (Tube)

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