2.5V Drive Pch+SBD MOSFET

V _{DSS}	-20V
R _{DS(on)} (Max.)	200mΩ
I _D	±1.5A
P _D	1.25W

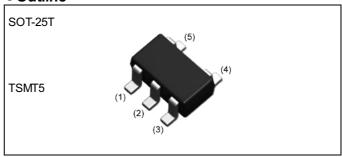
Features

- The QS5U21 combines Pch MOSFET with a Schottky barrier diode in a single TSMT5 package.
- 2) Low on-state resistance with fast swicthing
- 3) Low voltage drive (2.5V drive).
- 4) Built-in Low V_F schottky barrier diode.
- 5) Pb-free lead plating; RoHS compliant.

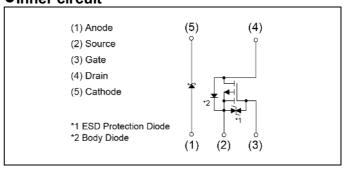
Application

Load switch, DC/ DC conversion

Outline



•Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
• •	Quantity (pcs)	3000
	Taping code	TR
	Marking	U21

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

<MOSFET>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	-20	V
Gate - Source voltage	V _{GSS}	±12	V
Continuous drain current	I _D	±1.5	А
Pulsed drain current	I _{DP} *1	±6.0	Α
Continuous source current (body diode)	I _S	-0.75	Α
Pulsed source current (body diode)	I _{SP} *1	-3.0	А
Power dissipation	P _D *3	0.9	W/element
Junction temperature	T _j	150	°C

● Absolute maximum ratings (T_a = 25°C)

<Di>

Parameter	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RM}	25	V
Reverse voltage	V _R	20	V
Forward current	I _F	1.0	А
Forward current surge peak	I _{FSM} *2	3.0	А
Power dissipation	P _D *3	0.7	W/element
Junction temperature	T _j	150	°C

<MOSFET + Di>

Parameter	Symbol	Value	Unit
Power dissipation	P _D *3	1.25	W/total
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Electrical characteristics (T_a = 25°C)

<MOSFET>

Daramatar	Cymah ol	Conditions	Values			Linit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0V$	-	-	±10	μΑ
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = -1mA$		-20	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V	-	-	-1	μA
Gate threshold voltage	V _{GS(th)}	$V_{DS} = -10V, I_{D} = -1mA$	-0.7	-	-2.0	V
		$V_{GS} = -4.5V, I_D = -1.5A$	1	160	200	
Static drain - source on - state resistance	R _{DS(on)} *4	$V_{GS} = -4V, I_D = -1.5A$	-	180	240	mΩ
		V_{GS} = -2.5V, I_D = -0.75A	-	260	340	
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = -10V, I _D = -0.75A	1.0	-	-	S

●Electrical characteristics (T_a = 25°C)

<MOSFET>

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offit
Input capacitance	C _{iss}	V _{GS} = 0V	-	325	-	
Output capacitance	C _{oss}	V _{DS} = -10V	-	60	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	40	-	
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq$ -15V, $V_{GS} =$ -4.5V	-	10	-	
Rise time	t _r *4	I _D = -0.75A	-	10	-	no
Turn - off delay time	t _{d(off)} *4	$R_L = 20\Omega$	-	35	-	ns
Fall time	t _f *4	$R_G = 10\Omega$	-	10	-	

● Gate charge characteristics (T_a = 25°C)

<MOSFET>

Darameter	Cymbol	Conditions	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*4}		-	4.2	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \simeq -15V, I_{D} = -1.5A$ $V_{GS} = -4.5V$	-	1.0	-	nC
Gate - Drain charge	Q _{gd} *4	765 1.07	-	1.1	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<MOSFET>

Parameter	Symbol	Conditions	Values			Linit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward voltage	V_{SD}^{*4}	$V_{GS} = 0V, I_{S} = -0.75A$	-	-	-1.2	V

● Electrical characteristics (T_a = 25°C)

<Di>

Parameter	Symbol	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward voltage	V_{F}	I _F = 1.0A	-	-	0.45	V
Reverse current	I _R	V _R = 20V	-	-	200	μA

^{*1} Pw \leq 10µs, Duty cycle \leq 1%

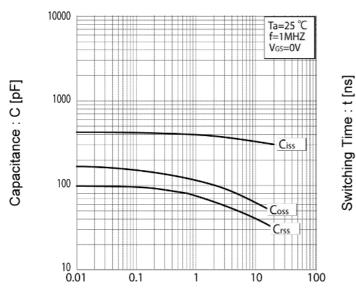
^{*2 60}Hz-1 cycle

^{*3} Mounted on a ceramic board

^{*4} Pulsed

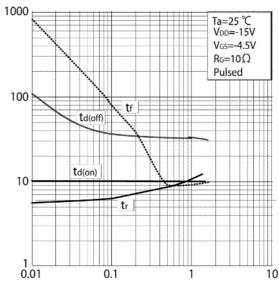
• Electrical characteristic curves < MOSFET>

Fig.1 Typical Capacitance vs. Drain -Source Voltage



Drain - Source Voltage : -V_{DS} [V]

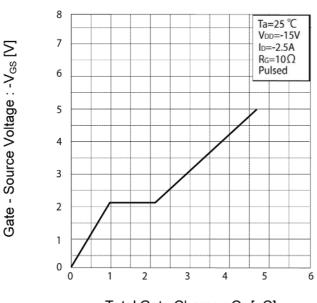
Fig.2 Switching Characteristics



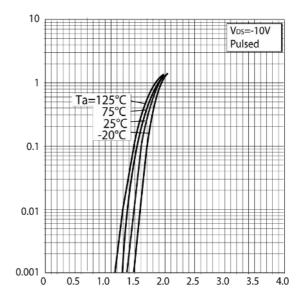
Drain Current: -ID [A]

Fig.4 Typical Transfer Characteristics

Fig.3 Dynamic Input Characteristics



Total Gate Charge : Q_g [nC]

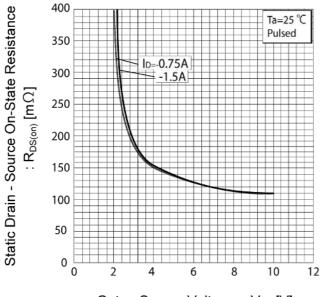


Gate - Source Voltage : V_{GS} [V]

Drain Current: -I_D [A]

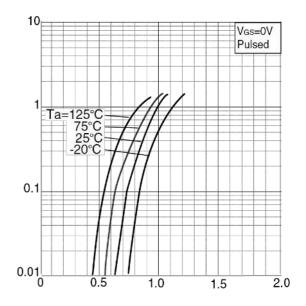
● Electrical characteristic curves < MOSFET>

Fig.5 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : -V_{GS} [V]

Fig.6 Source Current vs. Source Drain Voltage



Source - Drain Voltage : -V_{SD} [V]

Fig.7 Static Drain - Source On - State Resistance vs. Drain Current (I)

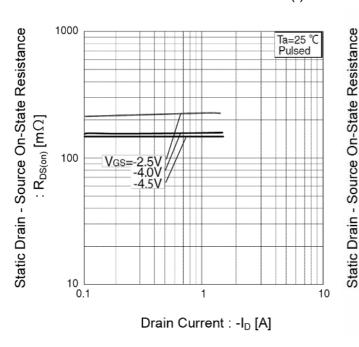
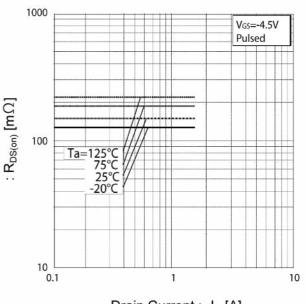


Fig.8 Static Drain - Source On - State Resistance vs. Drain Current (II)



Drain Current : -I_D [A]

Source Current : -Is [A]

• Electrical characteristic curves < MOSFET>

Fig.9 Static Drain - Source On - State Resistance vs. Drain Current (III)

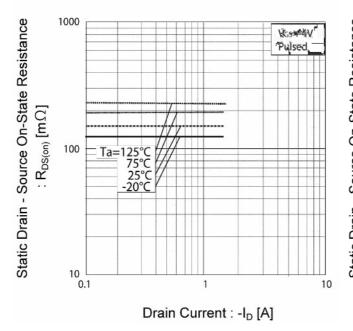
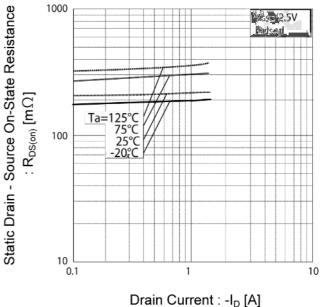


Fig.10 Static Drain - Source On - State Resistance vs. Drain Current (IV)



● Electrical characteristic curves < Diode >

Fig.11 Forward Current vs. Forward Voltage

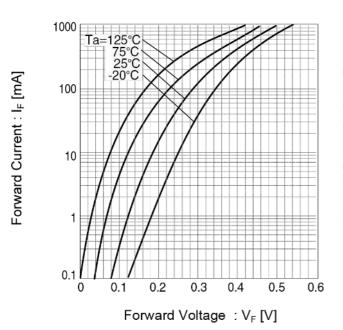
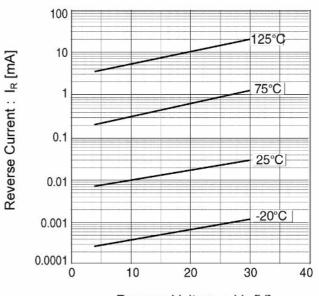


Fig.12 Reverse Current vs. Reverse Voltage



Reverse Voltage: V_R [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

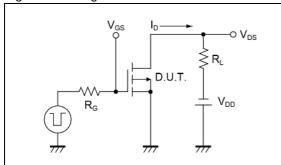


Fig.2-1 Gate Charge Measurement Circuit

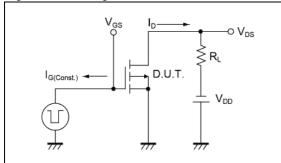


Fig.1-2 Switching Waveforms

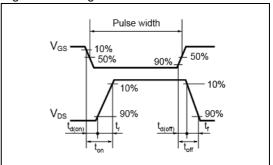
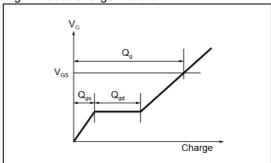


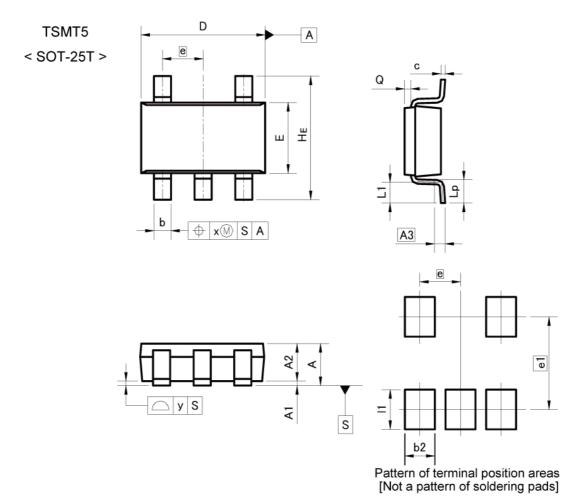
Fig.2-2 Gate Charge Waveform



Notice

- SBD has a large reverse leak current compared to other type of diode. Therefore, it would raise a junction temperature, and increase a reverse power loss. Further rise of inside temperature would cause a thermal runaway. This built-in SBD has low V_F characteristics and therefore, higher leak current. Please consider enough the surrounding temperature, generating heat of MOSFET and the reverse current.
- 2. This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions



DIM MILIME		ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	()	1.00	=	0.039
A1	0.00	0.10	0.000	0.004
A2	0.75	0.95	0.030	0.037
A3	0.	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.10	0.26	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.	95	0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.010
х	8-7	0.20	E.	0.008
у	? -	0.10	#	0.004

MILIMETERS INCHES DIM MIN MIN MAX MAX b2 0.70 0.028 2.10 0.083 e1 11 0.90 0.035

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CL ACCTI	CLASS II b	СГУССШ
CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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