Silicon Carbide Schottky Diode



V _{RRM}	=	1700 V
I F(T _C = 134°C)	=	100 A *
Qc	=	1076 nC *

Features

- Low V_F for High Temperature Operation
- Enhanced Surge and Avalanche Robustness
- Superior Figure of Merit Q_C/I_F
- Low Thermal Resistance
- Low Reverse Leakage Current
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of V_{F}
- Low V_F for High Temperature Operation

A A A ROHS ROHS SOT-227 K K K

Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Improved System Efficiency

Applications

Package

- EV Fast Chargers
- Solar Inverters
- Wind Energy Converters
- Train Auxiliary Power Supplies
- High Frequency Rectifiers
- Switched Mode Power Supplies
- Motor Drives
- Pulsed Power

Absolute Maximum Ratings (At T_c = 25°C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note	
Repetitive Peak Reverse Voltage (Per Leg)	V _{RRM}		1700	٧		
		T _C = 75°C, D = 1	84 / 168			
Continuous Forward Current (Per Leg / Per Device)	lF	I _F T _C = 100°C, D = 1		71 / 142	А	Fig. 4
		T _C = 134°C, D = 1	50 / 100			
Non-Repetitive Peak Forward Surge Current, Half Sine	Isou	T _C = 25°C, t _P = 10 ms	500	٨		
Wave (Per Leg)	IF,SM	T _C = 150°C, t _P = 10 ms	400	A		
Repetitive Peak Forward Surge Current, Half Sine Wave	I	T _C = 25°C, t _P = 10 ms	300	А		
(Per Leg)	I _{F,RM}	T _C = 150°C, t _P = 10 ms	210	A		
Non-Repetitive Peak Forward Surge Current (Per Leg)	I _{F,MAX}	T _C = 25°C, t _P = 10 μs	2500	Α		
i²t Value (Per Leg)	∫i²dt	T _C = 25°C, t _P = 10 ms	1250	A ² s		
Non-Repetitive Avalanche Energy (Per Leg)	E _{AS}	L = 1.0 mH, I _{AS} = 50 A	1301	mJ		
Diode Ruggedness (Per Leg)	dV/dt	V _R = 0 ~ 1360 V	200	V/ns		
Power Dissipation (Per Leg / Per Device)	Ртот	T _C = 25°C	387 / 774	W	Fig. 3	
Operating and Storage Temperature	Tj, Tstg		-55 to 175	°C		

* Per Device

GB2X50MPS17-227 1700V 100A SiC Schottky MPS™ Diode



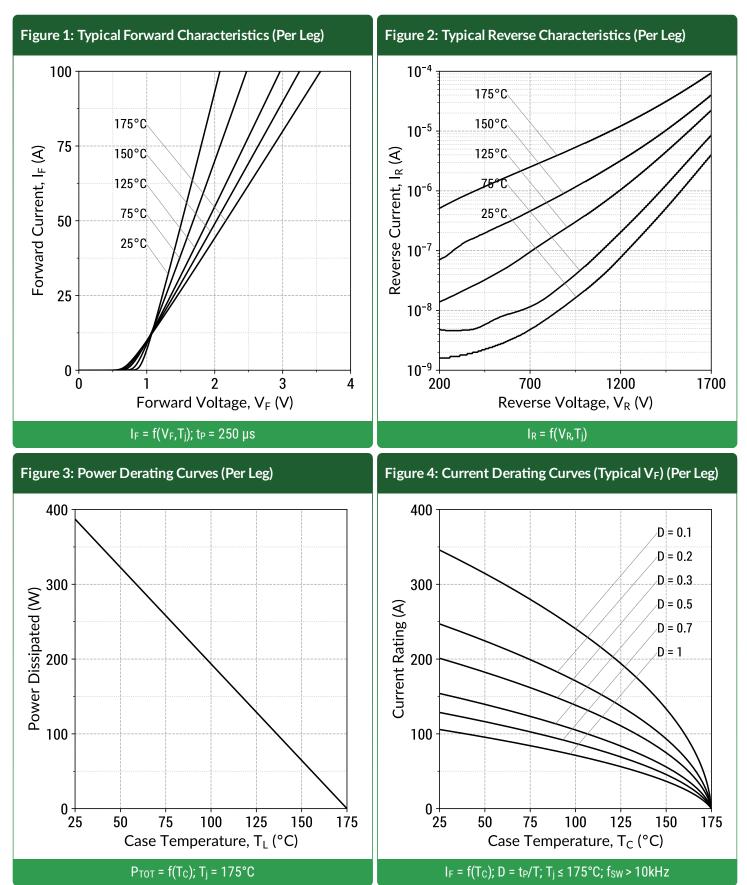
Electrical Characteristics (Per Leg)								
Parameter	Symbol	Conditions		Values			Unit	Note
Falalletei	Symbol			Min.	Тур.	Max.	Unit	Note
Diada Farward Valtaga	¥-	I _F = 50 A, T _j = 25°C			1.5	1.8	V	Fig. 1
Diode Forward Voltage	VF	I _F = 50 A, T _j = 175°C			2.1			
Reverse Current	I-	V _R = 1700 V, T _j = 25°C			2	10	μA	Fig. 2
Reverse Gurrent	IR	V _R = 1700 V, T _j = 175°C			42			
Total Consolitive Charge	Qc		V _R = 600 V		368		nC	Fig. 7
Total Capacitive Charge	QC	I _F ≤ I _{F,MAX}	V _R = 1200 V		538			
Switching Time	+	dl _F /dt = 200 A/µs	V _R = 600 V		< 10		20	
Switching Time	ts	V _R = 1200 V			< 10		ns	
Total Capacitance	С	V _R = 1 V, f = 1MHz			4701		рĘ	Fig. 6
	U U	V _R = 1200 V, f = 1MHz			259		pF	

Thermal/Package Characteristics

Parameter	Symbol	Conditions	Values			l Incia	Note
Paralleler	Symbol	Conditions	Min.	Тур.	Max.	Unit	Note
Thermal Resistance, Junction - Case (Per Leg)	RthJC			0.39		°C/W	Fig. 9
Weight	WT			28.0		g	
Mounting Torque	T _M	Screws to Heatsink			1.5	Nm	-
Terminal Connection Torque	Tc	M4 Screws			1.3	Nm	-
Isolation Voltage(RMS)	Vier	t = 1s (50/60 Hz)	3000			V	
Isolation voltage(RMS)	Viso	t = 60s (50/60 Hz)		2500		v	
Creepage Distance on Surface	d _{Ctt}	Terminal to Terminal		10.5		mm	
creepage distance on Surface	dctb	Terminal to Backside		8.5		mm	
Striking Distance Through Air	dstt	Terminal to Terminal		3.2		mm	
Striking Distance Through An	d _{Stb}	Terminal to Backside		6.8		mm	



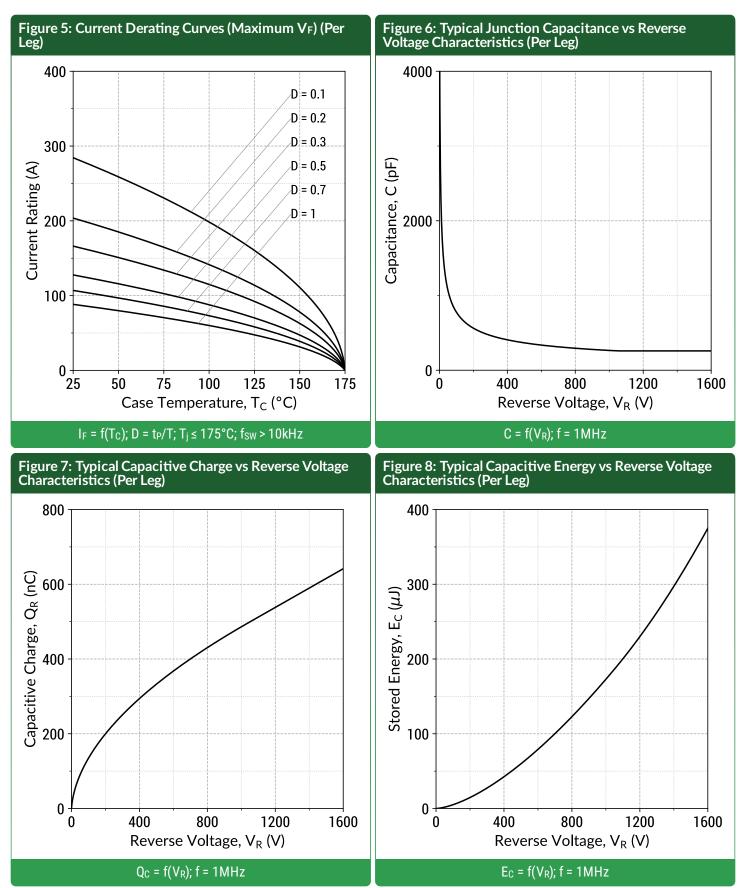




Rev 21/Mar

Latest Version at: www.genesicsemi.com/sic-schottky-mps/GB2X50MPS17-227/GB2X50MPS17-227.pdf Page 3 of 7





Rev 21/Mar

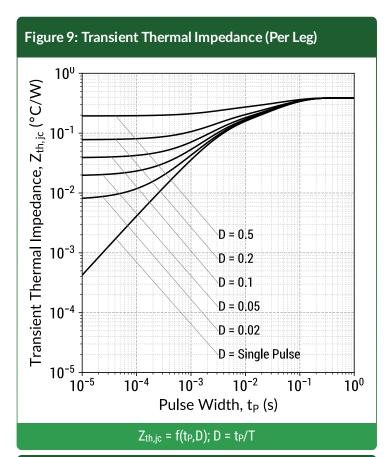
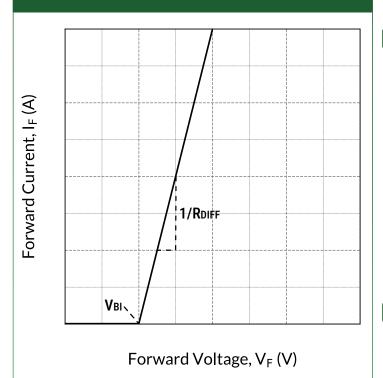


Figure 10: Forward Curve Model (Per Leg)



 $I_F = f(V_F, T_j)$

GeneSic SEMICONDUCTOR

Forward Curve Model Equation:

 $I_F = (V_F - V_{BI})/R_{DIFF} (A)$

Built-In Voltage (V_{BI}):

 $V_{Bl}(T_j) = m \times T_j + n (V)$ m = -0.00128 (V/°C) n = 0.99 (V)

Differential Resistance (RDIFF):

 $R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c (\Omega)$ a = 2.03e-07 (\Omega/\circ{C}^2) b = 7.11e-05 (\Omega/\circ{C}) c = 0.0093 (\Omega)

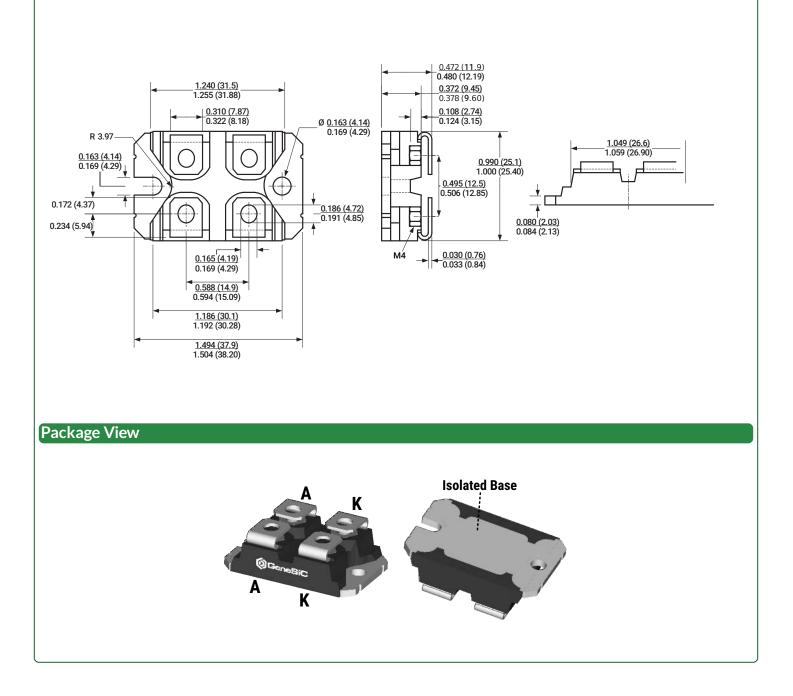
Forward Power Loss Equation:

 $P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$



Package Dimensions

SOT-227 Package Outline



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.



Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

Disclaimer

GeneSiC Semiconductor, Inc. reserves right to make changes to the product specifications and data in this document without notice. GeneSiC disclaims all and any warranty and liability arising out of use or application of any product. No license, express or implied to any intellectual property rights is granted by this document.

Unless otherwise expressly indicated, GeneSiC products are not designed, tested or authorized for use in life-saving, medical, aircraft navigation, communication, air traffic control and weapons systems, nor in applications where their failure may result in death, personal injury and/or property damage.

Related Links

SPICE Models:	https://www.genesicsemi.com/sic-schottky-mps/GB2X50MPS17-227/GB2X50MPS17-227_SPICE.zip			
• PLECS Models:	https://www.genesicsemi.com/sic-schottky-mps/GB2X50MPS17-227/GB2X50MPS17-227_PLECS.zip			
CAD Models:	https://www.genesicsemi.com/sic-schottky-mps/GB2X50MPS17-227/GB2X50MPS17-227_3D.zip			
 Evaluation Boards: https://www.genesicsemi.com/technical-support 				
Reliability:	https://www.genesicsemi.com/reliability			
Compliance:	https://www.genesicsemi.com/compliance			
• Quality Manual:	https://www.genesicsemi.com/quality			

Revision History

- Rev 21/Mar: Updated with most recent data
- Supersedes: Rev 20/Apr, Rev 20/Aug



www.genesicsemi.com/sic-schottky-mps/



Rev 21/Mar Copyright© 2021 GeneSiC Semiconductor Inc. All Rights Reserved.