# ne<mark>x</mark>peria

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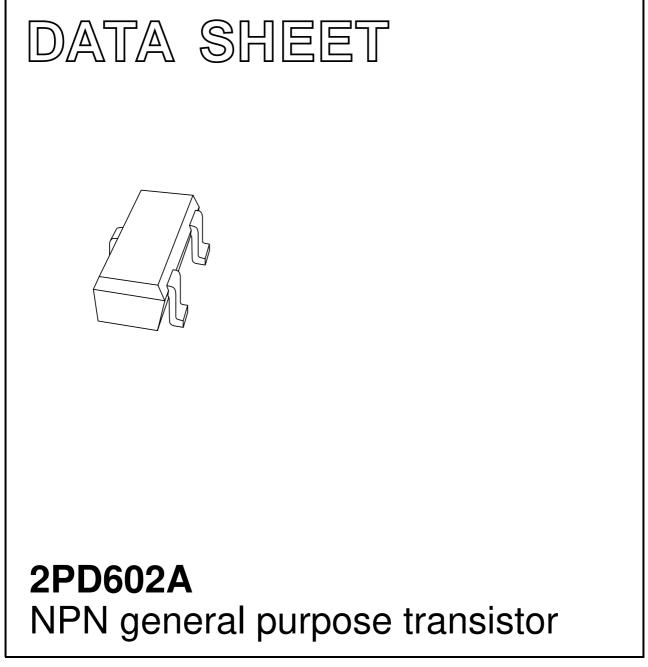
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Kind regards,

Team Nexperia

## DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1997 Jun 20 1999 Apr 23



### Product data sheet

## NPN general purpose transistor

#### FEATURES

- High current (max. 500 mA)
- Low voltage (max. 50 V).

#### APPLICATIONS

• General purpose switching and amplification.

#### DESCRIPTION

NPN transistor in an SC-59 plastic package. PNP complement: 2PB710A.

#### MARKING

TYPE NUMBER	MARKING CODE
2PD602AQ	XQ
2PD602AR	XR
2PD602AS	XS

### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	-	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	5	V
I <sub>C</sub>	collector current (DC)		-	500	mA
I <sub>CM</sub>	peak collector current		-	1	А
I <sub>BM</sub>	peak base current		-	200	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$ ; note 1	-	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	

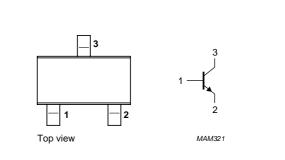


Fig.1 Simplified outline (SC-59) and symbol.

## 2PD602A

## NPN general purpose transistor

## 2PD602A

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 60 V	_	10	nA
		$I_E = 0; V_{CB} = 60 V; T_j = 150 \ ^{\circ}C$	_	5	μA
I <sub>EBO</sub>	emitter cut-off current	$I_{C} = 0; V_{EB} = 4 V$	_	10	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 150 mA; V <sub>CE</sub> = 10 V; note 1			
	2PD602AQ		85	170	
	2PD602AR		120	240	
	2PD602AS		170	340	
	DC current gain	I <sub>C</sub> = 500 mA; V <sub>CE</sub> = 10 V; note 1	40	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{C} = 300 \text{ mA}; I_{B} = 30 \text{ mA}; \text{ note } 1$	_	600	mV
C <sub>c</sub>	collector capacitance	I <sub>E</sub> = i <sub>e</sub> = 0; V <sub>CB</sub> = 10 V; f = 1 MHz	_	15	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 10 V;			
	2PD602AQ	f = 100 MHz; note 1	140	_	MHz
	2PD602AR		160	-	MHz
	2PD602AS		180	_	MHz

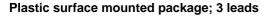
#### Note

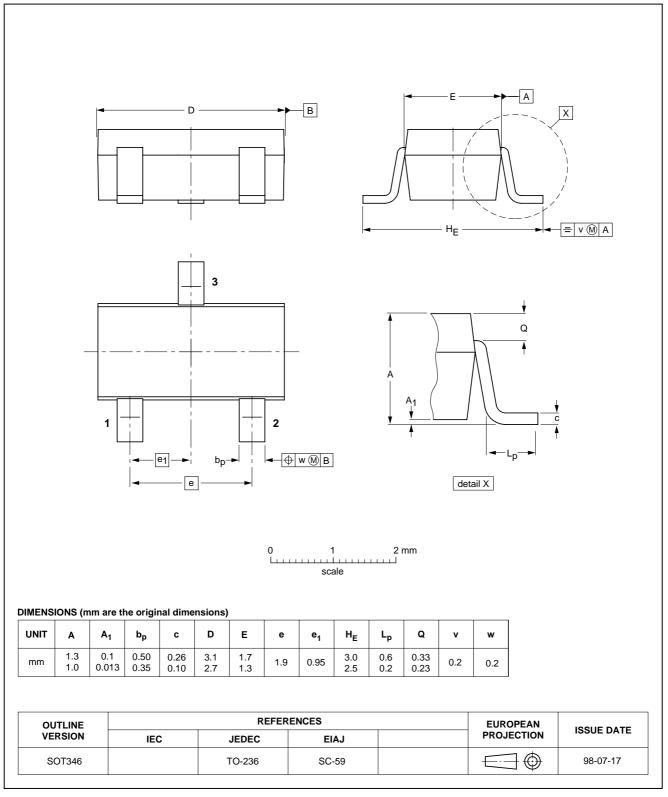
1. Pulse test:  $t_p \leq 300~\mu s;~\delta \leq 0.02.$ 

2PD602A

## NPN general purpose transistor

#### PACKAGE OUTLINE





**SOT346** 

## NPN general purpose transistor

2PD602A

#### DATA SHEET STATUS

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
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## NXP Semiconductors

#### **Customer notification**

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#### **Contact information**

For additional information please visit: http://www.nxp.com For sales offices addresses send e-mail to: salesaddresses@nxp.com

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