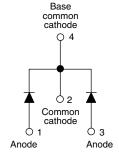


COMPLIANT

High Performance Schottky Rectifier, 2 x 6 A





D-PAK (T	O-252AA)
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PRODUCT SUMMARY							
Package	D-PAK (TO-252AA)						
I _{F(AV)}	2 x 6 A						
V_R	60 V						
V _F at I _F	0.57 V						
I _{RM}	35 mA at 125 °C						
T _J max.	150 °C						
Diode variation	Common cathode						
E _{AS}	7 mJ						

FEATURES

- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-12CWQ06FNPbF surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	12	A						
V_{RRM}		60	V						
I _{FSM}	t _p = 5 μs sine	320	A						
V _F	6 A _{pk} , T _J = 125 °C (per leg)	0.57	V						
T _J	Range	-55 to +150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-12CWQ06FNPbF	UNITS					
Maximum DC reverse voltage	V_{R}	60	V					
Maximum working peak reverse voltage	V_{RWM}	00	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS				
Maximum average per forward current	por rog		6	Α				
See fig. 5 per de	ice I _{F(AV)}	30 % duty cycle at 18 = 131 C	12	A				
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	320	А			
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	105				
Non-repetitive avalanche energy per le	g E _{AS}	T _J = 25 °C, I _{AS} = 1.2 A, L = 10 mH		7	mJ			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.8	А			



ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
Maximum forward voltage drop per leg See fig. 1		6 A	T _{.1} = 25 °C	0.61				
	V _{FM} ⁽¹⁾	12 A	1j=25 C	0.79	V			
	VFM (')	6 A	T _ 105 °C	0.57				
		12 A	T _J = 125 °C	0.72				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	3	mA			
See fig. 2	I 'RM ` ′	$T_{\rm J} = 125~{\rm °C}$		35	IIIA			
Threshold voltage	V _{F(TO)}	$T_{.1} = T_{.1}$ maximum	0.36	V				
Forward slope resistance	r _t	ıj=ıjınaxımum	24.14	mΩ				
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} , (test signal ran	360	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 m	5.0	nH				

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C			
Maximum thermal resistance,	per leg	D	DC operation	3.0	°C/W			
junction to case	per device	R_{thJC}	See fig. 4	1.5	C/ VV			
Approximate weight				0.3	g			
Approximate weight				0.01	OZ.			
Marking device			Case style D-PAK (similar to TO-252AA)	12CW	Q06FN			

Note

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

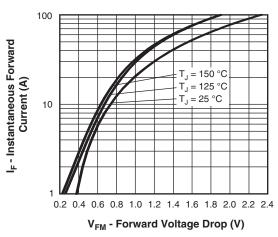


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

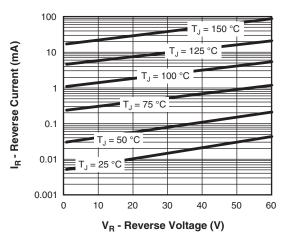


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

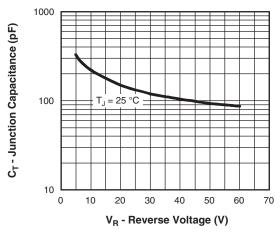


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

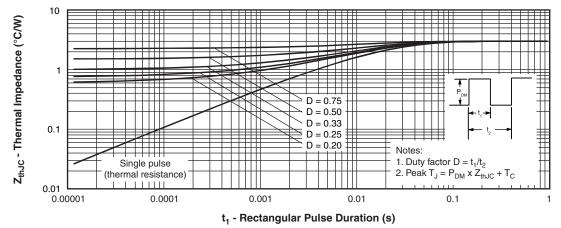


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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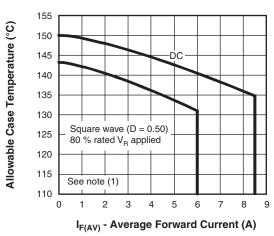


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

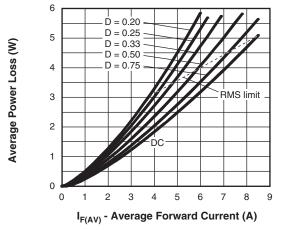


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

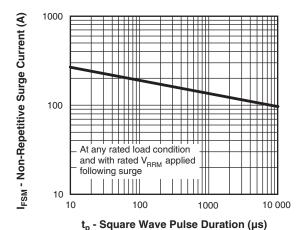


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

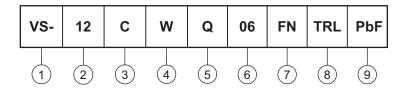
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Current rating (12 A)

Center tap configuration-

4 - Package identifier:

4 W = D-PAK

5 - Schottky "Q" series

6 - Voltage rating (06 = 60 V)

7 - FN = TO-252AA

None = tube (50 pieces)

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

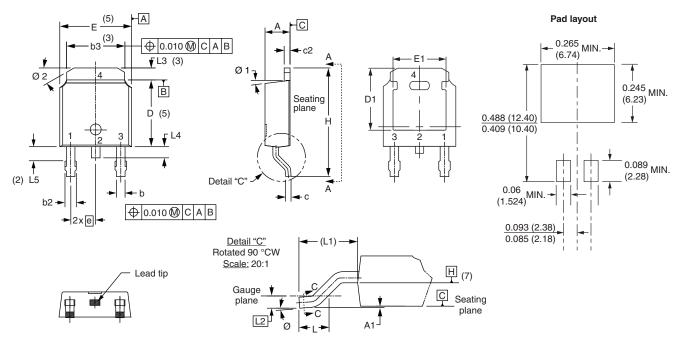
9 - PbF = lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95016</u>						
Part marking information	www.vishay.com/doc?95059					
Packaging information	www.vishay.com/doc?95033					
SPICE model	www.vishay.com/doc?95278					



D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



CVMDOL			INCHES		RS INCHES		MILLIMETERS INCHES		NOTES	NOTES	ES NOTES		SYMBOL	MILLIM	IETERS	INC	HES	NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES						
Α	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC							
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410							
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070							
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.							
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC							
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3						
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040							
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2						
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°							
Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°							
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°							

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC outline TO-252AA



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