# RGTV80TK65

## 650V 40A Field Stop Trench IGBT

Datasheet

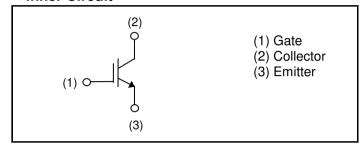
$V_{CES}$	650V
I <sub>C (100°C)</sub>	23A
V <sub>CE(sat) (Typ.)</sub>	1.5V
$P_D$	85W

# Outline TO-3PFM

## Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching & Low Switching Loss
- 3) Short Circuit Withstand Time 2µs
- 4) Pb free Lead Plating; RoHS Compliant

## ●Inner Circuit



## Application

Solar Inverter

**UPS** 

Welding

ΙH

**PFC** 

Packaging Specifications

• Packaging Specifications					
	Packaging	Tube			
	Reel Size (mm)	-			
Type	Tape Width (mm)	-			
Type	Basic Ordering Unit (pcs)	450			
	Packing Code	C11			
	Marking	RGTV80TK65			

# ● **Absolute Maximum Ratings** (at T<sub>C</sub> = 25°C unless otherwise specified)

	0	, , , , , , , , , , , , , , , , , , ,		
Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CES</sub>	650	V
Gate - Emitter Voltage		V <sub>GES</sub>	±30	V
Collector Current	T <sub>C</sub> = 25°C	I <sub>C</sub>	39	Α
	T <sub>C</sub> = 100°C	I <sub>C</sub>	23	Α
Pulsed Collector Current		I <sub>CP</sub> *1	160	Α
Power Dissipation	T <sub>C</sub> = 25°C	P <sub>D</sub>	85	W
	T <sub>C</sub> = 100°C	P <sub>D</sub>	42	W
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C

<sup>\*1</sup> Pulse width limited by T<sub>imax.</sub>

## ●Thermal Resistance

Parameter	Symbol	Values			Unit
Farameter	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	1	1.75	°C/W

# ●IGBT Electrical Characteristics (at T<sub>i</sub> = 25°C unless otherwise specified)

Parameter	Cumbal	Conditions	Values			l loit
Parameter	Symbol		Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	ı	1	V
Collector Cut - off Current	I <sub>CES</sub>	$V_{CE} = 650V, V_{GE} = 0V$	-	ı	10	μΑ
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V$ , $V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 27.5 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_{C} = 40A, V_{GE} = 15V,$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.5 1.85	1.9 -	V

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Davamatav	Symbol	Conditions		l lmit		
Parameter			Min.	Тур.	Max.	Unit
Input Capacitance	C <sub>ies</sub>	$V_{CE} = 30V$ ,	-	2370	-	
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$ ,	-	94	-	рF
Reverse transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	38	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 400V,	-	81	-	
Gate - Emitter Charge	$Q_ge$	$I_C = 40A$ ,	-	17	-	nC
Gate - Collector Charge	$Q_{gc}$	$V_{GE} = 15V$	-	31	-	
Turn - on Delay Time	t <sub>d(on)</sub>		-	39	-	
Rise Time	t <sub>r</sub>	$I_{C} = 40A, V_{CC} = 400V,$ $V_{GE} = 15V, R_{G} = 10\Omega,$	-	17	-	
Turn - off Delay Time	$t_{d(off)}$	$T_i = 25^{\circ}C$	-	113	-	ns ns
Fall Time	t <sub>f</sub>	Inductive Load	-	45	-	
Turn - on Switching Loss	E <sub>on</sub>	*E <sub>on</sub> include diode reverse recovery	-	1.02	-	mJ
Turn - off Switching Loss	E <sub>off</sub>	,	-	0.71	-	
Turn - on Delay Time	t <sub>d(on)</sub>		-	38	-	
Rise Time	t <sub>r</sub>	$I_{C} = 40A, V_{CC} = 400V,$ $V_{GE} = 15V, R_{G} = 10\Omega,$	-	19	-	ns
Turn - off Delay Time	t <sub>d(off)</sub>	$T_i = 175^{\circ}C$	-	130	-	
Fall Time	t <sub>f</sub>	Inductive Load *E <sub>on</sub> include diode reverse recovery	-	86	-	
Turn - on Switching Loss	E <sub>on</sub>		-	1.07	-	mJ
Turn - off Switching Loss	E <sub>off</sub>		-	1.01	-	1110
Reverse Bias Safe Operating Area		$I_C = 160A, V_{CC} = 520V,$	FULL SQUARE		-	
	RBSOA	$V_P = 650V, V_{GE} = 15V,$				
		$R_G = 100\Omega, T_j = 175^{\circ}C$				ı
		$V_{CC} \le 360V$ ,				
Short Circuit Withstand Time	t <sub>sc</sub>	V <sub>GE</sub> = 15V,	2	-	-	μs
		$T_j = 25^{\circ}C$				

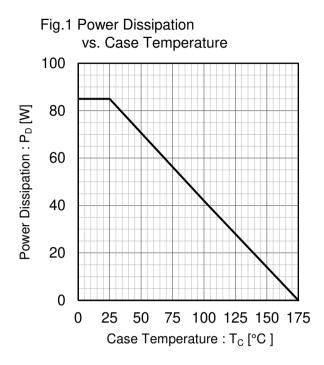


Fig.2 Collector Current vs. Case Temperature  $\begin{array}{c} 50 \\ \hline 40 \\ \hline 2 \\ \hline \end{array}$   $\begin{array}{c} 40 \\ \hline 20 \\ \hline \end{array}$   $\begin{array}{c} 7 \\ \hline \end{array}$   $\begin{array}{c} 10 \\ \hline \end{array}$   $\begin{array}{c} 7 \\ \hline \end{array}$   $\begin{array}{c} 175^{\circ}\text{C} \\ \hline \end{array}$   $\begin{array}{c} 7 \\ \hline \end{array}$   $\begin{array}{c} 175^{\circ}\text{C} \\ \hline \end{array}$   $\begin{array}{c} 7 \\ \hline \end{array}$   $\begin{array}{c} 175^{\circ}\text{C} \\ \hline \end{array}$   $\begin{array}{c} 25 \\ \hline \end{array}$   $\begin{array}{c} 50 \\ \hline \end{array}$   $\begin{array}{c} 7 \\ \hline \end{array}$   $\begin{array}{c} 175^{\circ}\text{C} \\ \hline \end{array}$   $\begin{array}{c} 7 \\ \hline \end{array}$   $\begin{array}{c} 25 \\ \hline \end{array}$   $\begin{array}{c} 50 \\ \hline \end{array}$   $\begin{array}{c} 75 \\ \hline \end{array}$   $\begin{array}{c} 175^{\circ}\text{C} \\ \hline \end{array}$   $\begin{array}{c} 75 \\ \hline \end{array}$   $\begin{array}{c} 175^{\circ}\text{C} \\ \hline \end{array}$   $\begin{array}{c} 75 \\ \hline \end{array}$   $\begin{array}{c} 175 \\ \hline \end{array}$ 

Fig.3 Forward Bias Safe Operating Area

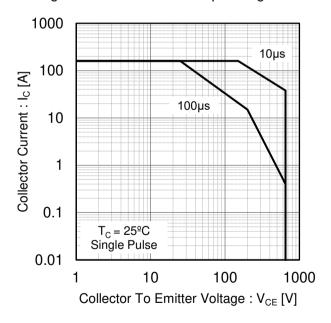


Fig.4 Reverse Bias Safe Operating Area

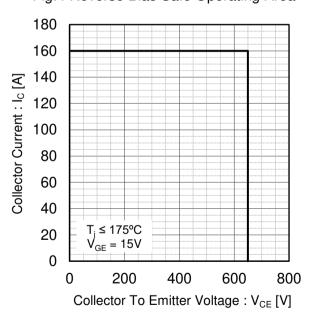


Fig.5 Typical Output Characteristics

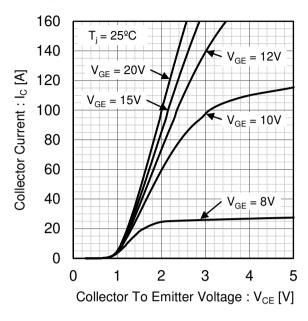


Fig.6 Typical Output Characteristics

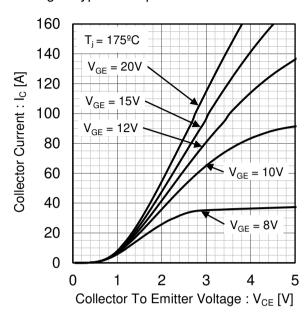


Fig.7 Typical Transfer Characteristics

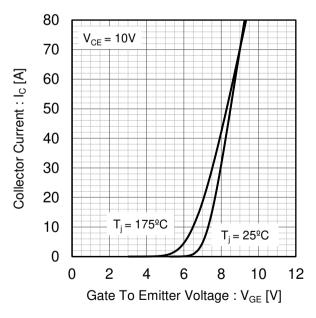
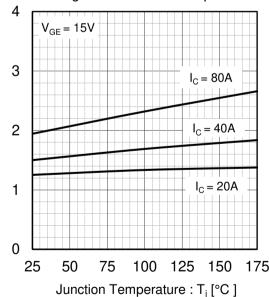


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



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Collector To Emitter Saturation

Voltage: V<sub>CE(sat)</sub> [V]

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

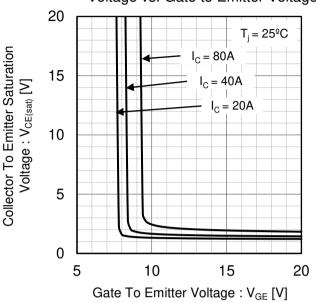


Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

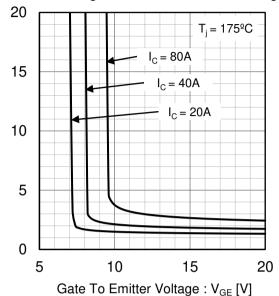


Fig.11 Typical Switching Time vs. Collector Current

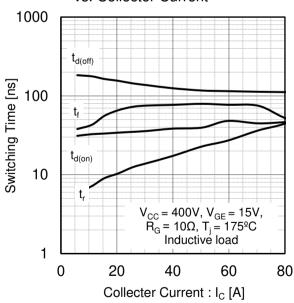
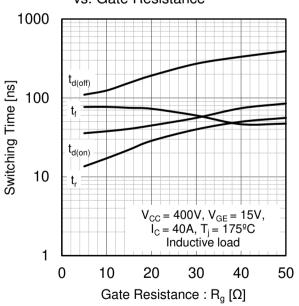


Fig.12 Typical Switching Time vs. Gate Resistance



Collector To Emitter Saturation

Voltage: V<sub>CE(sat)</sub> [V]

Fig.13 Typical Switching Energy Losses vs. Collector Current

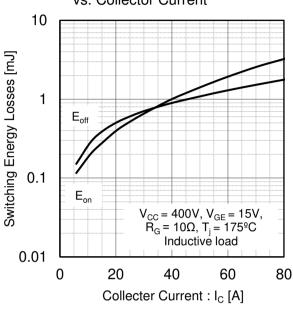


Fig.14 Typocal Switching Energy Losses vs. Gate Resistance

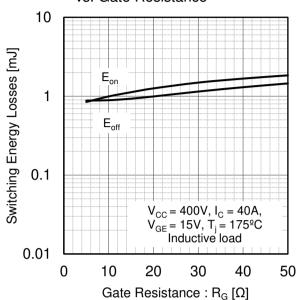


Fig.15 Typical Capacitance vs. Collector to Emitter Voltage

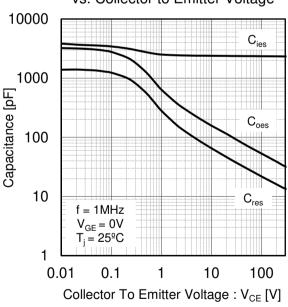
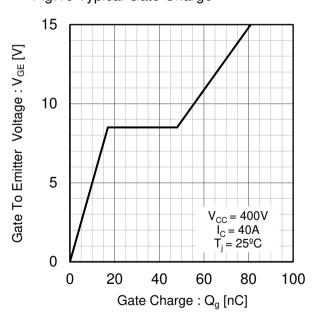
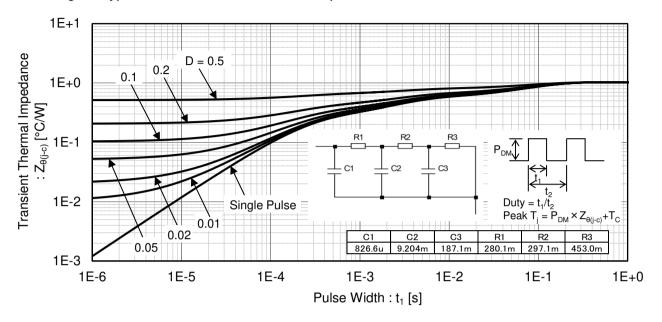


Fig.16 Typical Gate Charge

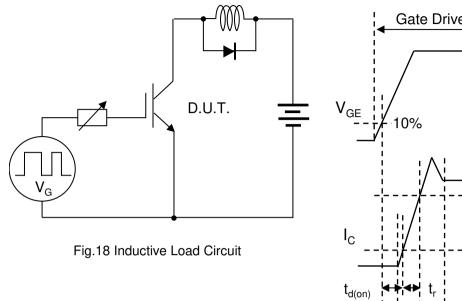


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Fig.17 Typical IGBT Transient Thermal Impedance



## ●Inductive Load Switching Circuit and Waveform



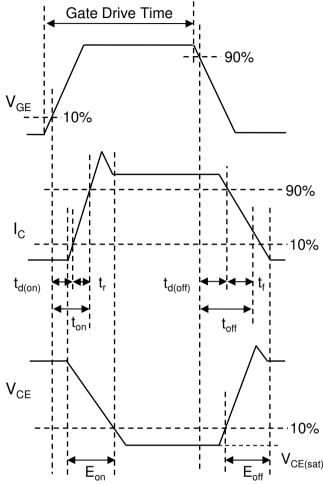


Fig.19 Inductive Load Waveform

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