



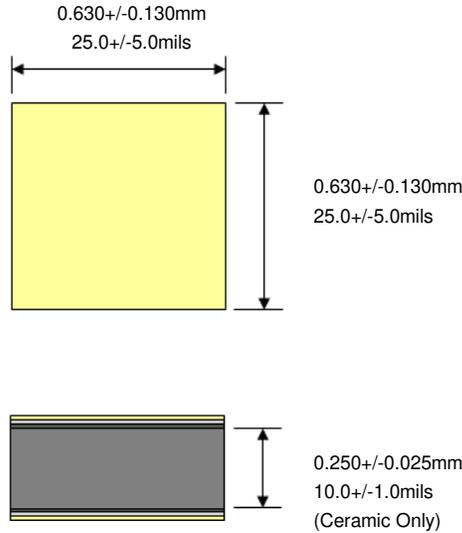
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SPECIFICATION SHEET



Manufactured to metric dimensions. Imperial units are for reference only.

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|--|---|
| Part Number: | SKT03C471M11A1 |
| Dielectric Constant(K): | 30000 |
| Capacitance Value: | 470[pF] @ 1kHz, 1 Vrms, 25°C, No DC Bias |
| Capacitance tolerance: | M (tolerance: ± 20%) |
| Dissipation Factor (DF): | 2.5% Max @ 1kHz, 1 Vrms, 25°C, No DC Bias |
| Rated Working Voltage (RWV): | 50 V |
| Insulation Resistance (IR): | 10,000MΩ Min @ 50Vdc, 25°C |
| Dielectric Withstanding Voltage (DWV): | No breakdown @ 125Vdc x 2sec, 25°C |
| Temperature Characteristic of Capacitance: | ±22% (X7S @ -55°C to +125°C)@ No DC Bias |
| DC Bias Characteristics: | -35% @ 50Vdc, 1kHz, 1 Vrms, 25°C |
| Metallization: | |
| Top: | Ti-Pt-Au 1.5μm Min |
| Bottom: | Ti-Pt-Au 1.5μm Min |

NOTES:

- Other specifications not listed are available at www.tecdia.com. Specifications may be subject to change without prior notice.
- RoHS compliant.
- Wire bonding location should be 25um or further from edges of the electrode to avoid electrode peeling.
- Capacitance, Temperature Coefficient and Dissipation Factor are measured before any AC or DC bias has been applied.
- Recommended Storage Conditions (Waffle Packaging): 23 +/- 10°C @ 60% RH Max
- Guaranteed Shelf Life: 1 year after delivery under recommended storage conditions.
- Successful wire bonding and die attachment are dependent on the types of bonding tools and conditions used. Please check the wire bonding and die attach conditions of your site to prevent the wire/electrode from peeling or detaching.
Tecdia is not responsible for mechanical issues such as cracking or detaching that can occur when solder die mounting.

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| PREPARED BY: M. Simpson 2020/6/2 | DESCRIPTION: CHIP CAPACITOR | Scale: Not to Scale |
| APPROVED BY: T. Yoshikawa 2020/6/2 | TECDIA PART NUMBER: SKT03C471M11A1 | SHEET: 1 of 1 |