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## ON Semiconductor®

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December 2010

## NC7SP04 — TinyLogic® ULP Inverter

#### **Features**

- 0.9V to 3.6V V<sub>CC</sub> Supply Operation
- 3.6V Over-Voltage Tolerant I/Os at V<sub>CC</sub> from 0.9V to 3.6V
- Propagation Delay (t<sub>PD</sub>):
  - 4.0ns Typical for 3.0V to 3.6V V<sub>CC</sub>
  - 5.0ns Typical for 2.3V to 2.7V V<sub>CC</sub>
  - 6.0ns Typical for 1.65V to 1.95V V<sub>CC</sub>
  - 7.0ns Typical for 1.40V to 1.60V V<sub>CC</sub>
  - 11.0ns Typical for 1.10V to 1.30V V<sub>CC</sub>
  - 27.0ns Typical for 0.90V V<sub>CC</sub>
- Power-Off High-Impedance Inputs and Outputs
- Static Drive (I<sub>OH</sub>/I<sub>OL</sub>):
  - ± 2.6mA at 3.00V V<sub>CC</sub>
  - ± 2.1mA at 2.30V V<sub>CC</sub>
  - ± 1.5mA at 1.65V V<sub>CC</sub>
  - ± 1.0mA at 1.40V V<sub>CC</sub>
  - ± 0.5mA at 1.10V V<sub>CC</sub>
  - ± 20µA at 0.9V V<sub>CC</sub>
- Quiet Series™ Noise / EMI Reduction Circuitry
- Ultra Small MicroPak™ Packages
- Ultra Low Dynamic Power

### **Description**

The NC7SP04 is a single inverter from Fairchild's Ultra Low Power (ULP) series of TinyLogic®. Ideal for applications where battery life is critical, this product is designed for ultra low power consumption within the  $V_{\rm CC}$  operating range of 0.9V to 3.6V.

The internal circuit is composed of a minimum of inverter stages, including the output buffer, to enable ultra low static and dynamic power.

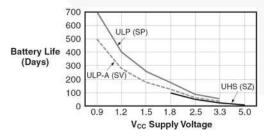
The NC7SP04, for lower drive requirements, is uniquely designed for optimized power and speed and is fabricated with an advanced CMOS technology to achieve best-in-class speed of operation, while maintaining extremely low CMOS power dissipation.

#### **Related Resources**

 AN-5055 — Portability and Ultra Low Power <u>TinyLogic®</u>

## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
NC7SP04P5X	P04	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3000 Units on Tape & Reel
NC7SP04L6X	J6	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel
NC7SP04FHX	J6	6-Lead, MicroPak2™, 1x1mm Body, .35mm Pitch	5000 Units on Tape & Reel



#### Notes:

- 1. TinyLogic ULP and ULP-A with up to 50% less power consumption can extend battery life significantly.
- Battery Life=(V<sub>battery</sub> x I<sub>battery</sub> x 0.9) / (P<sub>device</sub>) / 24hrs/day; where, P<sub>device</sub>=(I<sub>CC</sub> x V<sub>CC</sub>) + (C<sub>PD</sub> + C<sub>L</sub>) x V<sub>CC</sub><sup>2</sup> x f.
- Assumes ideal 3.6V Lithium Ion battery with current rating of 900mAH and derated 90% and device frequency at 10MHz, with C<sub>L</sub>=15pF load.

Figure 1. Battery Life vs. V<sub>CC</sub> Supply Voltage

## **Connection Diagrams**

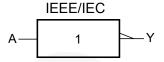


Figure 2. Logic Symbol

## **Pin Configurations**

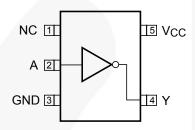


Figure 3. SC70 (Top View)

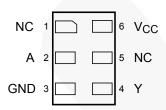


Figure 4. MicroPak™ (Top Through View)

## **Function Table**

Inputs	Output
Α	Y
L	Н
Н	L

L = Low Logic Level H = High Logic Level

## **Pin Definitions**

Pin # SC70	Pin # MicroPak™	Name	Description
1	1, 5	NC	No Connect
2	2	Α	Input
3	3	GND	Ground
4	4	Y	Output
5	6	V <sub>CC</sub>	Supply Voltage

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Para	meter	Min.	Max.	Unit	
V <sub>CC</sub>	Supply Voltage		-0.5	4.6	V	
V <sub>IN</sub>	DC Input Voltage		-0.5	4.6	V	
V	DC Output Voltage	HIGH or LOW State <sup>(4)</sup>	-0.5	V <sub>CC</sub> to +0.5	V	
V <sub>OUT</sub>	DC Output Voltage	V <sub>CC</sub> =0V	-0.5	4.6	V	
I <sub>IK</sub>	DC Input Diode Current at V <sub>IN</sub> <	0V		-50	mA	
	DC Output Diada Current	V <sub>OUT</sub> < 0V		-50	m Λ	
I <sub>OK</sub>	DC Output Diode Current	V <sub>OUT</sub> > V <sub>CC</sub>		+50	mA	
I <sub>OH</sub> / I <sub>OL</sub>	DC Output Source/Sink Current			±50	mA	
I <sub>CC</sub> or Ground	DC V <sub>CC</sub> or Ground Current per S	Supply Pin		±50	mA	
T <sub>STG</sub>	Storage Temperature Range		-65	+150	°C	
TJ	Junction Temperature Under Bi	as		+150	°C	
$T_L$	Junction Lead Temperature (So	ldering, 10 Seconds)		+260	°C	
A		SC70-5		150		
$P_{D}$	P <sub>D</sub> Power Dissipation at +85°C	MicroPak™-6		130	mW	
		MicroPak2™-6		120		
ESD	Human Body Model	JEDEC: JESD22-A114		4000	V	
ESD	Charged Device Model	JEDEC: JESD22-C101		2000	V	

#### Note:

4. The I<sub>O</sub> maximum rating must be observed.

## **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
Vcc	Supply Voltage		0.9	3.6	V	
V <sub>IN</sub>	Input Voltage <sup>(5)</sup>		0	3.6	V	
.,	Outrot Valtage	HIGH or LOW State	0	V <sub>CC</sub>	V	
V <sub>OUT</sub>	Output Voltage	V <sub>CC</sub> =0V	0	3.6	]	
		V <sub>CC</sub> =3.0V to 3.6V		±2.6		
		V <sub>CC</sub> =2.3V to 2.7V		±2.1	mA	
	Output Compant in 1 /1	V <sub>CC</sub> =1.65V to 1.95V	4.9/	±1.5		
I <sub>OH</sub> / I <sub>OL</sub>	Output Current in I <sub>OH</sub> / I <sub>OL</sub>	V <sub>CC</sub> =1.40V to 1.60V		±1.0		
		V <sub>CC</sub> =1.10V to 1.30V		±0.5		
		V <sub>CC</sub> =0.9V		20.0	μΑ	
T <sub>A</sub>	Free Air Operating Temperature		-40	+85	°C	
Δt / ΔV	Minimum Input Edge Rate	V <sub>IN</sub> =0.8V to 2.0V, V <sub>CC</sub> =3.0V		10	ns/V	
		SC70-5		425		
$\theta_{JA}$	Thermal Resistance	MicroPak™-6		500	°C/W	
		MicroPak2™-6		560		

#### Note:

5. Unused inputs must be held HIGH or LOW. They may not float.

## **DC Electrical Characteristics**

Cumbal	Dovemeter	V <sub>cc</sub>	Conditions	T <sub>A</sub> =25°C		T <sub>A</sub> =-40 to 85°C		Units
Symbol	Parameter	V CC	Conditions	Min.	Max.	Min.	Max.	Unii
		0.90		0.65 x V <sub>CC</sub>		0.65 x V <sub>CC</sub>		
		1.10 ≤ V <sub>CC</sub> ≤ 1.30		0.65 x V <sub>CC</sub>		0.65 x V <sub>CC</sub>		•
.,	HIGH Level Input	1.40 ≤ V <sub>CC</sub> ≤ 1.60		0.65 x V <sub>CC</sub>		0.65 x V <sub>CC</sub>		V
$V_{IH}$	Voltage	1.65 ≤ V <sub>CC</sub> ≤ 1.95		0.65 x V <sub>CC</sub>		0.65 x V <sub>CC</sub>		V
		2.30 ≤ V <sub>CC</sub> ≤ 2.70		1.6		1.6		•
		3.00 ≤ V <sub>CC</sub> ≤ 3.60		2.1		2.1		
		0.90			0.35 x V <sub>CC</sub>		0.35 x V <sub>CC</sub>	
		1.10 ≤ V <sub>CC</sub> ≤ 1.30			0.35 x V <sub>CC</sub>		0.35 x V <sub>CC</sub>	
	LOW Level Input	1.40 ≤ V <sub>CC</sub> ≤ 1.60			0.35 x V <sub>CC</sub>		0.35 x V <sub>CC</sub>	
$V_{IL}$	Voltage	1.65 ≤ V <sub>CC</sub> ≤ 1.95			0.35 x V <sub>CC</sub>		0.35 x V <sub>CC</sub>	V
		2.30 ≤ V <sub>CC</sub> ≤ 2.70			0.7		0.7	
		3.00 ≤ V <sub>CC</sub> ≤ 3.60			0.9		0.9	
		0.90		V <sub>CC</sub> - 0.1	1	V <sub>CC</sub> - 0.1		
		1.10 ≤ V <sub>CC</sub> ≤ 1.30		V <sub>CC</sub> - 0.1		V <sub>CC</sub> - 0.1		
	/	1.40 ≤ V <sub>CC</sub> ≤ 1.60		V <sub>CC</sub> - 0.1		V <sub>CC</sub> - 0.1		
	7	1.65 ≤ V <sub>CC</sub> ≤ 1.95	— I <sub>OH</sub> =-20μA	V <sub>CC</sub> - 0.1		V <sub>CC</sub> - 0.1		
	7/4	2.30 ≤ V <sub>CC</sub> ≤ 2.70		V <sub>CC</sub> - 0.1		V <sub>CC</sub> - 0.1		
$V_{OH}$	HIGH Level Output Voltage	3.00 ≤ V <sub>CC</sub> ≤ 3.60		V <sub>CC</sub> - 0.1		V <sub>CC</sub> - 0.1		V
	Output Voltage	1.10 ≤ V <sub>CC</sub> ≤ 1.30	I <sub>OH</sub> =-0.5mA	0.75 x V <sub>CC</sub>		0.75 x V <sub>CC</sub>		
		1.40 ≤ V <sub>CC</sub> ≤ 1.60	I <sub>OH</sub> =-1mA	1.07		0.99		
		1.65 ≤ V <sub>CC</sub> ≤ 1.95	I <sub>OH</sub> =-1.5mA	1.24		1.22		
		2.30 ≤ V <sub>CC</sub> ≤ 2.70	I <sub>OH</sub> =-2.1mA	1.95		1.87		
		3.00 ≤ V <sub>CC</sub> ≤ 3.60	I <sub>OH</sub> =-2.6mA	2.61		2.55		
		0.90	Y /		0.1		0.1	
		1.10 ≤ V <sub>CC</sub> ≤ 1.30			0.1		0.1	
		1.40 ≤ V <sub>CC</sub> ≤ 1.60			0.1	/	0.1	
		1.65 ≤ V <sub>CC</sub> ≤ 1.95	I <sub>OL</sub> =20μA		0.1		0.1	
		2.30 ≤ V <sub>CC</sub> ≤ 2.70			0.1		0.1	
$V_{OL}$	LOW Level Output Voltage	3.00 ≤ V <sub>CC</sub> ≤ 3.60			0.1		0.1	٧
	Voltage	1.10 ≤ V <sub>CC</sub> ≤ 1.30	I <sub>OL</sub> =0.5mA		0.30 x V <sub>CC</sub>		0.30 x V <sub>CC</sub>	
		1.40 ≤ V <sub>CC</sub> ≤ 1.60	I <sub>OL</sub> =1mA		0.31		0.37	•
		1.65 ≤ V <sub>CC</sub> ≤ 1.95	I <sub>OL</sub> =1.5mA		0.31		0.35	
	2.30 ≤ V <sub>CC</sub> ≤ 2.70	I <sub>OL</sub> =2.1mA		0.31		0.33		
		3.00 ≤ V <sub>CC</sub> ≤ 3.60	I <sub>OL</sub> =2.6mA		0.31		0.33	
I <sub>IN</sub>	Input Leakage Current	0.90 to 3.60	0 ≤ V <sub>I</sub> ≤ 3.6V		±0.1		±0.5	μA
I <sub>OFF</sub>	Power Off	0	$0 \le (V_O, V_I) \le 3.6V$		0.5		0.5	μA
I <sub>CC</sub>	Quiescent Supply Current	0.90 to 3.60	V <sub>IN</sub> =V <sub>CC</sub> or GND		0.9		0.9	μA

## **AC Electrical Characteristics**

Comphal	Davamatav	W	Conditions		T <sub>A</sub> =25°(		T <sub>A</sub> =-40	) to 85°C	l lmita	F:	
Symbol	Parameter	V <sub>cc</sub>	Conditions	Min.	Тур.	Max.	Min.	Max.	Units	Figure	
		0.90			27.0						
		$1.10 \le V_{CC} \le 1.30$		3.5	11.0	21.8	3.0	34.3			
		$1.40 \le V_{CC} \le 1.60$	C <sub>L</sub> =10pF,	2.5	7.0	14.8	2.0	15.0			
		1.65 ≤ V <sub>CC</sub> ≤ 1.95	$R_L=1M\Omega$	2.0	6.0	12.0	1.5	12.2			
		$2.30 \le V_{CC} \le 2.70$		1.5	5.0	9.4	1.0	9.9			
		$3.00 \le V_{CC} \le 3.60$		1.0	4.0	8.3	1.0	9.0			
		0.90			30.0					Figure 1,	
		$1.10 \le V_{CC} \le 1.30$	$C_L$ =15pF, $R_L$ =1M $\Omega$	4.0	11.0	22.8	3.5	37.3	ns		
	Propagation Delay	1.40 ≤ V <sub>CC</sub> ≤ 1.60		3.0	8.0	15.5	2.5	16.5			
t <sub>PHL</sub> , t <sub>PLH</sub>	Propagation Delay	1.65 ≤ V <sub>CC</sub> ≤ 1.95		R <sub>L</sub> =1MΩ	2.5	6.0	12.6	2.0	13.6	115	Figure 2
		$2.30 \le V_{CC} \le 2.70$			2.0	5.0	9.9	1.5	10.8		
		$3.00 \le V_{CC} \le 3.60$		1.5	4.0	8.7	1.0	9.5			
		0.90			32.0						
		1.10 ≤ V <sub>CC</sub> ≤ 1.30		5.0	13.0	25.9	4.0	46.3			
		1.40 ≤ V <sub>CC</sub> ≤ 1.60	C <sub>L</sub> =30pF,	4.0	9.0	17.8	3.5	18.2			
		1.65 ≤ V <sub>CC</sub> ≤ 1.95	$R_L=1M\Omega$	3.0	7.0	14.4	2.0	15.9			
		2.30 ≤ V <sub>CC</sub> ≤ 2.70		2.0	6.0	11.3	1.5	12.8			
		$3.00 \le V_{CC} \le 3.60$		1.5	5.0	9.2	1.0	10.7			
C <sub>IN</sub>	Input Capacitance	0			2					pF	
C <sub>PD</sub>	Power Dissipation Capacitance	0.90 to 3.60	V <sub>IN</sub> =0V or V <sub>CC</sub> , f=10MHz		8					pF	

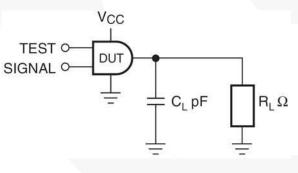


Figure 5. AC Test Circuit

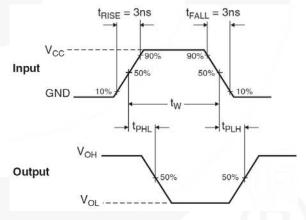


Figure 6. AC Waveforms

Symbol		V <sub>cc</sub>				
	3.3V ± 0.3V	2.5V ± 0.2V	1.8V ± 0.15V	1.5V ± 0.1V	1.2V ± 0.1V	0.9V
$V_{\text{mi}}$	1.5V	V <sub>CC</sub> / 2				
$V_{mo}$	1.5V	V <sub>CC</sub> / 2				

## **Physical Dimensions**

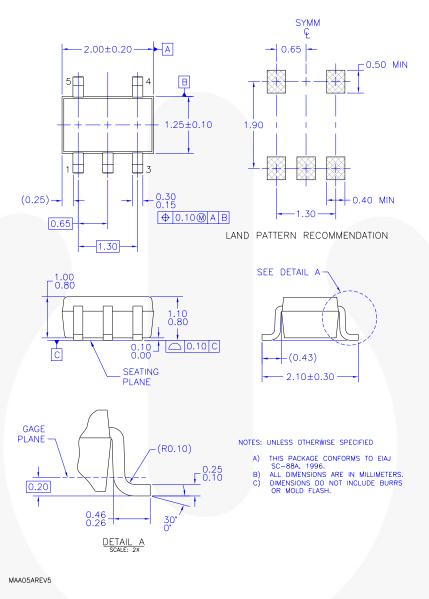


Figure 7. 5-Lead, SC70, EIAJ SC-88a, 1.25mm Wide

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

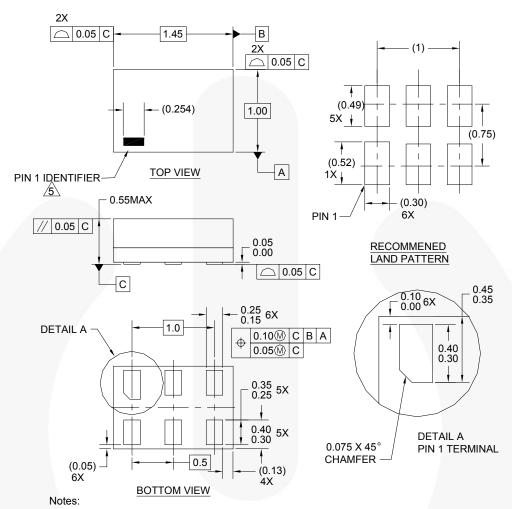
Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

#### **Tape and Reel Specification**

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/products/analog/pdf/sc70-5 tr.pdf.

Package Designator	ackage Designator Tape Section		<b>Cavity Status</b>	Cover Type Status	
	Leader (Start End)	125 (Typical)	Empty	Sealed	
P5X	Carrier	3000	Filled	Sealed	
	Trailer (Hub End)	75 (Typical)	Empty	Sealed	

## **Physical Dimensions**



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994
- 4. FILENAME AND REVISION: MAC06AREV4
- 5 PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

OTHER LINE IN THE MARK CODE LAYOUT.

Figure 8. 6-Lead, MicroPak™, 1.0mm Wide

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#### **Tape and Reel Specification**

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Package Designator	Package Designator Tape Section		Cavity Status	Cover Type Status	
	Leader (Start End)	125 (Typical)	Empty	Sealed	
L6X	Carrier	5000	Filled	Sealed	
	Trailer (Hub End)	75 (Typical)	Empty	Sealed	

## **Physical Dimensions**

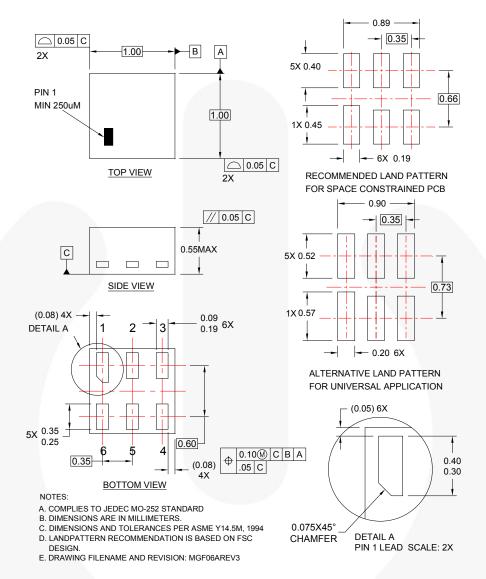


Figure 9. 6-Lead, MicroPak2™, 1x1mm Body, .35mm Pitch

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#### **Tape and Reel Specification**

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Package Designator	Tape Section	<b>Cavity Number</b>	Cavity Status	Cover Type Status	
	Leader (Start End)	125 (Typical)	Empty	Sealed	
FHX	Carrier	5000	Filled	Sealed	
	Trailer (Hub End)	75 (Typical)	Empty	Sealed	





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MicroFETTM
MicroPakTM
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MicroPak2™
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