

Data Sheet

Description

The MP2-202S is a fast recovery diode of 200 V / $20\,A$. The maximum t_{rr} of 50 ns is realized by optimizing a life-time control.

Features

• V _{RSM}	200 V
• I _{F(AV)}	20 A
• V _F	0.98 V
• t _{rr1}	50 ns

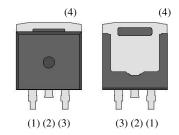
- Bare Lead Frame: Pb-free (RoHS Compliant)
- Flammability: Equivalent to UL94V-0
- Suitable for High Reliability and Automotive Requirement

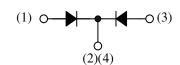
Applications

- Secondary-side Rectifier Diode (Flyback Converter, LLC Converter, etc.)
- Freewheel Diode (Offline Buck Converter, Offline Buck-boost Converter, etc.)

Package

TO220S





- (1) Anode
- (2) Cathode
- (3) Anode
- (4) Cathode

Not to scale

Absolute Maximum Ratings

Unless otherwise specified, $T_A = 25$ °C.

Parameter	Symbol	Conditions	Rating	Unit
Nonrepetitive Peak Reverse Voltage ⁽¹⁾	V _{RSM}		200	V
Repetitive Peak Reverse Voltage ⁽¹⁾	V_{RM}		200	V
Average Forward Current	I _{F(AV)}	See Figure 1 and Figure 2	20	A
Surge Forward Current ⁽¹⁾	I _{FSM}	Half cycle sine wave, positive side, 10 ms, 1 shot	110	A
I ² t Limiting Value ⁽¹⁾	I ² t	$1 \text{ ms} \le t \le 10 \text{ ms}$	60.5	A^2s
Junction Temperature	T _J		-40 to 150	°C
Storage Temperature	T_{STG}		-40 to 150	°C

Electrical Characteristics

Unless otherwise specified, $T_A = 25$ °C.

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage Drop ⁽¹⁾	V_{F}	$T_J = 25 ^{\circ}\text{C}, I_F = 20 \text{A}$	_	_	0.98	V
		$T_J = 100 ^{\circ}\text{C}, I_F = 20 \text{A}$	_	0.90	_	V
Reverse Leakage Current ⁽¹⁾	I_R	$V_R = V_{RM}$			200	μΑ
Reverse Leakage Current under High Temperature ⁽¹⁾	$H \cdot I_R$	$V_R = V_{RM}$, $T_J = 150$ °C	_		400	μΑ
Reverse Recovery Time ⁽¹⁾	t _{rr1}	$I_F = I_{RP} = 100 \text{ mA},$ 90% recovery point, $T_J = 25 \text{ °C}$	_	_	50	ns
	t _{rr2}	$I_F = 100 \text{ mA},$ $I_{RP} = 200 \text{ mA},$ $75\% \text{ recovery point},$ $T_J = 25 \text{ °C}$	_	_	35	ns
Thermal Resistance (2)	R _{th(J-C)}		_		2.5	°C/W

⁽¹⁾ Specifies a value per chip; the MP2-202S consists of two chips.

 $^{^{(2)}}$ $R_{th (J-C)}$ is thermal resistance between junction and the case.

Rating and Characteristic Curves

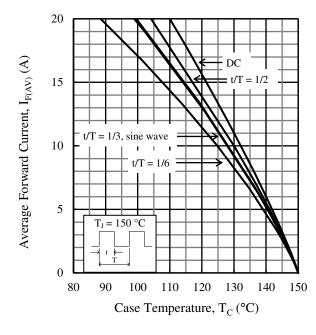


Figure 1. Typical Characteristics: $I_{F(AV)}$ vs. T_C ($V_R = 0 \text{ V}$)

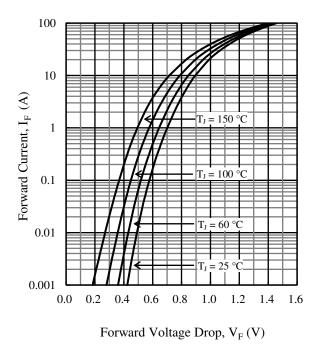


Figure 3. Typical Characteristics: I_F vs. V_F

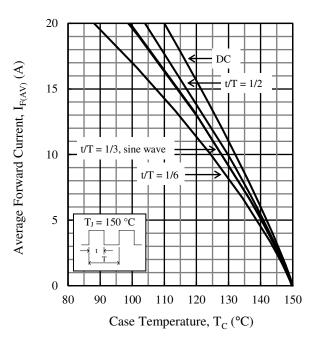


Figure 2. Typical Characteristics: $I_{F(AV)}$ vs. T_C ($V_R = 200 \ V$)

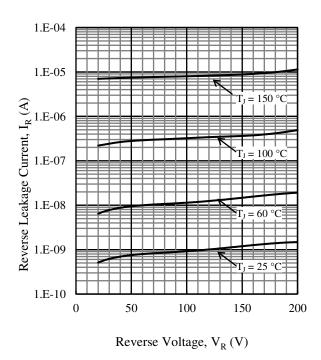
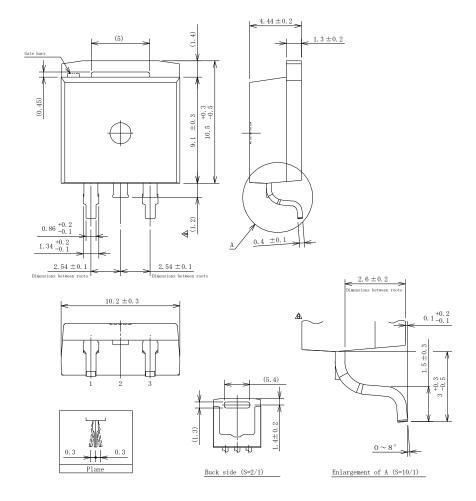


Figure 4. Typical Characteristics: I_R vs. V_R

Physical Dimensions

• TO220S Package



NOTES:

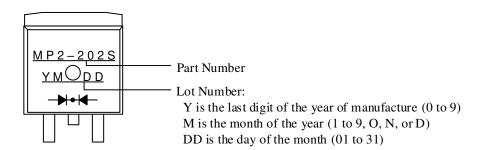
- Dimensions in millimeters
- Bare lead frame: Pb-free (RoHS compliant)
- Maximum gate burr height is 0.3 mm.
- When soldering the products, it is required to minimize the working time within the following limits:
 - Reflow

Preheat: $180 \, ^{\circ}\text{C} / 90 \pm 30 \, \text{s}$

Solder heating: 250 °C / 10 \pm 1s, 2 times (260 °C peak)

- Soldering iron: 380 ± 10 °C / 3.5 ± 0.5 s, 1 time

Marking Diagram



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