

Si4348DY

RoHS

COMPLIANT

New Product

Vishay Siliconix

N-Channel 30-V (D-S) MOSFET

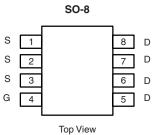
PRODUCT SUMMARY				
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)		
30	0.0125 at V _{GS} = 10 V	11		
	0.014 at V _{GS} = 4.5 V	10		

FEATURES

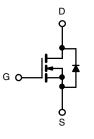
• TrenchFET[®] Gen II Power MOSFET

APPLICATIONS

- High-Side DC/DC Conversion
 - Notebook
 - Desktop
 - Server
- Notebook Logic DC/DC, Low-Side



TOP VIEW



Ordering Information: Si4348DY-T1-E3 (Lead (Pb)-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 \ ^{\circ}C$, unle	ess otherwise	noted		
Parameter		Symbol	10 sec	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 12		
	T _A = 25 °C	I _D	11	8.0	•
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T _A = 70 °C		8.9	6.5	
Pulsed Drain Current		I _{DM}	40		A
Continuous Source Current (Diode Conduction) ^a		ا _S	2.2	1.20	
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	2.5	1.31	w
	T _A = 70 °C		1.6	0.84	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	$t \le 10 \text{ sec}$	R _{thJA}	43	50	
Maximum Junction-to-Ambient ^a	Steady State		74	95	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		19	25	

Notes:

a. Surface Mounted on 1" x 1" FR4 Board.

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Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static				•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.8		2.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
		V_{DS} = 30 V, V_{GS} = 0 V, T_{J} = 55 °C		5	μΑ		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	30			Α	
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 11 A	0.0105 0.0125		0.0125		
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		0.0115	0.014	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 11 A		40		S	
Diode Forward Voltage ^a	V _{SD}	$I_{S} = 2.2 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.1	V	
Dynamic ^b	I I				<u> </u>		
Total Gate Charge	Qg			15	23		
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 11 \text{ A}$	-	5		nC	
Gate-Drain Charge	Q _{gd}			4.3		1	
Gate Resistance	Rg			0.5		Ω	
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		11	17	ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong\text{1}$ A, V_GEN = 10 V, R_g = 6 Ω		55	85		
Fall Time	t _f			9	15		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.2 A, di/dt = 100 A/μs		22	35		

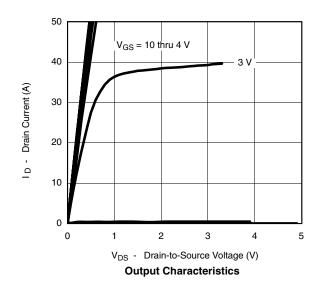
Notes:

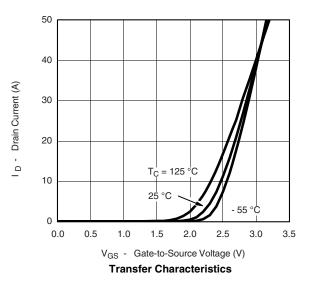
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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25

100

125

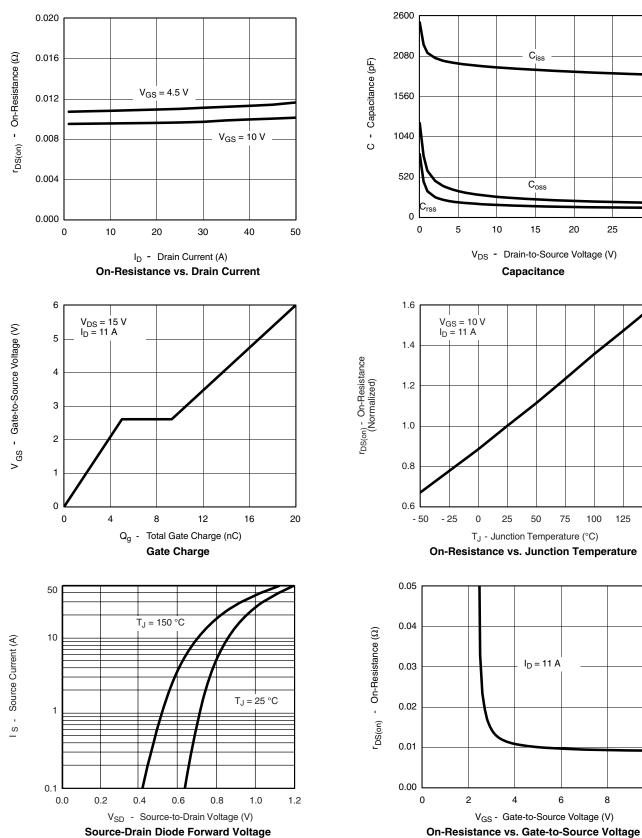
150

30

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

VISHAY



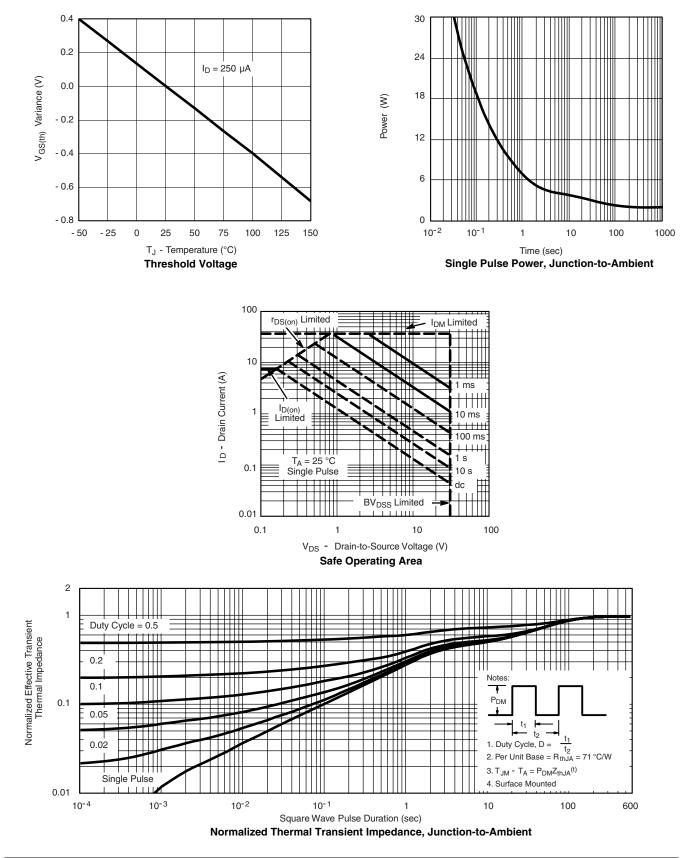
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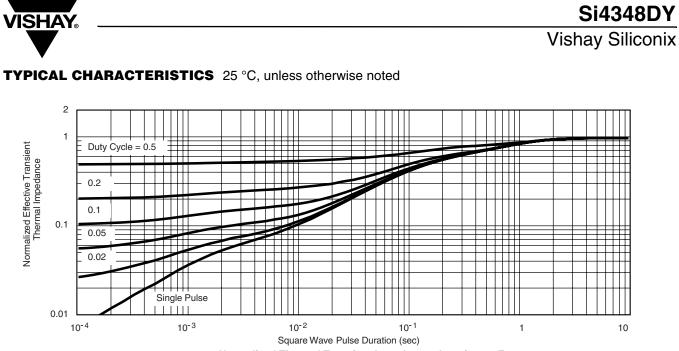
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Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72790.



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