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LMV431/LMV431A/LMV431B Low-Voltage (1.24V) Adjustable Precision Shunt Regulators

LMV431/LMV431A/LMV431B Low-Voltage (1.24V) Adjustable Precision Shunt **Regulators**

General Description

N**ational** Semiconductor

The LMV431, LMV431A and LMV431B are precision 1.24V shunt regulators capable of adjustment to 30V. Negative feedback from the cathode to the adjust pin controls the cathode voltage, much like a non-inverting op amp configuration (Refer to Symbol and Functional diagrams). A two resistor voltage divider terminated at the adjust pin controls the gain of a 1.24V band-gap reference. Shorting the cathode to the adjust pin (voltage follower) provides a cathode voltage of a 1.24V.

The LMV431, LMV431A and LMV431B have respective initial tolerances of 1.5%, 1% and 0.5%, and functionally lends themselves to several applications that require zener diode type performance at low voltages. Applications include a 3V to 2.7V low drop-out regulator, an error amplifier in a 3V off-line switching regulator and even as a voltage detector. These parts are typically stable with capacitive loads greater than 10nF and less than 50pF.

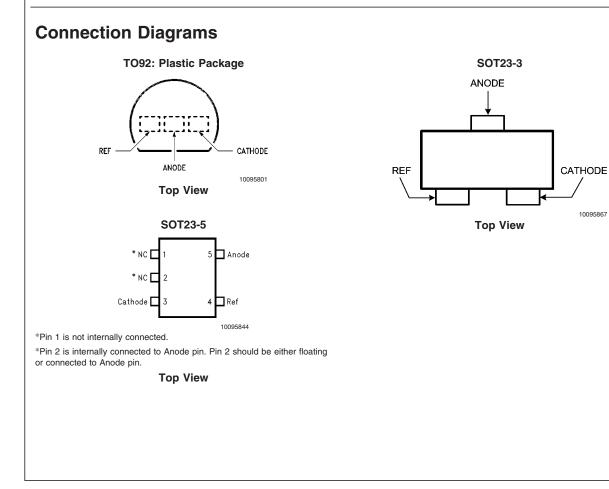
The LMV431, LMV431A and LMV431B provide performance at a competitive price.

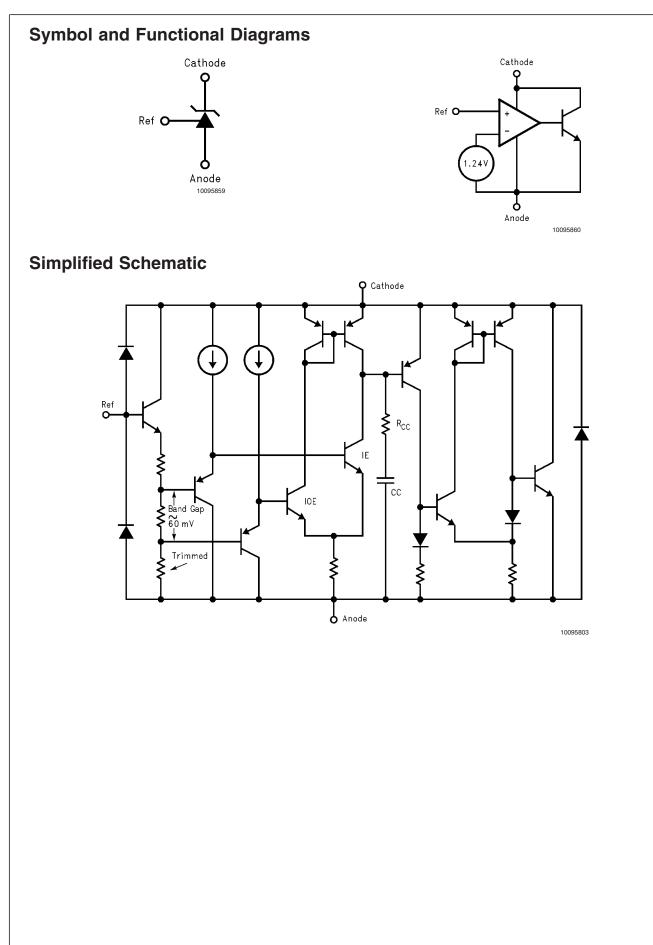
Features

- Low Voltage Operation/Wide Adjust Range (1.24V/30V)
- 0.5% Initial Tolerance (LMV431B)
- Temperature Compensated for Industrial Temperature Range (39 PPM/°C for the LMV431AI)
- Low Operation Current (55µA)
- Low Output Impedance (0.25Ω)
- Fast Turn-On Response
- Low Cost

Applications

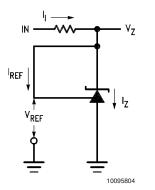
- Shunt Regulator
- Series Regulator
- Current Source or Sink
- Voltage Monitor
- Error Amplifier
- 3V Off-Line Switching Regulator
- Low Dropout N-Channel Series Regulator

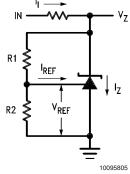




Package	Temperature Range	Voltage Tolerance	Part Number	Package Marking	NSC Drawing	
	Industrial Range	1%	LMV431AIZ	LMV431AIZ		
TO92	–40°C to +85°C	1.5%	LMV431IZ	LMV431IZ		
	Commorial Dange	0.5%	LMV431BCZ	LMV431BCZ	Z03A	
	Commerial Range 0°C to +70°C	1%	LMV431ACZ	LMV431ACZ		
	0010+700	1.5%	LMV431CZ	LMV431CZ		
		1%	LMV431AIM5	N08A		
	Industrial Range -40°C to +85°C	1%	LMV431AIM5X	N08A		
		1.5%	LMV431IM5	N08B		
		1.5%	LMV431IM5X	N08B		
SOT23-5		0.5%	LMV431BCM5	N09C	MF05A	
TO92 SOT23-5		0.5%	LMV431BCM5X	N09C	WI USA	
	Commercial Range	1%	LMV431ACM5	N09A		
	0°C to +70°C	1%	LMV431ACM5X	N09A		
		1.5%	LMV431CM5	N09B		
		1.5%	LMV431CM5X	N09B		
		0.5%	LMV431BIMF	BLB		
SOT23-3	Industrial Range	0.5%	LMV431BIMFX		MF03A	
30123-3	–40° to +85°C	1%	LMV431AIMF	RLA	IVIEUSA	
		1%	LMV431AIMFX			

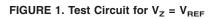
DC/AC Test Circuits for Table and Curves





Note: $V_Z = V_{REF} (1 + R1/R2) + I_{REF} R1$

FIGURE 2. Test Circuit for $V_Z > V_{REF}$



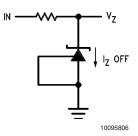


FIGURE 3. Test Circuit for Off-State Current

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ . Distributors for availability and specifications.

Storage Temperature Range	–65°C to +150°C
Operating Temperature Range	
Industrial (LMV431AI, LMV431I)	–40°C to +85°C
Commercial (LMV431AC,	0°C to +70°C
LMV431C, LMV431BC)	
Lead Temperature	
TO92 Package/SOT23 -5,-3 Package	
(Soldering, 10 sec.)	265°C
Internal Power Dissipation (Note 2) TO92	0.78W
SOT23-5, -3 Package	0.28W
Cathode Voltage	35V
Continuous Cathode Current	-30 mA to +30mA
Reference Input Current range	–.05mA to 3mA

Cathode Current	0.1 mA to 15mA
Temperature range	
LMV431AI	$-40^{\circ}C \leq T_A \leq 85^{\circ}C$
Thermal Resistance (θ_{JA}) (Note 3)	
SOT23-5, -3 Package	455 °C/W
TO-92 Package	161 °C/W
Derating Curve (Slope = $-1/\theta_{JA}$)	
€ 1000	
NO	
STOD 500 70 92	
VAXIMUM CONTINUOUS DISSIPATION (m.W)	
CON	
S07-23	

> 70 85

TEMPERATURE (°C)

125

10095830

100 25

Operating Conditions

Cathode Voltage

 V_{REF} to 30V

LMV431C Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Conditio	ns	Min	Тур	Max	Units
V _{REF}	Reference Voltage	$V_Z = V_{REF}, I_Z = 10mA$	T _A = 25°C	1.222	1.24	1.258	
		(See Figure 1)	T _A = Full Range	1.21		1.27	V
V _{DEV}	Deviation of Reference Input Voltage	$V_Z = V_{REF}, I_Z = 10mA,$		•	4	12	mV
	Over Temperature (Note 4)	T _A = Full Range (See Fig	ure 1)				
ΔV _{REF}	Ratio of the Change in Reference	I _Z = 10mA (see Figure 2))		-1.5	-2.7	mV/V
ΔV_Z	Voltage to the Change in Cathode	V_Z from V_{REF} to 6V					
-	Voltage	$R_1 = 10k, R_2 = \infty$ and 2.6	δk				
I _{REF}	Reference Input Current	$R_1 = 10k\Omega, R_2 = \infty$			0.15	0.5	μA
		I _I = 10mA <i>(see Figure 2</i>)					
∝I _{REF}	Deviation of Reference Input Current	$R_1 = 10k\Omega, R_2 = \infty,$			0.05	0.3	μA
	over Temperature	I _I = 10mA, T _A = Full Rang	ge <i>(see Figure 2</i>)		0.05	0.5	μΑ
I _{Z(MIN)}	Minimum Cathode Current for	$V_Z = V_{REF}$ (see Figure 1)			55	80	μA
	Regulation						
I _{Z(OFF)}	Off-State Current	V_Z =6V, V_{REF} = 0V (see F	Figure 3)		0.001	0.1	μA
r _z	Dynamic Output Impedance (Note 5)	$V_z = V_{REF}$, $I_z = 0.1 mA$ to	15mA				
		Frequency = 0Hz (see Fig	gure 1)		0.25	0.4	Ω

LMV431I Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Conditio	ns	Min	Тур	Мах	Units
V _{REF}	Reference Voltage	$V_Z = V_{REF}, I_Z = 10mA$	$T_A = 25^{\circ}C$	1.222	1.24	1.258	v
		(See Figure 1)	T _A = Full Range	1.202		1.278	
V _{DEV}	Deviation of Reference Input Voltage	$V_Z = V_{REF}, I_Z = 10 \text{mA},$			6	20	mV
	Over Temperature (Note 4)	T _A = Full Range (See Fig	iure 1)				
ΔV_{REF}	Ratio of the Change in Reference	I _Z = 10mA (see Figure 2))		-1.5	-2.7	mV/V
ΔV_Z	Voltage to the Change in Cathode	V_Z from V_{REF} to 6V					
-	Voltage	$R_1 = 10k, R_2 = \infty$ and 2.6	δk				
I _{REF}	Reference Input Current	$R_1 = 10k\Omega, R_2 = \infty$			0.15	0.5	μA
		$I_1 = 10 \text{mA} (see Figure 2)$					
∝I _{REF}	Deviation of Reference Input Current	$R_1 = 10k\Omega, R_2 = \infty,$			0.1	0.4	μA
	over Temperature	$I_{I} = 10$ mA, $T_{A} = Full Range$	ge <i>(see Figure 2</i>)		0.1	0.4	μΑ
I _{Z(MIN)}	Minimum Cathode Current for	$V_{Z} = V_{REF}$ (see Figure 1)			55	80	μA
	Regulation				55	80	μΑ
I _{Z(OFF)}	Off-State Current	$V_Z = 6V, V_{REF} = 0V$ (see	Figure 3)		0.001	0.1	μA
r _z	Dynamic Output Impedance (Note 5)	$V_z = V_{REF}$, $I_z = 0.1$ mA to	15mA				
		Frequency = 0Hz (see Fig	gure 1)		0.25	0.4	Ω

LMV431AC Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Conditio	ns	Min	Тур	Max	Units
V _{REF}	Reference Voltage	$V_Z = V_{REF}$, $I_Z = 10 \text{ mA}$	T _A = 25°C	1.228	1.24	1.252	v
		(See Figure 1)	T _A = Full Range	1.221		1.259	
V _{DEV}	Deviation of Reference Input Voltage	$V_Z = V_{REF}, I_Z = 10mA,$			4	12	mV
	Over Temperature (Note 4)	T _A = Full Range (See Fig	ure 1)				
ΔV_{REF}	Ratio of the Change in Reference	I _z = 10 mA <i>(see Figure 2</i>	$I_Z = 10 \text{ mA} (see Figure 2)$ V_Z from V_{REF} to 6V				mV/V
ΔV_Z	Voltage to the Change in Cathode	V _Z from V _{REF} to 6V					
-	Voltage	$R_1 = 10k, R_2 = \infty$ and 2.6	δk				
I _{REF}	Reference Input Current	$R_1 = 1 k\Omega, R_2 = \infty$			0.15	0.50	μA
		I _I = 10 mA <i>(see Figure 2</i>)					
∝I _{REF}	Deviation of Reference Input Current	$R_1 = 10 \ k\Omega, \ R_2 = \infty,$				0.3	
	over Temperature	$I_I = 10 \text{ mA}, T_A = \text{Full Ran}$	ge <i>(see Figure 2</i>)		0.05	0.3	μΑ
I _{Z(MIN)}	Minimum Cathode Current for	$V_Z = V_{REF}$ (see Figure 1)			55	80	
	Regulation				55	80	μA
I _{Z(OFF)}	Off-State Current	$V_Z = 6V, V_{REF} = 0V$ (see	Figure 3)		0.001	0.1	μA
r _z	Dynamic Output Impedance (Note 5)	$V_Z = V_{REF}$, $I_Z = 0.1 \text{mA to}$	15mA				
		Frequency = 0 Hz (see Fi	igure 1)		0.25	0.4	Ω

LMV431AI Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Condition	ns	Min	Тур	Max	Units
V _{REF}	Reference Voltage	$V_Z = V_{REF}, I_Z = 10mA$	T _A = 25°C	1.228	1.24	1.252	
		(See Figure 1)	T _A = Full Range	1.215		1.265	V
V _{DEV}	Deviation of Reference Input Voltage	$V_Z = V_{REF}, I_Z = 10mA,$	•		6	20	mV
	Over Temperature (Note 4)	T _A = Full Range (See Fig.	ure 1)				
ΔV_{REF}	Ratio of the Change in Reference	I _Z = 10mA (see Figure 2)			-1.5	-2.7	mV/V
ΔV_Z	Voltage to the Change in Cathode	V_Z from V_{REF} to 6V	V _Z from V _{REF} to 6V				
-	Voltage	$R_1 = 10k, R_2 = \infty$ and 2.6	ik				
I _{REF}	Reference Input Current	$R_1 = 10k\Omega, R_2 = \infty$			0.15	0.5	μA
		$I_1 = 10 \text{mA}$ (see Figure 2)					
∝I _{REF}	Deviation of Reference Input Current	$R_1 = 10k\Omega, R_2 = \infty,$				0.4	
	over Temperature	$I_{I} = 10$ mA, $T_{A} = Full Range$	ge <i>(see Figure 2</i>)		0.1	0.4	μA
I _{Z(MIN)}	Minimum Cathode Current for	$V_Z = V_{REF}$ (see Figure 1)			55	80	
	Regulation				55	00	μA
I _{Z(OFF)}	Off-State Current	$V_Z = 6V, V_{REF} = 0V$ (see	Figure 3)		0.001	0.1	μA
r _z	Dynamic Output Impedance (Note 5)	$V_z = V_{REF}$, $I_z = 0.1 \text{mA to}$	15mA				
		Frequency = 0Hz (see Fig	gure 1)		0.25	0.4	Ω

LMV431BC Electrical Characteristics

$T_{A} = 25$	°C	unless	otherwise	specified

Symbol	Parameter	Condition	ns	Min	Тур	Max	Units
V_{REF}	Reference Voltage	$V_Z = V_{REF}, I_Z = 10mA$	T _A = 25°C	1.234	1.24	1.246	
		(See Figure 1)	T _A = Full Range	1.227		1.253	V
V _{DEV}	Deviation of Reference Input Voltage	$V_z = V_{REF}, I_z = 10mA,$			4	12	mV
	Over Temperature (Note 4)	T _A = Full Range (See Fig.	ure 1)				
ΔV_{REF}	Ratio of the Change in Reference	I _Z = 10mA (see Figure 2)			-1.5	-2.7	mV/V
ΔV_Z	Voltage to the Change in Cathode	V_Z from V_{REF} to 6V					
-	Voltage	$R_1 = 10k, R_2 = \infty$ and 2.6	ik				
I _{REF}	Reference Input Current	$R_1 = 10k\Omega, R_2 = \infty$			0.15	0.50	μA
		I _I = 10mA <i>(see Figure 2</i>)					
∝I _{REF}	Deviation of Reference Input Current	$R_1 = 10 k\Omega, R_2 = \infty,$			0.05	0.3	
	over Temperature	$I_I = 10mA, T_A = Full Range$	ge <i>(see Figure 2</i>)		0.05	0.5	μA
I _{Z(MIN)}	Minimum Cathode Current for	$V_Z = V_{REF}$ (see Figure 1)			55	80	
	Regulation				55	00	μA
I _{Z(OFF)}	Off-State Current	$V_Z = 6V, V_{REF} = 0V$ (see	Figure 3)		0.001	0.1	μA
r _z	Dynamic Output Impedance (Note 5)	$V_Z = V_{REF}$, $I_Z = 0.1 \text{mA to}$	15mA				
		Frequency = 0Hz (see Fig	gure 1)		0.25	0.4	Ω

LMV431BI Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Conditions Min			Тур	Max	Units
V _{REF}	Reference Voltage	$V_Z = V_{REF}, I_Z = 10mA$	T _A = 25°C	1.234	1.24	1.246	
		(See Figure 1)	T _A = Full Range	1.224		1.259	V
V _{DEV}	Deviation of Reference Input Voltage	$V_Z = V_{REF}, I_Z = 10mA,$	$V_Z = V_{REF}, I_Z = 10mA,$				mV
	Over Temperature (Note 4)	T _A = Full Range (See Figure 1)					
	Ratio of the Change in Reference	I _Z = 10mA (see Figure 2)			-1.5	-2.7	mV/V
$\frac{\Delta V_{REF}}{\Delta V_Z}$	Voltage to the Change in Cathode	V_Z from V_{REF} to 6V					
-	Voltage	$R_1 = 10k, R_2 = \infty$ and 2.6	ik				
I _{REF}	Reference Input Current	$R_1 = 10k\Omega, R_2 = \infty$			0.15	0.50	μA
		$I_1 = 10 \text{mA} (see Figure 2)$					

LMV431BI Electrical Characteristics (Continued)

 $T_A = 25^{\circ}C$ unless otherwise specified

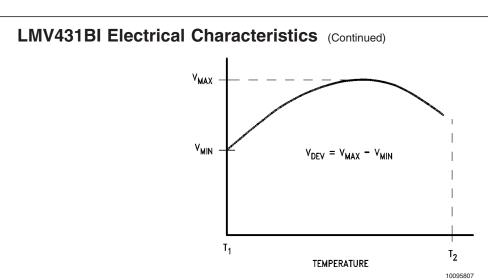
1 _A = 23	r _A = 25 6 unless otherwise specified						
Symbol	Parameter	Conditions	Min	Тур	Мах	Units	
∝I _{REF}	Deviation of Reference Input Current over Temperature	$R_1 = 10k\Omega, R_2 = \infty,$ $I_1 = 10mA, T_A = Full Range (see Figure 2)$		0.1	0.4	μA	
I _{Z(MIN)}	Minimum Cathode Current for Regulation	$V_Z = V_{REF}$ (see Figure 1)		55	80	μA	
I _{Z(OFF)}	Off-State Current	$V_Z = 6V, V_{REF} = 0V$ (see Figure 3)		0.001	0.1	μA	
r _z	Dynamic Output Impedance (Note 5)	$V_Z = V_{REF}$, $I_Z = 0.1$ mA to 15mA					
		Frequency = 0Hz (see Figure 1)		0.25	0.4	Ω	

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Electrical specifications do not apply when operating the device beyond its rated operating conditions.

Note 2: Ratings apply to ambient temperature at 25°C. Above this temperature, derate the TO92 at 6.2 mW/°C, and the SOT23-5 at 2.2 mW/°C. See derating curve in Operating Condition section.

Note 3: $T_{J Max} = 150^{\circ}C$, $T_{J} = T_{A}+ (\theta_{JA} P_{D})$, where P_{D} is the operating power of the device.

Note 4: Deviation of reference input voltage, V_{DEV}, is defined as the maximum variation of the reference input voltage over the full temperature range. See following:



The average temperature coefficient of the reference input voltage, ${}^{\sim}V_{\text{REF}}$, is defined as:

$$\propto V_{\mathsf{REF}} \frac{\mathsf{ppm}}{^{\circ}\mathsf{C}} = \frac{\pm \left[\frac{\mathsf{V}_{\mathsf{Max}} - \mathsf{V}_{\mathsf{Min}}}{\mathsf{V}_{\mathsf{REF}} (\mathsf{at} \, 25^{\circ}\mathsf{C})}\right] 10^{6}}{\mathsf{T}_{2} - \mathsf{T}_{1}} = \frac{\pm \left[\frac{\mathsf{V}_{\mathsf{DEV}}}{\mathsf{V}_{\mathsf{REF}} (\mathsf{at} \, 25^{\circ}\mathsf{C})}\right] 10^{6}}{\mathsf{T}_{2} - \mathsf{T}_{1}}$$

Where:

 $T_2 - T_1$ = full temperature change.

 ${}^{\propto}V_{\text{REF}}$ can be positive or negative depending on whether the slope is positive or negative.

Example: $V_{DEV} = 6.0 \text{mV}$, REF = 1240mV, $T_2 - T_1 = 125^{\circ}\text{C}$.

$${}_{\infty}V_{\mathsf{REF}} = \frac{\left[\frac{6.0 \text{ mV}}{1240 \text{ mV}}\right] 10^{6}}{125^{\circ}\mathsf{C}} = +39 \text{ ppm/}^{\circ}\mathsf{C}$$

Note 5: The dynamic output impedance, r_Z , is defined as:

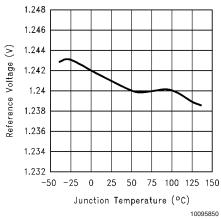
$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

When the device is programmed with two external resistors, R1 and R2, (see Figure 2), the dynamic output impedance of the overall circuit, r_z, is defined as:

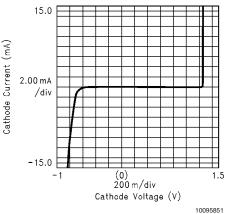
$$\mathbf{r}_{Z} = \frac{\Delta V_{Z}}{\Delta I_{Z}} \simeq \left[\mathbf{r}_{Z} \left(\mathbf{1} + \frac{\mathbf{R}\mathbf{1}}{\mathbf{R}\mathbf{2}} \right) \right]$$

Typical Performance Characteristics



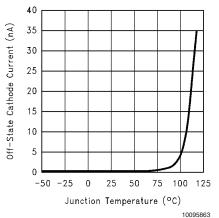


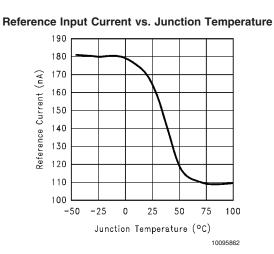
Cathode Current vs. Cathode Voltage 1



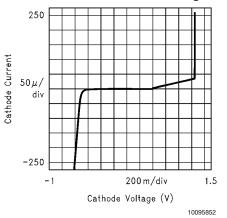
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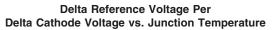
Off-State Cathode Current vs. Junction Temperature

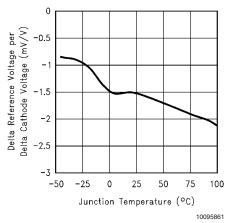


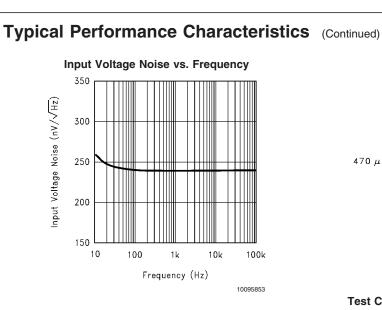


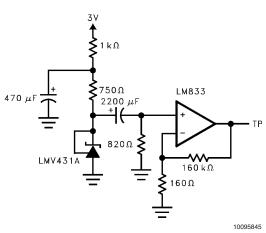
Cathode Current vs. Cathode Voltage 2



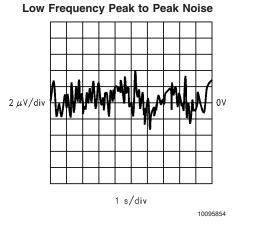






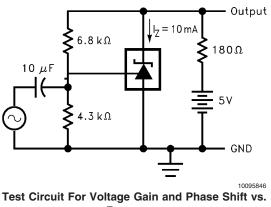


Test Circuit for Input Voltage Noise vs. Frequency



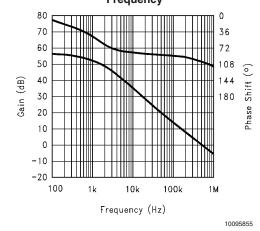
3V kΩ 750Ω LM833 470 μF 2200 µF TP 820Ω . 160 kΩ LMV431A 0. 160Ω 10095864

Test Circuit for Peak to Peak Noise (BW= 0.1Hz to 10Hz)



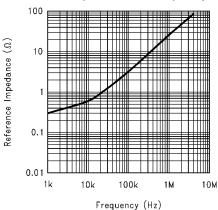
Frequency

Small Signal Voltage Gain and Phase Shift vs. Frequency



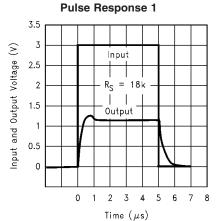
Typical Performance Characteristics (Continued)

Reference Impedance vs. Frequency



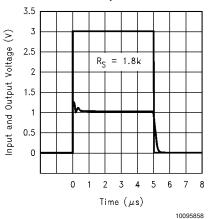


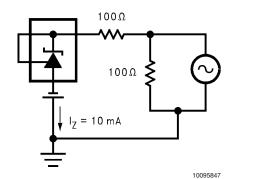
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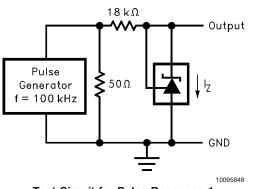


Pulse Response 2

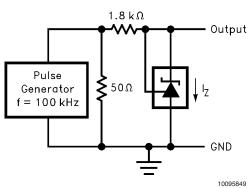




Test Circuit for Reference Impedance vs. Frequency







Test Circuit for Pulse Response 2

