



DMP3100L

P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance: $100m\Omega @ V_{GS} = -10V, I_D = -2.7A$
 - $170m\Omega @ V_{GS} = -4.5V, I_D = -2.0A$
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 4)
- Qualified to AEC-Q101 Standards for High Reliability

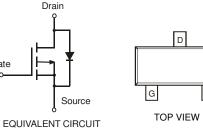


- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. • UL Flammability Classification Rating 94V-0

S

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.008 grams (approximate)





Maximum Ratings @T_A = 25°C unless otherwise specified

Characte	ristic		Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±20	V
Drain Current (Note 1) V _{GS} = -10V	Steady $T_A = 25^{\circ}C$ State $T_A = 70^{\circ}C$		ID	-2.7 -2	А
Pulsed Drain Current (Note 3)			IDM	-8	А

Gate

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 1)	PD	1.08	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 1)	$R_{ ext{ heta}JA}$	115	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
DFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30			V	$V_{GS} = 0V, I_D = -250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	—	-800	nA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_	_	±80 ±800	nA	$\begin{array}{l} V_{GS}=\pm 12V, V_{DS}=0V\\ V_{GS}=\pm 15V, V_{DS}=0V \end{array}$	
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(th)}	-1.3	-1.8	-2.1	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
Static Drain-Source On-Resistance	R _{DS (ON)}	—	86 147	100 170	mΩ	$V_{GS} = -10V, I_D = -2.7A$ $V_{GS} = -4.5V, I_D = -2.0A$	
Forward Transfer Admittance	Y _{fs}		3.6		S	$V_{DS} = -5V, I_D = -2.7A$	
Diode Forward Voltage (Note 5)	V _{SD}		_	-1.26	V	$V_{GS} = 0V, I_{S} = -2.7A$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}	_	227		pF		
Output Capacitance	C _{oss}		64		pF	$V_{DS} = -10V, V_{GS} = 0V$ = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}		36		pF		

Notes: Device mounted on FR-4 PCB. t \leq 5 sec. 1.

No purposefully added lead. 2.

3. Pulse width $\leq 10\mu$ S, Duty Cycle $\leq 1\%$.

Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php. 4

Short duration pulse test used to minimize self-heating effect. 5.

25°C

 $T_A = 150^{\circ}C$

T_A = 125°C

 $T_A = 85^{\circ}C$

 $T_A = 25^{\circ}C$

 $T_A = -55^{\circ}C$

2.5

3

2

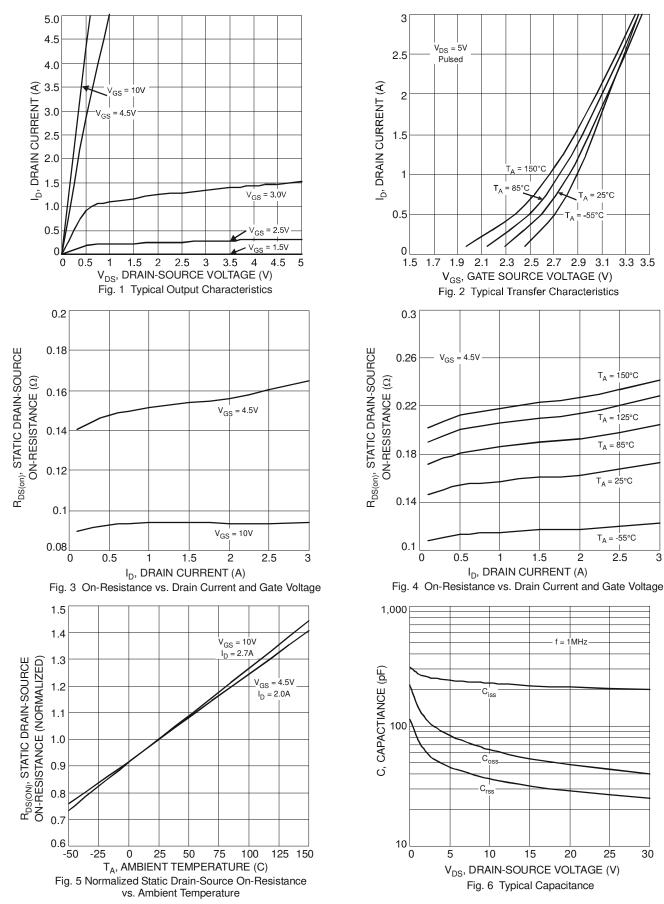
f = 1MHz

20

25

-55°C

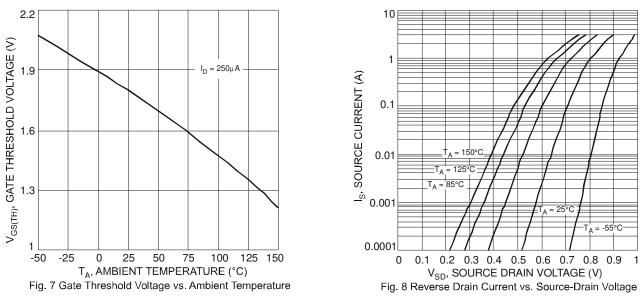




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DMP3100L

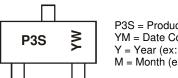


Ordering Information (Note 6)

Part Number	Case	Packaging
DMP3100L-7	SOT-23	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

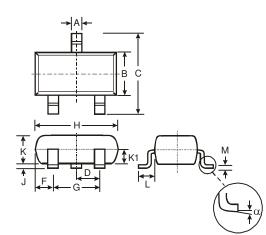
Marking Information



P3S = Product Type Marking Code YM = Date Code Marking Y = Year (ex: U = 2007) M = Month (ex: 9 = September)

Date Code Key												
Year	20	07	20	08	20	09	20)10	20	11	20	12
Code	l	J		V	V	V)	Х	,	Y		<u>Z</u>
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

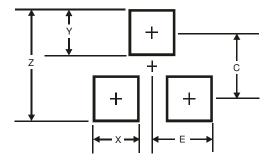
Package Outline Dimensions



	SOT-23						
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
К	0.903	1.10	1.00				
K1	-	-	0.400				
L	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All	All Dimensions in mm						



Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

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