

REV.

PRECISION

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REV.

INDIT SIGNA				
RN: RED GR		NO CONNECTION.	NC :	30
		NO CONNECTION.	NC	9
NOIE: DEFIN		DIGITAL GROUND FOR GATE DRIVER.	DVSS	8
- 1		DIGITAL POWER SUPPLY FOR GATE DRIVER.	DVDD	57
4		POWER SUPPLY FOR LCM GATE LOW (-7V)	VEE	56
LIGHT 0		POWER SUPPLY FOR LCM GATE HIGH (+15V)	VGH	55
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+		OSD ENABLE INPUT. NORMALLY PULL LOW.	OSD_EN	3
+	BASIO	OSD BLUE DATA INPUT. NORMALLY PULL LOW.	OSD_B	7
4		OSD GREEN DATA INPUT. NORMALLY PULL LOW.	OSD-G	0,
		OSD RED DATA INPUT. NORMALLY PULL LOW.	OSD-R	5
RI ACK		OSD CLOCK OUTPUT.	OSD_CLK	4
DOL	COLON	OSD VSYNC OUTPUT.	OSD_VS	3
NED AY		OSD HSYNC OUTPUT.	OSD_HS	2
CULUR DAIA INPUL	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	COMMON ELECTRODE DRIVING SIGNAL.	VCOM	
DATA INIDI IT	20100	FUNCTION	SYMBOL	Ĭ
		INTERFACE PIN CONNECTION /	FACE PIN C	NTER

25 26 27~34 35~42

16 17~24

15

		SLUE	٦ 	SCALE	GRAY					REEN	۲ 	SCALE	GRAY						<u> </u>	NON TO	Ď/V						COLOR	SASIC					COLOR		COLOR
BLUE	LIGHT	←			\rightarrow	DARK	BLACK	GREEN	LIGHT	←			\rightarrow	DARK	BLACK	RED	LIGHT	←			→	DARK	BLACK	MHITE	YELLOW	MAGNETA	RED	CYAN	GREEN	BLUE	BLACK		DISPLAY		COLOR DATA INPUT
0	0	0		• •	0	0	0	0	0	0			0	0	0	_	0	_			0	_	0	_	_	1	_	0	0	0	0	RO			Ĕ
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B63	B62	B61	טייטטט	R 7∾B60	B2	B1	B0	G63	G62	G61	60,5600	032~20	G2	G1	G0	R63	R62	R61	1/0/2/1/00	038~za	R2	R1	RO	ı	ı	1	ı	1	1	ı	ı	LEVEL	SCALE	GRAY	

NITION OF GRAY

RN: RED GRAY, GN: GREEN GRAY, BN: BLUE GRAY (N=GRAY LEVEL) INPUT SIGNAL: 0=LOW LEVEL VOLTAGE, 1=HIGH LEVEL VOLTAGE

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RELABILITY NOTE
OUR MANY YEARS OF EXPERIENCE DATA ACCUMULATION INDICATE THAT
SOLDER HEAT IS A MAJOR CAUSE OF EARLY AND FUTURE FAILURE.
PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.

6:00 VIEW, LED BACKLIGHT,

5.7"

ACTIVE MATRIX FULL COLOR TFT PANEL

-20°C TO +70°C OPERATING TEMP

_CT-H320240M57W

PART NUMBER

REV.

DRAWN BY:

CHECKED 290 E. HELEN ROAD
PALATINE, IL 60067–6976
PHONE: +1.847.359.2790
US WEB: www.lumex.com
TW WEB: www.lumex.com.tw BY: APPROVED BY: DATE:

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.CT-H320240M57W PART NUMBER

REV.

ELECTRICAL CHARASTERISTICS

ITOM	201	IAIS	STANDARD VALUE	ALUE		DEMADES
	SIMBOL	MIN	TYP.	MAX		NEMAIN
	DVDD	2.7	3.3	5.5	٧	
POWER VOLTAGE	AVDD	3.8	5	5.5	<	
	DVCC	3	3.3	3.6	<	
GATE ON VOLTAGE	VGH	7	15	VEE+40	<	
GATE OFF VOLTAGE	VGL	-20	-10	-5	<	
INPUT HIGH VOLTAGE	ИIV	0.7*VDDIO	_	VDD	٧	
INPUT LOW VOLTAGE	VIL	Vss	ı	0.3xVDD	٧	
OUTPUT HIGH VOLTAGE	VOH	VDD-0.3	ı	VDD	٧	10H=200uA
OUTPUT LOW WOLTAGE	VOL	Vss	ı		<	10L=200uA
OUTPUT VOLTAGE DEVIATION	VVD	_	±20	-	m۷	
DC OFFSET	SOA	_	1	02 4	m۷	

ı				_					Ι~		_		N
	HALF-BRIGHTNESS LIFE TIME	REMARK	UNIFORMITY	LUMINANCE (BLU ONLY)	CHACAMACA	CHROMACITY COORDINATES	RESERVE CURRENT	REVERSE VOLTAGE	ABSOLUTE MAX FORWARD CURRENT	FORWARD VOLTAGE		TEM	BACKLIGHT SPECIFICATIONS
			⊳	Lv	Y	×	₹	٧r	lfm	Vf	CIMIDOL	CVMBOI	
	50	E	70	2800	0.276	0.287	ı	ı	ı	12	MIN	STA	
	50000 HOURS	LED PATENTED	75	3400	-	1	ı	-	60	13	TYP.	STANDARD VALUE	
	7S	ED	85	4000	0.328	0.320	15	5	ı	14	MAX	LUE	
			%	cd/m	1	1	A	<	mA	<			

If=60mA REMARKS

Vr=3.0V

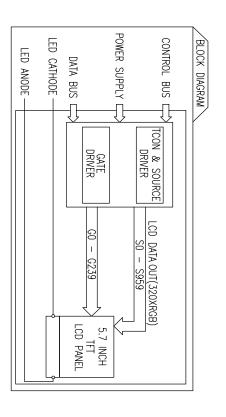
MIN/MAX*100%

lf=60mA

\
ABS0LUTE
MAXIMUM
RATINGS
/

ITCM	SVIDO	TEST	4IS	STANDARD VALUE	'ALUE	TINIT
	SIMDUL	CONDITION	MIN	TYP.	MAX	ONI
POWER VOLTAGE	DVDD	DVSS=0	-0.3	_	7.0	٧
	AVDD	AVSS=0	-0.3	_	7.0	<
	DVCC	DGND=0	-0.3	_	7.0	<
GATE ON VOLTAGE	VGH		-0.3	_	32	<
GATE OFF VOLTAGE	VGL	GIND-O	-22	_	0.3	٧
INPUT VOLTAGE	VIN	_	-0.3	_	DVCC+0.3	٧
LOGICAL OUTPUT VOLTAGE	VOUT	ı	-0.3	ı	7.0	<

TIMES UNEVENNES CAN BE SEEN) UNDER LOW TEMPERATURE AND HIGH TEMPERATURE ENVIRONMENT. WHEN THE APPLICATION TEMPERATURE RETURNS BACK TO ROOM TEMPERATURE, LCD COLOR AND CONTRAST WILL REVERSE BACK TO ITS ORIGINAL COLOR AND CONTRAST WITHIN 24 HOURS AND ITS FUNCTIONALITY IS NOT AFFECTED. IT IS A NORMAL CHARACTERISTIC THAT THE LCD COLOR AND CONTRAST WILL CHANGE (SOME



**UNILESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.25 (±0.010), X.XXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN= +DECIMAL PRECISION = -0.000

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RELABILITY NOTE
OUR MANY YEARS OF EXPERIENCE DATA ACCUMULATION INDICATE THAT
SOLDER HEAT IS A MAJOR CAUSE OF EARLY AND FITURE FAILURE.
PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.

DRAWN BY: CHECKED 290 E. HELEN ROAD
PALATINE, IL 60067–6976
PHONE: +1.847.359.2790
US WEB: www.lumex.com
TW WEB: www.lumex.com.tw в :: APPROVED BY: DATE:

MAX.= +c

PRECISION

PAGE: 04.28.09 3 OF 9

SCALE:

REV.

5.7" ACTIVE MATRIX FULL COLOR TFT PANEL

LCT-H320240M57W PART NUMBER

6:00 VIEW, LED BACKLIGHT, -20°C TO +70°C OPERATING TEMP.

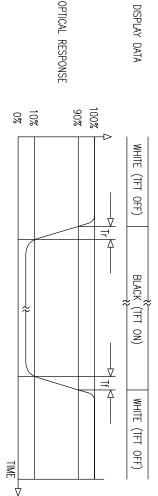
'n

OPTICAL CHARASTERISTICS _CT-H320240M57W PART NUMBER

REV.

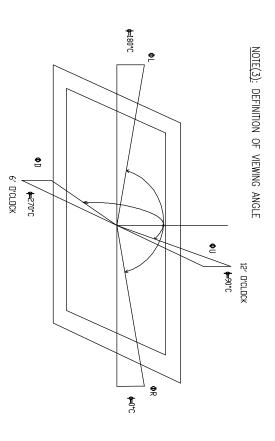
OPTIMUM VIEWING DIRECTION			EFERENCE ONLY"	"SIMULATION DATA"			(CIE1931)	COLOR CROMACITY	(CENTER POINT OF LCM)		(CK 2 10)	VIEWING ANGLE		CONTRAST RATIO	RESPUNSE HWE	BESDONISE TIME		Mali
	Wy	W×	Ву	Bx	Gy	Gx	Ry	R _x		LOWER	UPPER	LEFT	RIGHT	CR	Tf	T	טוואוטטר	SYMBOI
									1	ф= 270°C	0°06=	ф=1 80°C	₽ 0°C	₽ ₩0°C	į	P	CONDITION	CONDITION
6 O'CLOCK	0.299	0.282	0.083	0.107	0.553	0.268	0.314	0.610	200	ı	ı	-	1	150	ı	ı	N N	/TS
X	0.329	0.312	0.103	0.137	0.583	0.298	0.344	0.640	250	35	15	45	45	250	35	15	TYP.	STANDARD VALUE
	0.359	0.342	0.123	0.167	0.613	0.328	0.374	0.670	ı	_	-	1	1	1	50	30	MAX	TUE
	-	-	ı	ı	ı	ı	ı	ı	Cd/m²	DEG	DEG	DEG	DEG	٧	<	<		
ı				ONLY"	REFERENCE	"SIMI II ATION	4	4	5				3	2		_	-	NOTE

NOTE(1): DEFINITION OF RESPONSE TIME



 $\underline{\text{NOTE}(2)}$: DEFINITION OF CONTRAST RATIO CR=BRIGHTNESS AT ALL PIXELS "BLACK"

 ${\tt NOTE}(5)$: MEASURED AT CENTER POINT VERTICALLY WITH BACKLIGHT ON.



TEMPÉRÂTURE FOR 30MIN, THE MEASUREMENT SHOULD BE EXECUTED. MEASURMENT SHOULD BE EXECUTED IN STABLE, WINDLESS, AND DARK ROOM 30 MINS AFTER LIGHTING THE BACK-LIGHT. THIS SHOULD BE MEASURED IN THE ENVIRONMENT CONDITION: Ta=25±2°C BACK-LIGHT ON CONDITION CENTER OF SCREEN. <u>NOTE(4):</u> AFTER STABILIZING AND LEAVING THE PANEL ALONE AT GIVEN

TFT-LCD MODULE PROTECTOR FIELD BM-7:50cm LCD PANEL

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DRAWN BY:

CHECKED в :: APPROVED В ::

290 E. HELEN ROAD
PALATINE, IL 60067–6976
PHONE: +1.847.359.2790
US WEB: www.lumex.com
TW WEB: www.lumex.com.tw DATE:

PAGE: 04.28.09 4 OF 9

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PART NUMBER

ACTIVE MATRIX FULL COLOR TFT PANEL

6:00 VIEW, LED BACKLIGHT, -20°C TO +70°C OPERATING TEMP.

RELABILITY NOTE
OUR MANY YEARS OF EXPERIENCE DATA ACCUMULATION INDICATE THAT
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PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.

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STANDARD SPECIFICATION FOR REABILITY

STANDARD SPECIFICATION OF REABILITY TEST

	7		6	Δı	4	3	2	_	8
	DROP TEST		TEMPERATURE CYCLE	HIGH TEMPERATURE/ HUMIDITY OPERATION	LOW TEMPERATURE OPERATION	HIGH TEMPERATURE OPERATION	LOW TEMPERATURE STORAGE	HIGH TEMPERATURE STORAGE	TEST ITEM
	ENDURANCE TEST APPLYING THE DROP DURING TRANSPORTATION.	MECHANICAL TEST	ENDURANCE TEST APPLYING THE LOW AND HIGH TEMPERATURE CYCLE. -20°C 25°C 70°C 30 MIN 5 MIN 30 MIN 1 CYCLE	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND TEMPERATURE / HUMIDITY STRESS TO THE ELEMENT FOR A LONG TIME.	ENDURANCE TEST APPLYING THE ELECTRIC STRESS UNDER LOW TEMPERATURE FOR A LONG TIME.	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND THE THERMAL STRESS 70+/-3°C 240HRS TO THE ELEMENT FOR A LONG TIME.	ENDURANCE TEST APPLYING THE HIGH STORAGE TEMPERATURE FOR A LONG TIME.	ENDURANCE TEST APPLYING THE HIGH STORAGE TEMPERATURE FOR A LONG TIME.	CONTENT OF TEST
,	PACKED,100cm FREE FALL(6 SLIDES, 1 CORNER, 3 EDGES)		-20°C/ 70°C 10 CYCLES	40°C, 90%RH 120HRS	-20+/-3°C 240HRS	70+/-3°C 240HRS	-30+/-3°C 240HRS	80+/-3°C 240HRS	TEST CONDITION
	 		1 1 1	MIL-202E-103B JIS-C5023	1 1 1	1	 	 	APPLICABLE STANDARD

REMARKS:

- 1. FOR OPERATION TEST, ABOVE SPECIFICATION IS APPLICABLE WHEN TEST PATTERN IS CHANCING DURING ENTIRE OPERATION TEST.
 2. INSPECTIONS AFTER RELIABILITY TESTS ARE PERFORMED WHEN THE DISPLAY TEMPERATURE RESUMES BACK TO ROOM TEMPERATURE.
 3. IT IS A NORMAL CHARACTERISTIC THAT SOME DISPLAY ABNORMALITY CAN BE SEEN DURING REABILITY TEST. IF THE DISPLAY ABNORMALITY CAN RESUME BACK TO NORMAL CONDITION AT ROOM TEMPERATURE WITHIN 24 HOURS, THERE IS NO PERMANENT DESTRUCTION OVER THE DISPLAY. THE DISPLAY STILL POSSESSES ITS FUNCTIONALITY AFTER REABILITY TESTS.

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DRAWN BY: CHECKED BY:

290 E. HELEN ROAD
PALATINE, IL 60067–6976
PHONE: +1.847.359.2790
US WEB: www.lumex.com
TW WEB: www.lumex.com.tw

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PAGE: SCALE: 04.28.09

6:00 VIEW, LED BACKLIGHT, -20°C TO +70°C OPERATING TEMP. 5.7" ACTIVE MATRIX FULL COLOR TFT PANEL

_CT-H320240M57W PART NUMBER

REV.

RELUBLITY NOTE
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5 OF 9

QUALITY ASSURANCE

ACCEPTABLE QUALITY LEVEL (AQL)

EACH LOT SHOULD SATISFY THE QUALITY LEVEL DEFINED AS FOLLOWS:

A. INSPECTION METHOD: MIL-SDT-105E LEVEL II NORMAL ONE TIME SAMPLING.

B. AQL LEVEL.

MINOR		MAJOR	CATEGORY AQI
1.00%		0.25%	AQL
1.00% STANDARD.	CATIEV ALL ELINCTIONS AS DEPOBLICT BLIT NOT SATISFY COSMETIC	0.25% FUNCTIONAL DEFECTIVE AS PRODUCT.	DEFINITION

COSMETIC SCREENING CRITERIA

IHE PATTERNS OF DISPLAY SHALL LIGHT UP AS REQUIRED. NO DISPLAY OR MISSING DISPLAY ARE
SHORT CIRCUIT ARE NOT ACCEPTABLE.
DIM DISPLAY ON THE PATTERNS, EXTRA PATTERN AND
OBVIOUS UNVEN COLOR (RAINBOW) SHALL NOT BE NOTICEABLE.
ABOVE DEFECTS SHOULD BE SEPARATED MORE THAN
0
3
DISREGARD
DISREGARD
WIDTH, W(mm) LENGTH, L(mm) IN ACTIVE AREA
ACCEPTABLE QUANTITY
DISREGARD
ACCEPTABLE QUANTITY IN ACTIVE AREA
JUDGMENT CRITERIA

NOTE: D= (LONG LENGTH + SORTH LENGTH)/2

FAILURE JUDGMENT CRITERIA

AFTER REABILITY TEST ABOVE, TEST SAMPLE SHALL BE LET RUN TO ROOM TEMPERATURE AND HUMIDITY AT LEAST 4 HOURS BEFORE FINAL TESTS ARE CARRIED OUT.

MECHANICAL CHARACTERISTIC OUT OF MECHANICAL SPECIFICATION.
--

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5.7"

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SCALE:

PRECAUTIONS FOR USING LCD MODULE

H320240M57W

PART NUMBER

REV.

- DROPPING OR IMPACT WHICH MAY CAUSE CHIPPING ESPECIALLY ON THE EDGES. THE DISPLAY PANEL IS MADE OF GLASS AND POLARIZER. DO NOT SUBJECT IT TO MECHANICAL SHOCK BY
- LEAD (GLASS,TWEEZERS, ETC.). THE POLARIZER COVERING THE DISPLAY SURFACE OF THE LCD MODULE IS DO NOT TOUCH, PUSH OR RUB THE EXPOSED POLARIZERS WITH ANYTHING HARDER THAN AN HB PENCIL
- POLARIZER SURFACE. ALCOHOL. AVOID USING SOLVENTS LIKE ACETONE (KETENE), WATER, TOLUENE, ETHANOL TO CLEAN THE
- 4. PLEASE KEEP THE TEMPERATURE WITHIN SPECIFIED RANGE FOR USE AND STORAGE. POLARIZATION DEGRADATION, BUBBLE GENERATION OR POLARIZER PEEL-OFF MAY OCCUR WITH HIGH TEMPERATURE AND HIGH HUMIDIT
- 5. DO NOT APPLY EXCESSIVE FORCE TO THE DISPLAY SURFACE OR THE ADJOINING AREAS SINCE THIS MAY CAUSE THE COLOR TONE TO VARY.
- SURE IT IS FREE OF TWISTING, WARPING AND DISTORTION. 6. INSTALL THE LCD MODULE BY USING THE MOUNTING HOLES. WHEN MOUNTING THE LCD MODULE MAKE
- ACCELERATED BY WATER DROPLETS, MOISTURE CONDENSATION OR A CURRENT FLOW IN A HIGH-HUMIDITY ENVIRONMENT 7. EXERCISE CARE TO MINIMIZE CORROSION OF THE ELECTRODE. CORROSION OF THE ELECTRODES IS
- 8. NC TERMINAL SHOULD BE OPEN. DO NOT CONNECT ANYTHING.
 9. IF THE LOGIC CIRCUIT POWER IS OFF, DO NOT APPLY THE INPUT SIGNALS
- 10. AVOID CONTACTING OIL AND FATS.
- DIRTY THE POLARIZERS. AFTER PRODUCTS ARE TESTED AT LOW TEMPERATURE THEY MUST BE WARMED UP IN 5. WHEN TURNING THE POWER ON, INPUT EACH SIGNAL AFTER THE POSITIVE/NEGATIVE VOLTAGE BECOMES A CONTAINER BEFORE COMING IN CONTACT WITH ROOM TEMPERATURE AIR. 11. CONDENSATION ON THE SURFACE AND CONTACT WITH TERMINALS DUE TO COLD WILL DAMAGE, STAIN OR
- MAY CAUSE DEFORMATION OR COLOR FADING. 12. WIPE OFF SALIVA OR WATER DROPS IMMIDEATLY, CONTACT WITH WATER OVER A LONG PERIOD OF TIME

PRECAUTION OF SOLDERING TO THE LCM

- 1, OBSERVE THE FOLLOWING WHEN SOLDERING LEAD WIRE, CONNECTOR CABLE AND ETC. TO THE LCD MODULE.
- SOLDERING IRON TEMPERATURE: 300~350°CSOLDERING TIME: ≤3 SEC.

- SOFT AND EASILY SCRATCHED. HANDLE THIS POLARIZER CAERFULLY.

 SOLDER: LUILCIIC SOLDER.

 SOLDER: LUILCIIC SOLDER.

 SOLDER: LUILCIIC SOLDER.

 SOLDER.

 SOLDER.

 SOLDER: LUILCIIC SOLDER.

 SOLD ACCORDINGLY.
- OPERATION. (THIS DOSE NOT APPLY IN THE CASE OF A NON-HALOGEN TYPE OF FLUX.) IT IS RECOMMENDED 2. IF SOLDERING FLUX IS USED, BE SURE TO REMOVE ANY REMANING FLUX AFTER FINISHING TO SOLDERING THAT YOU PROTECT THE LCD SURFACE WITH A COVER DURING SOLDERING TO PREVENT ANY DAMAGE DUE TO FLUX SPATTERS

PRECAUTION FOR OPERATION

- 1. VIEWING ANGLE VARIES WITH THE CHANGE OF LIQUID CRYSTAL DRIVING VOLTAGE (Vo). ADJUST Vo SHOW THE BEST CONTRAST.
- 2. DRIVING THE LCD IN THE VOLTAGE ABOVE THE LIMIT SHORTERNS ITS LIFETIME
- HOWEVER, IT WILL RECOVER WHEN IT RETURNS TO THE SPECIFIED TEMPERATURE RANGE 3. RESPONSE TIME IS GREATLY DELAYED AT TEMPERATURE BELOW THE OPERATING TEMPERATURE RANGE
- HOWEVER, IT WILL RETURN TO NORMAL IF IT IS TURNED OFF AND THEN BACK ON 4. IF THE DISPLAY AREA IS PUSHED HARD DURING OPERATION, THE DISPLAY WILL BECOME ABNORMAL
- DESIGN). STABLE (BELOW FIGURE IS A GENERAL ILLUSRATION WHERE TYPICAL VALUE DEPENDS ON INDIVIDUAL PRODUCT

1.5us

ELECTRO-STATIC DISCHARGE CONTROL

- 1, SINCE THIS MODULE USES A CMOS LSI, THE SAME CAERFUL ATTENTION SHOULD BE PAID ELECTROSTATIC DISCHARGE AS FOR AN ORDINARY CMOS IC.
- 2. BE SURE TO GROUND THE BODY WHEN HANDLING THE LCD MODULES. TOOLS REQUIRED FOR ASSEMBLING, SUCH AS SOLDERING IRONS, MUST BE PROPERLY GROUNDED.
 3. TO REDUCE THE AMOUNT OF STATIC ELECTRICITY GENERATED, DO NOT CONDUCT ASSEMBLING AND OTHER
- WORK UNDER DRY CONDITIONS. TO REDUCE THE GENERATION OF STATIC ELECTRICITY, BE CARFUL THAT THE
- AIR IN THE WORK IS NOT TOO DRIED. A RELATIVE HUMIDITY OF 50%-60% IS RECOMMENDED.

 4. THE LCD MODULE IS COATED WITH A FILM TO PROTECT THE DISPLAY SURFACE. EXERCISE CARE WHEN PEELING OFF THIS PROTECTIVE FILM SINCE STATIC ELECTRICITY MAY BE GENERATED.

SND

(signal)

7

min.50

3

max. (for grapgics)

GND

(negative voltage)

positive voltage

0 min.50

ms max.

(for

grapgics)

5. WHEN SOLDERING THE TERMINAL OF LCM, MAKE CERTAIN THE AC POWER SOURCE FOR THE SOLDERING IRON DOES NOT LEAK.

REV.

*UNLESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), XX=±0.5 (±0.020), XXX=±0.25 (±0.010), XXXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MN= +DECIMAL PRECISION -0.000 CONFIDENTIAL INFORMATION
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OUR MANY YEARS OF EXPERIENCE DATA ACCUMULATION INDICATE THAT SOLDER HEAT IS A MAJOR CAUSE OF EARLY AND FUTURE FAILURE. PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.

6:00 VIEW, LED BACKLIGHT, -20°C TO +70°C OPERATING TEMP.

ACTIVE MATRIX FULL COLOR TFT PANEL

_CT-H320240M57W PART NUMBER

DRAWN BY: 290 E. HELEN ROAD

PALATINE, IL 60067–6976 PHONE: +1.847.359.2790 US WEB: www.lumex.com TW WEB: www.lumex.com.tw

PRECISION

CHECKED BY: APPROVED BY: DATE: PAGE: 04.28.09 7 OF 9

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SCALE Z A

ROHS COMPLIANT PRODUCT

PACKAGING STANDARD

POLYBROMINATED DIPHENYL ETHERS (PBDEs)

LESS THAN 1000PPM

PRODUCT NO.	LCT-H320240M57W	RELEASE DATE 04/APR. 2007	04/APR. 2007
PRODUCT NAME.	TFT MODULE	PREPARE BY:	
SUPPLIER	JETUP ELECTRONIC (SHENZHEN) CO LTD	RECYCLE	NO
QUANTITY/ EACH BOX	168 PCS.	BOX MATERIAL PAPER CARTON	PAPER CARTON
OUTER CARTON BOX SIZE	465mm x 405mm x 305mm	BOX TYPE	NEW
QUANTITY/ INER BOX QUANTITY/ OUTER BOX	12 X 7 X 2 = 168 PCS.	WEIGHT	8.6 KG

THERE ARE 12 PCS LCD PER EACH ANTI-STATIC PLASTIC PLATE. THERE ARE 7 LAYER PLASTIC PLATES PER EACH INNER CARTON BOX. THERE ARE 2 INNER CARTON BOX PER EACH OUTER CARTON BOX.

STORAGE

- 1. WHEN STORING LCDS AS SPARES FOR SOME YEARS, THE FOLLOWING PRECAUCTIONS ARE NECESSARY.
- STORE THEM IN A SEALED POLYETHYLENE BAG. IF PROPERLY SEALED, THERE IS NO NEED FOR DESICCANI.
 STORE THEM IN A DARK PLACE. DO NOT EXPOSE TO SUNLIGHT OR FLUORESCENT LIGHT, KEEP THE TEMPERATURE BETWEEN 0°C AND 35°C.
- ENVIRONMENTAL CONDITIONS:
- 5. DO NOT LEAVE THEM FOR MORE THAN 168HRS. AT 60°C. 6. SHOULD NOT BE LEFT FOR MORE THAN 48HRS. AT -20°C

SAFETY

- 1. ITS RECOMMENDED TO CRUSH DAMAGED OR UNNECESSARY LCD INTO PIECES AND WASH THEM OFF WITH SOLVENTS SUCH AS ACETONE AND ETHANOL, WHICH SHOULD LATER BE BURNED.
 2. IF ANY LIQUID LEAKS OUT OF DAMAGED GLASS CELL AND COMES IN CONTACT WITH THE HANDS, WASH OFF THOROUGHLY WITH SOAP AND WATER.

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5.7" ACTIVE MATRIX FULL COLOR TFT PANEL

6:00 VIEW, LED BACKLIGHT, -20°C TO +70°C OPERATING TEMP.

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DRAWN BY: CHECKED BY:

290 E. HELEN ROAD
PALATINE, IL 60067–6976
PHONE: +1.847.359.2790
US WEB: www.lumex.com
TW WEB: www.lumex.com.tw

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