

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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EOL announced Product

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## 2SK1566, 2SK1567

Silicon N Channel MOS FET

REJ03G0953-0200  
(Previous: ADE-208-1293)  
Rev.2.00  
Sep 07, 2005

### Application

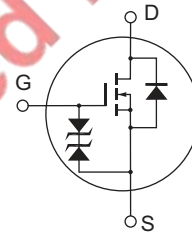
High speed power switching

### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

### Outline

RENESAS Package code: PRSS0003AD-A  
(Package name: TO-220FM)



1. Gate
2. Drain
3. Source

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	$V_{DSS}$	2SK1566	450	V
		2SK1567	500	
Gate to source voltage	$V_{GSS}$	$\pm 30$	V	
Drain current	$I_D$	7	A	
Drain peak current	$I_{D(pulse)}^{*1}$	28	A	
Body to drain diode reverse drain current	$I_{DR}$	7	A	
Channel dissipation	$P_{ch}^{*2}$	35	W	
Channel temperature	$T_{ch}$	150	°C	
Storage temperature	$T_{stg}$	-55 to +150	°C	

Note: 1.  $PW \leq 10 \mu s$ , duty cycle  $\leq 1\%$ 2. Value at  $T_C = 25^\circ C$ 

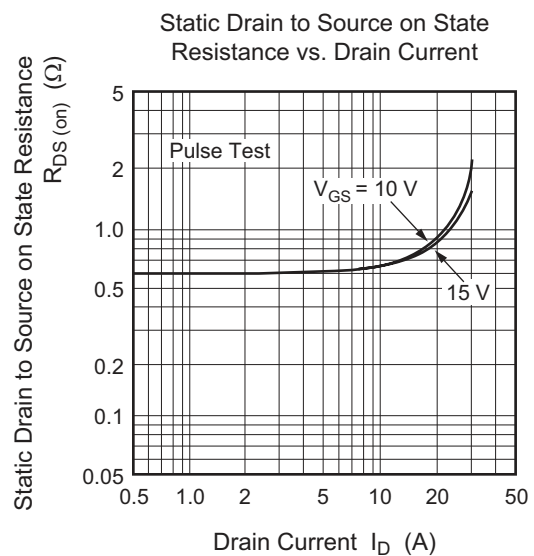
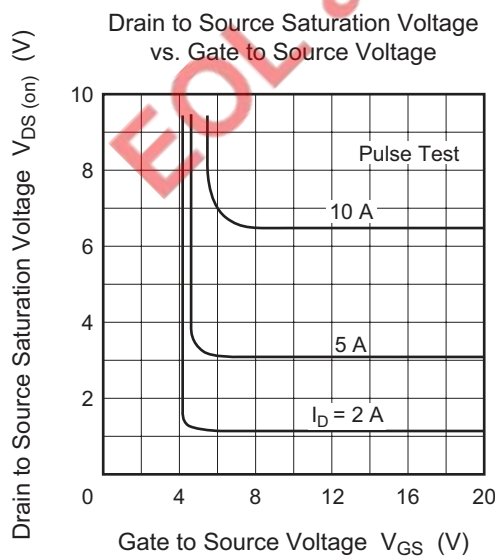
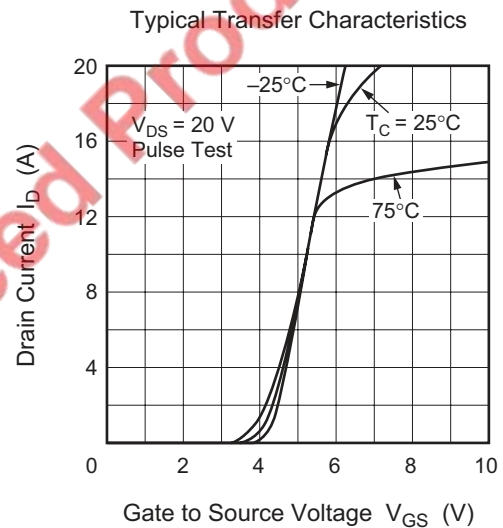
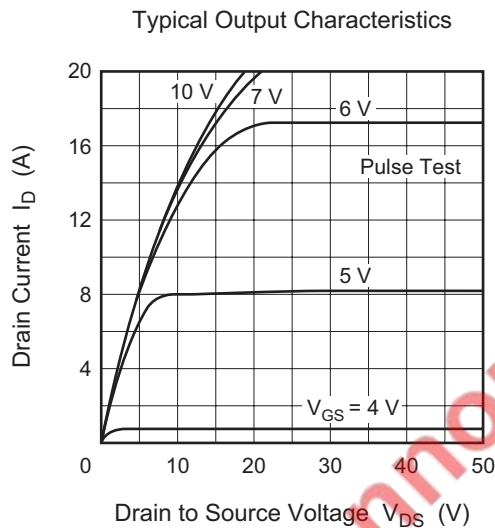
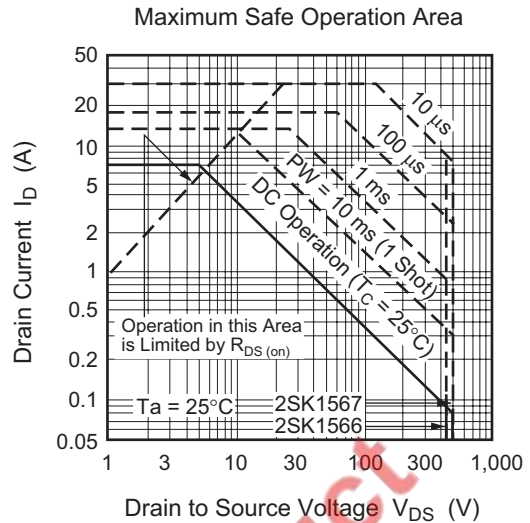
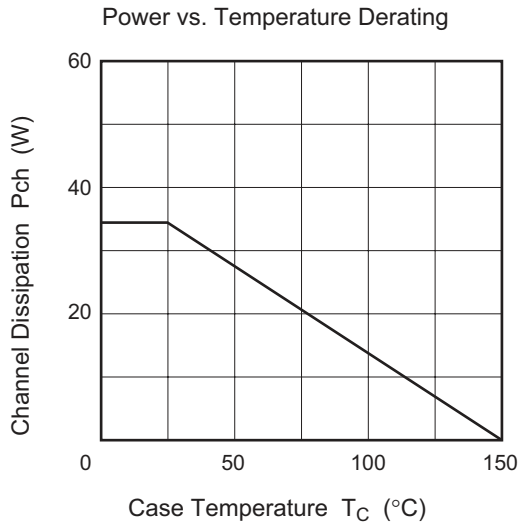
## Electrical Characteristics

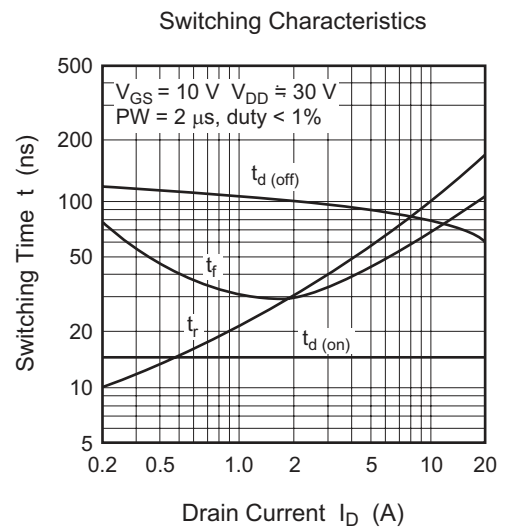
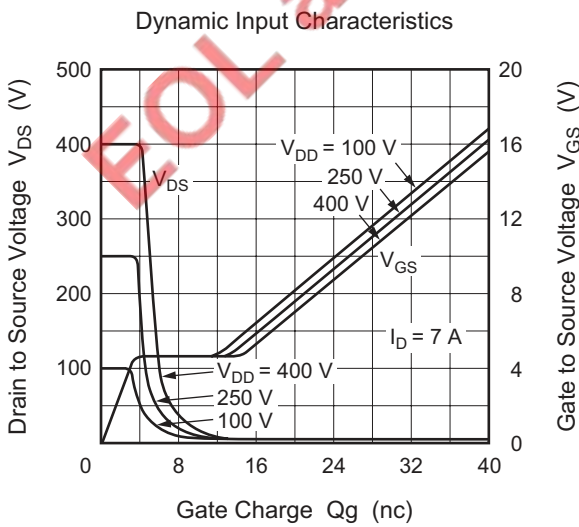
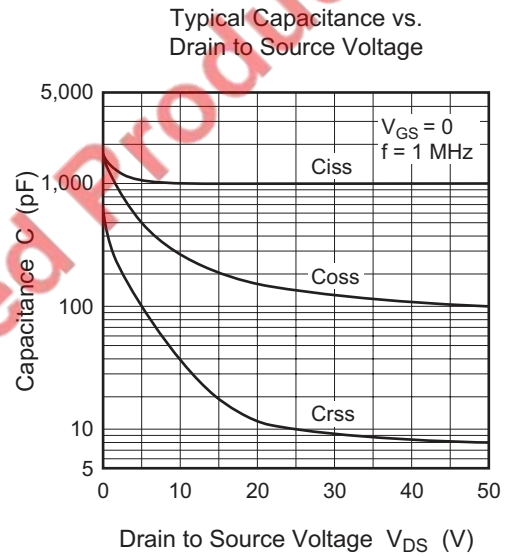
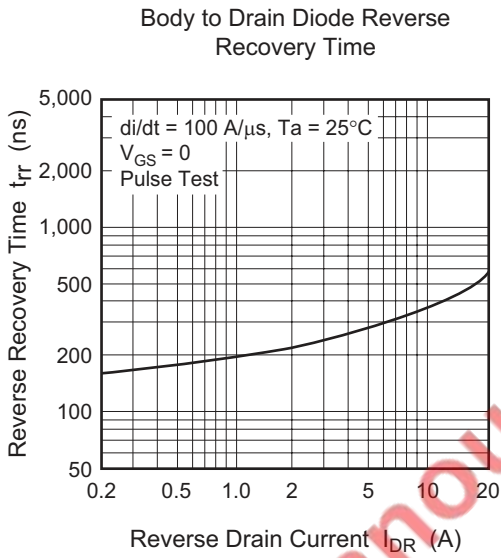
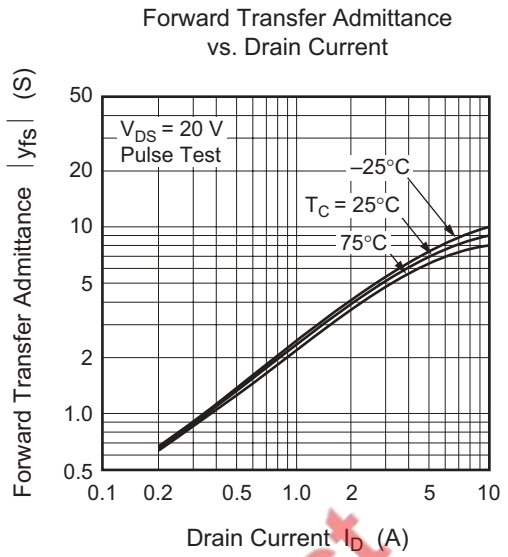
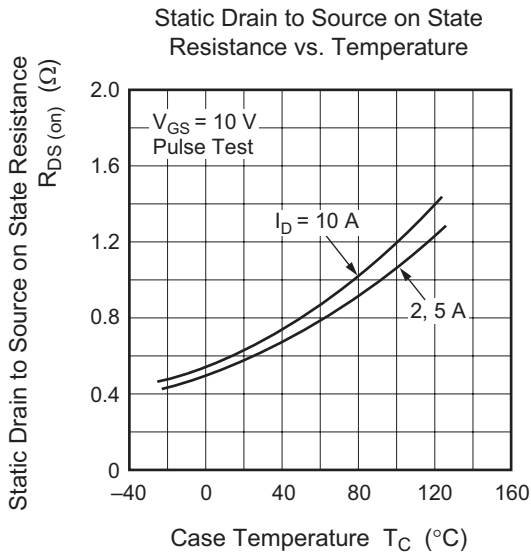
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions	
Drain to source breakdown voltage	$V_{(BR)DSS}$	2SK1566	450	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
		2SK1567	500	—	—		
Gate to source breakdown voltage	$V_{(BR)GSS}$	$\pm 30$	—	—	V	$I_G = \pm 100 \mu A$ , $V_{DS} = 0$	
Gate to source leak current	$I_{GSS}$	—	—	$\pm 10$	$\mu A$	$V_{GS} = \pm 25 \text{ V}$ , $V_{DS} = 0$	
Zero gate voltage drain current	$I_{DSS}$	2SK1566	—	—	250	$\mu A$	$V_{DS} = 360 \text{ V}$ , $V_{GS} = 0$
		2SK1567	—	—	—		$V_{DS} = 400 \text{ V}$ , $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	—	3.0	V	$I_D = 1 \text{ mA}$ , $V_{DS} = 10 \text{ V}$	
Static drain to source on state resistance	$R_{DS(on)}$	2SK1566	—	0.6	0.8	$\Omega$	$I_D = 4 \text{ A}$ , $V_{GS} = 10 \text{ V}^{*3}$
		2SK1567	—	0.7	0.9		
Forward transfer admittance	$ y_{fs} $	4.0	6.5	—	S	$I_D = 4 \text{ A}$ , $V_{DS} = 10 \text{ V}^{*3}$	
Input capacitance	$C_{iss}$	—	1050	—	pF	$V_{DS} = 10 \text{ V}$ , $V_{GS} = 0$ ,	
Output capacitance	$C_{oss}$	—	280	—	pF	$f = 1 \text{ MHz}$	
Reverse transfer capacitance	$C_{rss}$	—	40	—	pF		
Turn-on delay time	$t_{d(on)}$	—	15	—	ns	$I_D = 4 \text{ A}$ , $V_{GS} = 10 \text{ V}$ , $R_L = 7.5 \Omega$	
Rise time	$t_r$	—	55	—	ns		
Turn-off delay time	$t_{d(off)}$	—	95	—	ns		
Fall time	$t_f$	—	40	—	ns		
Body to drain diode forward voltage	$V_{DF}$	—	0.95	—	V	$I_F = 7 \text{ A}$ , $V_{GS} = 0$	
Body to drain diode reverse recovery time	$t_{rr}$	—	320	—	ns	$I_F = 7 \text{ A}$ , $V_{GS} = 0$ , $di_F/dt = 100 \text{ A}/\mu s$	

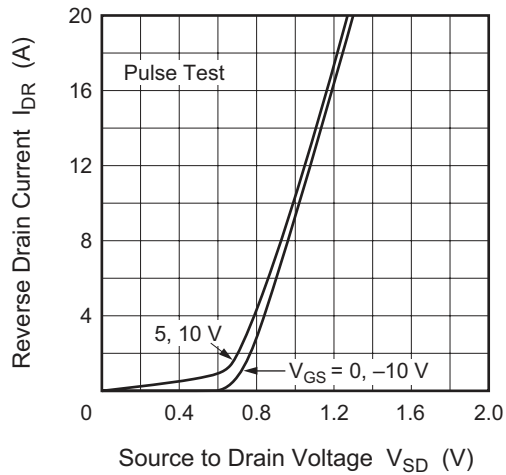
Note: 3. Pulse test

### Main Characteristics

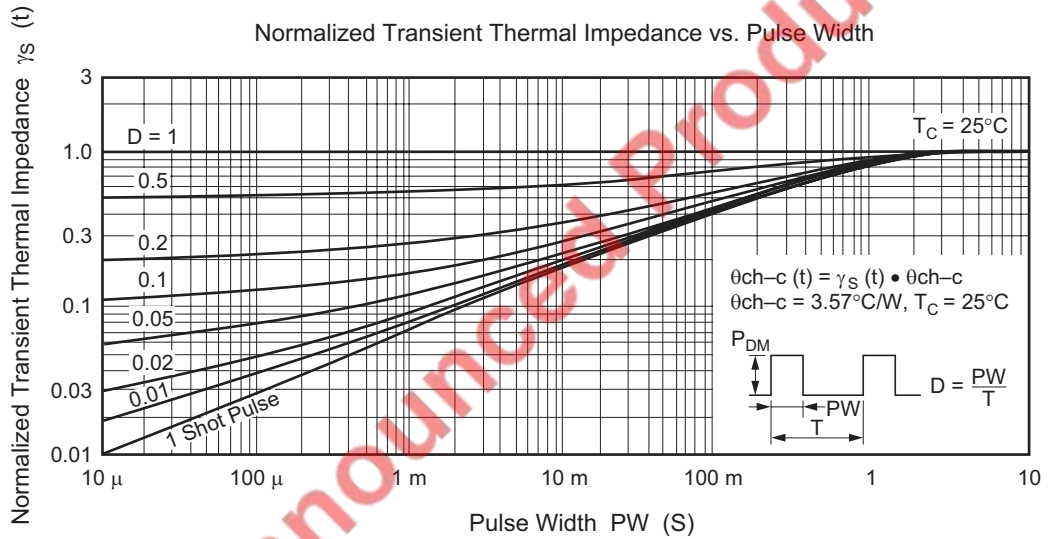




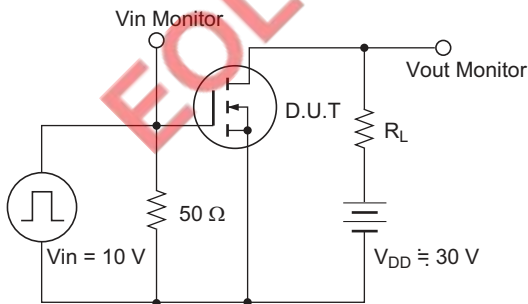
Reverse Drain Current vs. Source to Drain Voltage



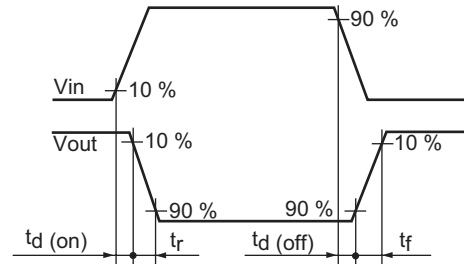
Normalized Transient Thermal Impedance vs. Pulse Width



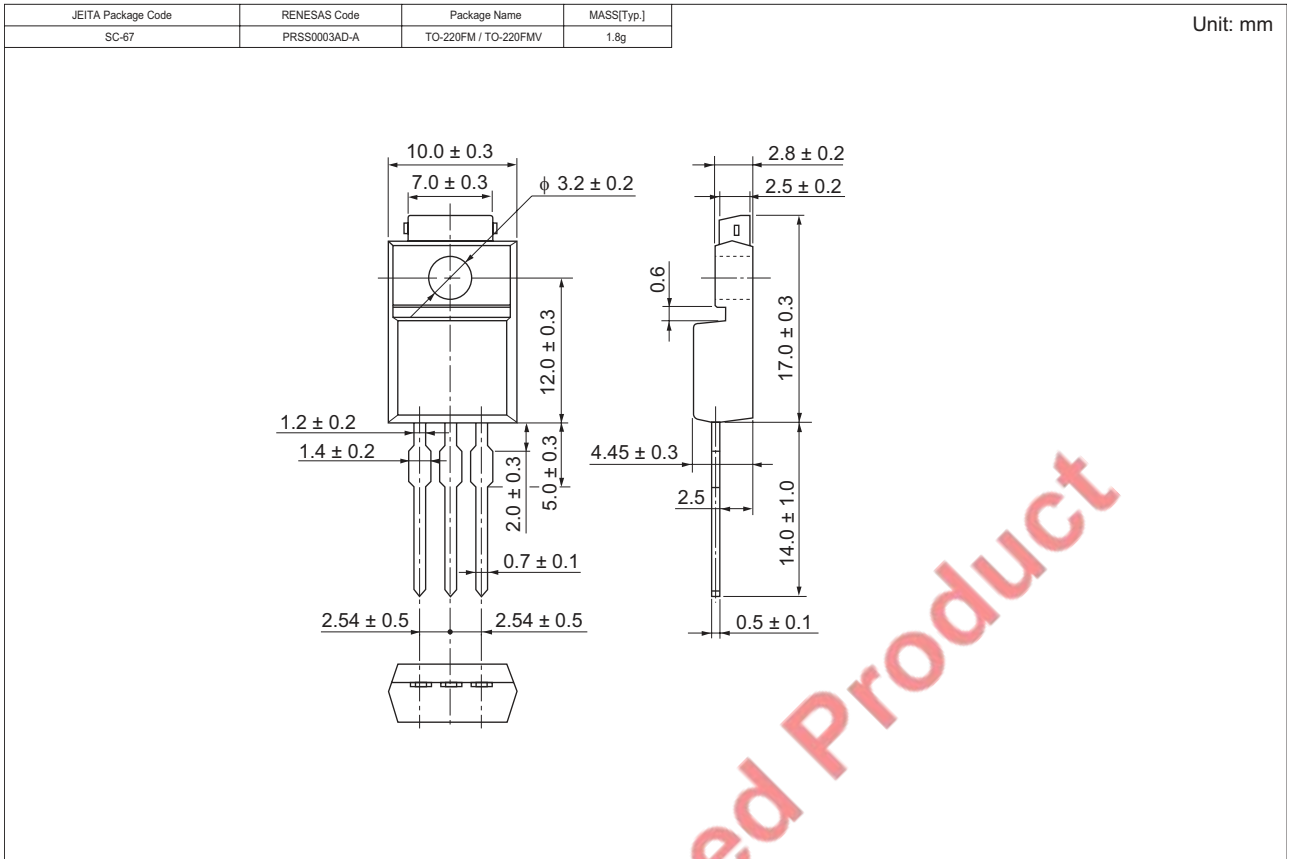
Switching Time Test Circuit



Waveforms



### Package Dimensions



### Ordering Information

Part Name	Quantity	Shipping Container
2SK1566-E	500 pcs	Box (Sack)
2SK1567-E	500 pcs	Box (Sack)

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