



SPECIFICATIONS

CUSTOMER : _____
MODEL NO. : **GFT043HA480272Y**
Version : **A**
DATE : **2011.02.14**
CERTIFICATION : **ROHS**

Customer Sign	Sales Sign	Approved By	Prepared By
			

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ISO 14001:2004
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Contents

1. General Specifications	4
2. Pin Assignment	5
2.1. TFT LCD Panel Driving Section.....	5
2.2. Touch Screen Panel Section.....	7
3. Operation Specifications.....	8
3.1. Absolute Maximum Ratings	8
3.2. Typical operation conditions	9
3.3. Backlight Driving Conditions	9
3.4. Power Sequence	10
3.5. Timing Characteristics	11
3.5.1. Timing Conditions.....	11
3.5.2. Timing Diagram	12
4. Touch Screen Panel Specifications	13
4.1. Electrical Characteristics	13
4.2. Mechanical & Reliability Characteristics	14
4.3. Linearity Definition	15
4.4. Housing design guide	16
5. Optical Specifications	17
6. Reliability Test Items	21
7. General Precautions	22
7.1. Safety	22
7.2. Handling	22
7.3. Static Electricity.....	22
7.4. Storage	22
7.5. Cleaning	22
8. Mechanical Drawing	23



1.General Specifications

No.	Item	Specification	Remark
1	LCD size	4.3 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	480 × 3(RGB) × 272	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.066(W) × 0.198(H) mm	
6	Active area	95.04 (W) × 53.856(H) mm	
7	Module size	105.5(W) × 67.2(H) × 4.05(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight Power consumption	0.558W(Typ.)	Note 2
12	Panel Power consumption	0.056W(Typ.)	Note 3

Note 1: Refer to Mechanical Drawing.



2.Pin Assignment

2.1. TFT LCD Panel Driving Section

FPC Connector is used for the module electronics interface. The recommended model is “FH19SC-40S-0.5SH” manufactured by HIROSE.

Pin No.	Symbol	I/O	Function	Remark
1	VLED-	P	Power for LED backlight cathode	
2	VLED+	P	Power for LED backlight anode	
3	GND	P	Power ground	
4	VDD	P	Power voltage	
5	R0	I	Red data (LSB)	
6	R1	I	Red data	
7	R2	I	Red data	
8	R3	I	Red data	
9	R4	I	Red data	
10	R5	I	Red data	
11	R6	I	Red data	
12	R7	I	Red data (MSB)	
13	G0	I	Green data (LSB)	
14	G1	I	Green data	
15	G2	I	Green data	
16	G3	I	Green data	
17	G4	I	Green data	
18	G5	I	Green data	
19	G6	I	Green data	
20	G7	I	Green data (MSB)	



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21	B0	I	Blue data (LSB)	
22	B1	I	Blue data	
23	B2	I	Blue data	
24	B3	I	Blue data	
25	B4	I	Blue data	
26	B5	I	Blue data	
27	B6	I	Blue data	
28	B7	I	Blue data (MSB)	
29	GND	P	Power ground	
30	CLK	I	Pixel clock	
31	DISP	I	Display on/off	
32	NC	-	No connection	
33	NC	-	No connection	
34	DE	I	Data Enable	
35	NC	-	No connection	
36	GND	P	Power ground	
37	X1	I/O	Right electrode – differential analog	
38	Y1	I/O	Bottom electrode – differential analog	
39	X2	I/O	Left electrode – differential analog	
40	Y2	I/O	Top electrode – differential analog	

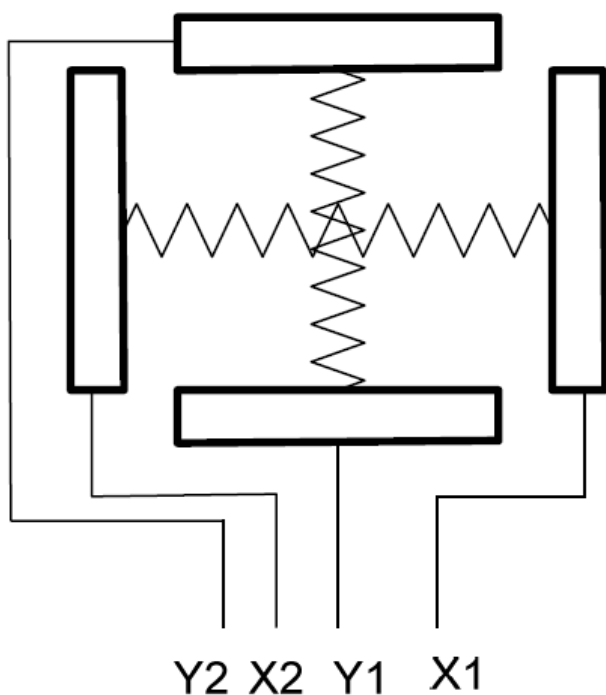
Note: I: input, O: output t, P: Power



2.2. Touch Screen Panel Section

Pin No.	Symbol	I/O	Function	Remark
1	X1	Right	Right electrode – differential analog	
2	Y1	Bottom	Bottom electrode – differential analog	
3	X2	Left	Left electrode – differential analog	
4	Y2	Top	Top electrode – differential analog	

Note: Touch Screen Panel Block



Top View



3. Operation Specifications

3.1. Absolute Maximum Ratings

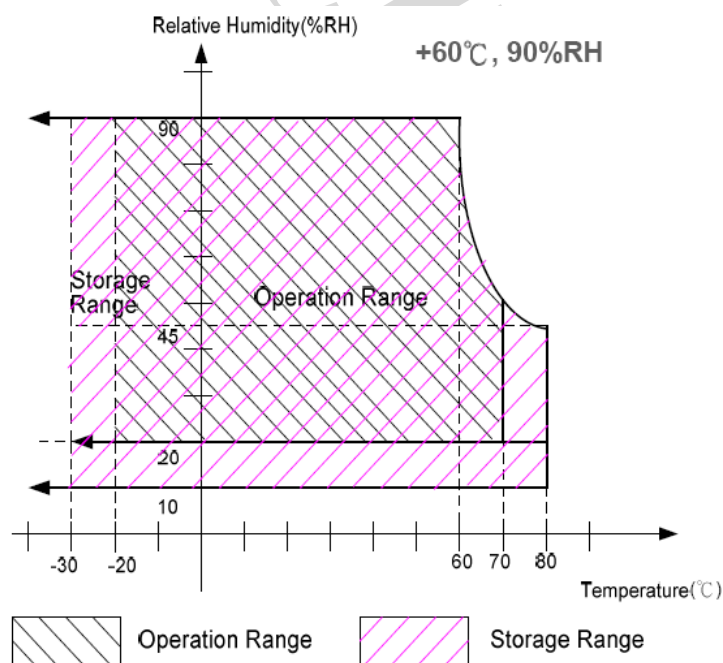
Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	VCC	-0.5	5.0	V	
Input signal voltage	Logic input	-0.5	5.0	V	
Operation temperature	T _{OP}	-20	70	°C	Note 3,4
Storage temperature	T _{ST}	-30	80	°C	Note 3,4
LED Reverse Voltage	V _R	-	1.2	V	Each LED Note 2
LED Forward Current	I _F	-	25	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed.

Note 2: V_R Conditions: Zener Diode 20mA

Note 3: 90% RH Max. (Max wet temp. is 60°C)

Maximum wet-bulb temperature is at 60°C or less. And No condensation (no drops of dew)



Note 4: In case of temperature below 0°C, the response time of liquid crystal (LC) becomes slower and the color of panel darker than normal one.



3.2. Typical Operation Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	V _{DD}	3.1	3.3	3.5	V	
Current for Driver	I _{VDD}	-	17	25	mA	V _{DD} =3.2V
Input logic high voltage	V _{IH}	0.8V _{DD}	-	V _{DD}	V	Note1
Input logic low voltage	V _{IL}	GND	-	0.2V _{DD}	V	

Note1: CLK, DE, R0~ R7, G0~ G7, B0~ B7.

3.3 Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED Backlight	V _L	25.2	27.9	31.5	V	Note2
Current for LED Backlight	I _L	18	20	22	mA	
LED life time	-	20,000	-	-	Hr	Note3

Note 1: The “LED life time” is defined as the module brightness decrease to 50% original brightness that the ambient temperature is 25°C and I_L=20mA. The

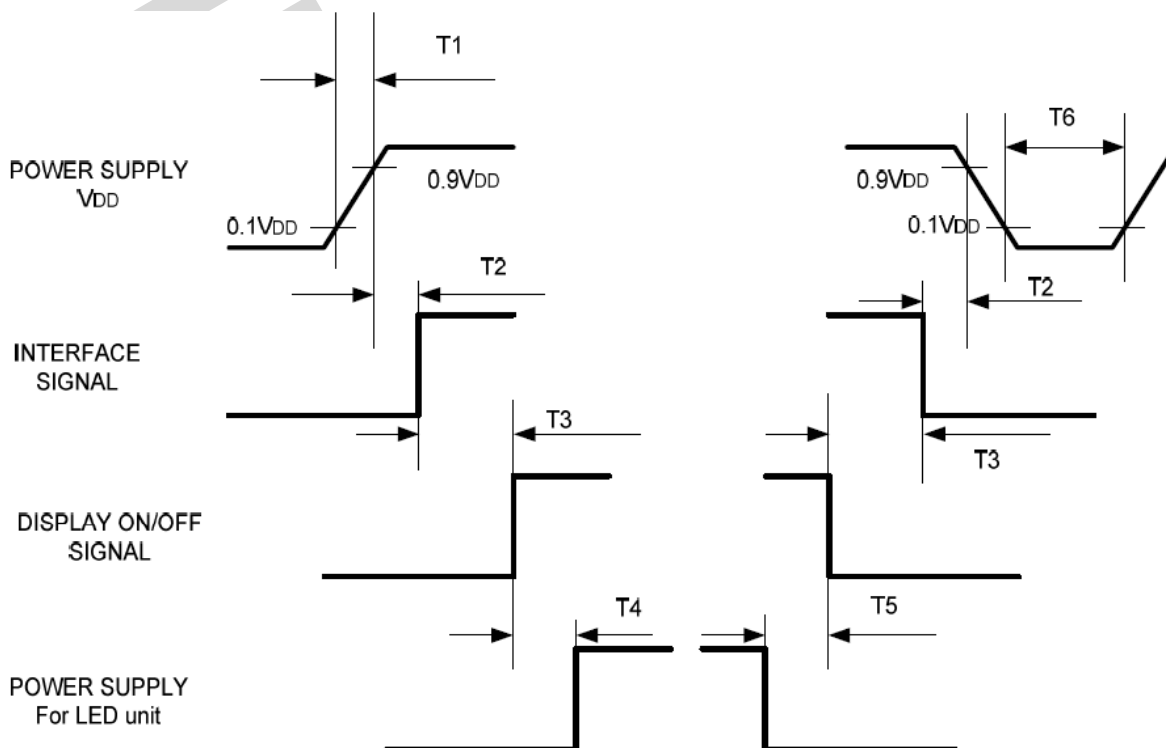
LED lifetime could be decreased if operating I_L is larger than 20 mA.

Note 2: The LED Supply Voltage is defined by the number of LED at Ta=25°C and I_L=20mA.



3.4. Power Sequence

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



Symbol	Specification	Symbol	Specification
T1	$0 \leq T1 \leq 10 \text{ msec}$	T4	$160 \text{ msec} \leq T4$
T2	$0 \leq T2 \leq 100 \text{ msec}$	T5	$160 \text{ msec} \leq T5$
T3	$0 \leq T3 \leq 200 \text{ msec}$	T6	$1 \text{ msec} \leq T6$



3.5. Timing Characteristics

3.5.1. Timing Conditions

Parallel DE mode RGB input timing table

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
CLK frequency	fc _{lk}	7	9	12	MHz
DEV period time	T _v	277	288	400	H
DEV display area	T _{vd}	272			H
DEV blanking	T _{vb}	5	16	128	H
DEH period time	T _h	520	525	800	CLK
DEH display area	T _{hd}	480			CLK
DEH blanking	T _{hb}	40	45	320	CLK
CLK cycle time	T _{clk}	83	110	143	ns
Clock width of high level	T _{cwh}	40	50	60	%
Clock width of low level	T _{cwl}	40	50	60	%
Clock rising time	t _{rk}		-	9	ns
Clock falling time	t _{fk}		-	9	ns
Data Setup Time	t _{desu}	10	-	-	ns
Data Hold Time	t _{dahd}	10	-	-	ns
DE Setup Time	t _{desu}	10	-	-	ns
DE Hold Time	t _{dehd}	10	-	-	ns

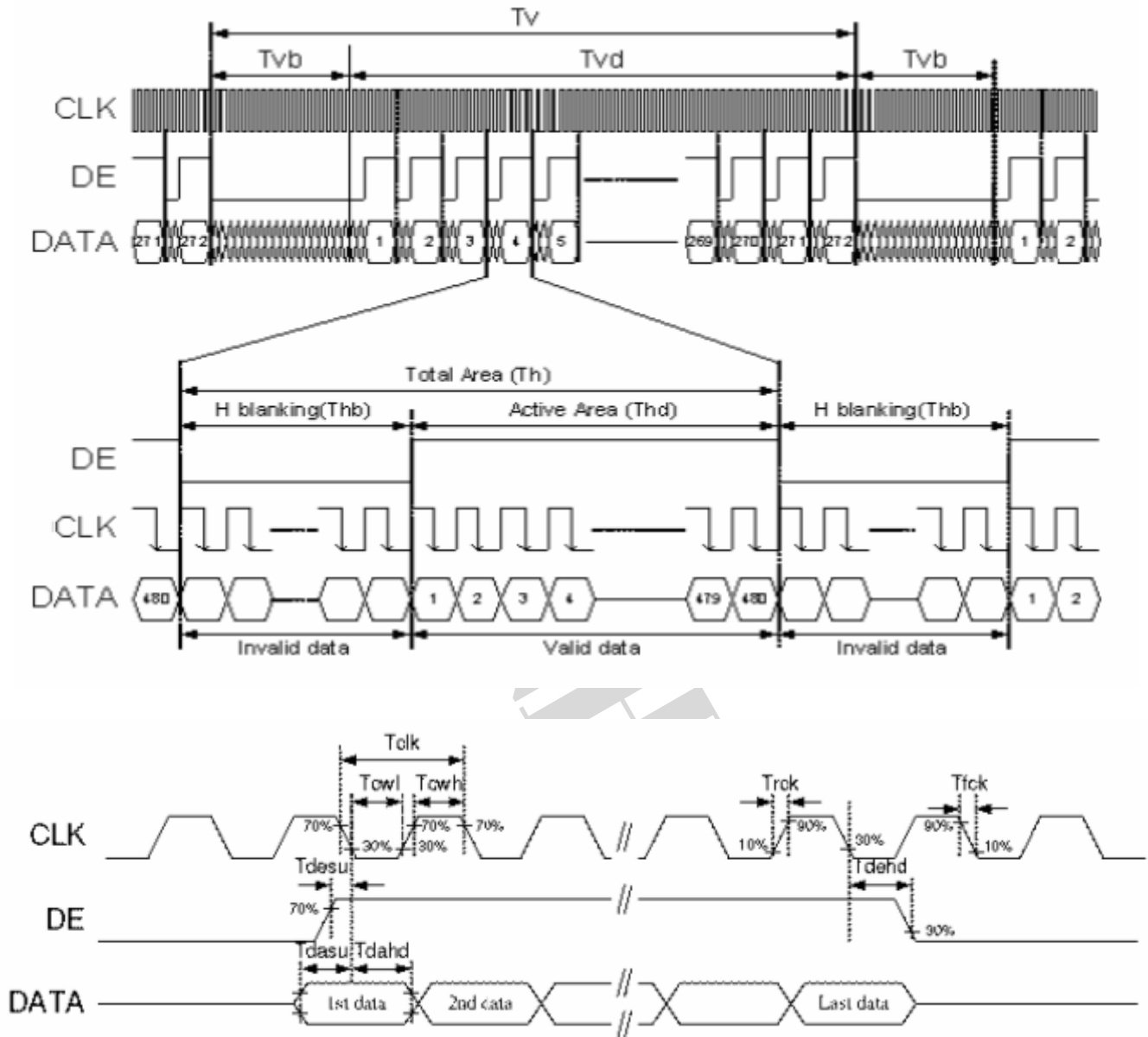


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3.5.2. Timing Diagram





4. Touch Screen Panel Specifications

4.1. Electrical Characteristics

Item	Values			Unit	Remark
	Min.	Typ.	Max.		
Linearity	-1.5	-	1.5	%	Analog X and Y directions
Terminal Resistance	300	-	1500	Ω	X
	100	-	900	Ω	Y
Insulation resistance	25	-	-	M Ω	DC 25V
Voltage	-	5	7	V	DC
Chattering	-	-	10	ms	100k Ω pull-up
Transparency	80	-	-	%	JIS K7105

Note: Avoid operating with hard or sharp material such as a ball point pen or a mechanical pencil except a polyacetal pen (tip R0.8mm or less) or a finger.

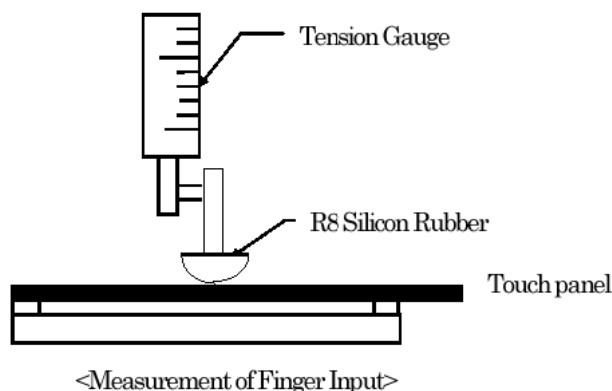
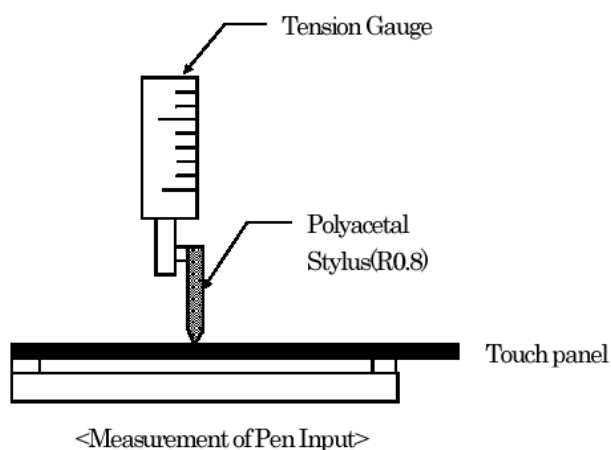


4.2. Mechanical & Reliability Characteristics

Item	Values			Unit	Remark
	Min.	Typ.	Max.		
Activation force	80	-	-	gf	Note 1
Durability-surface scratching	Write 100,000	-	-	characters	Note 2
Durability-surface pitting	1,000,000	-	-	touches	Note 3
Surface hardness	3	-	-	H	JIS K5400

Note 1: Activation force test condition

- (1) Input DC 5V on X direction, Drop off Polyacetal Stylus (R0.8), until output voltage stabilize ,then get the activation force °
- (2) R8.0mm Silicon rubber for finger Activation force test
- (3) Test point: 9 points



Note 2: Measurement for surface area.

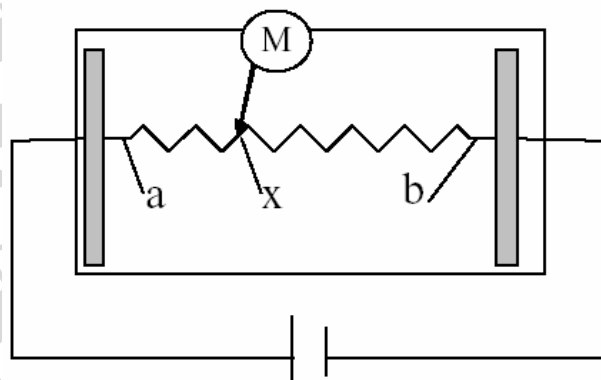
- Scratch 100,000 times straight line on the film with a stylus change every 20,000 times.
- Force: 250gf.
- Speed: 60mm/sec.
- Stylus: R0.8 polyacetal tip.

Note 3: Pit 1,000,000 times on the film with a R0.8 silicon rubber.

- Force: 250gf.
- Speed: 2times/sec.



4.3. Linearity Definition



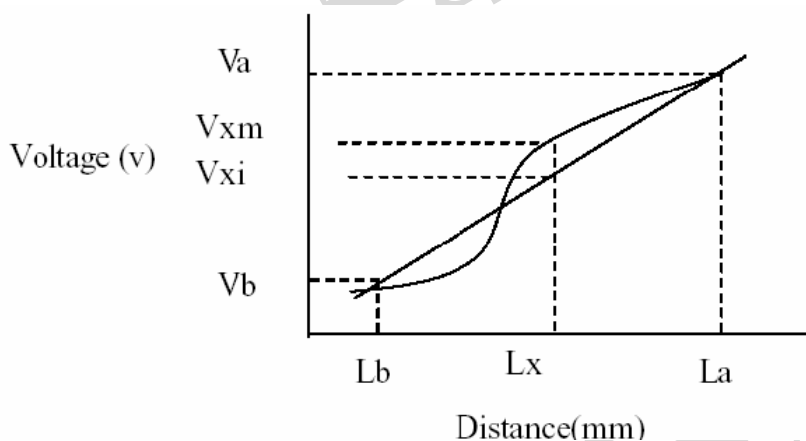
Va: maximum voltage in the active area of touch panel

Vb: minimum voltage in the active area of touch panel

X: random measuring point

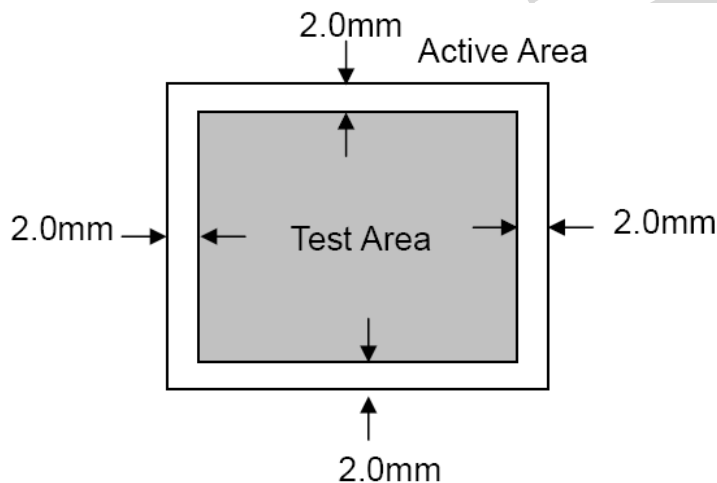
Vxm: actual voltage of Lx point

Vxi: theoretical voltage of Lx point



$$\text{Linearity} = \left[\frac{|V_{xi} - V_{xm}|}{(V_a - V_b)} \right] * 100\%$$

Note: Test area is as follows and operation force is 150gf.

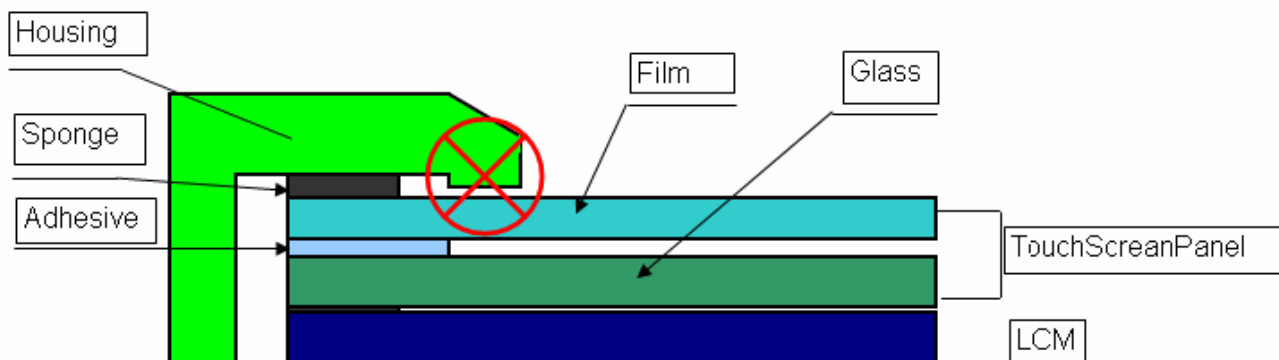




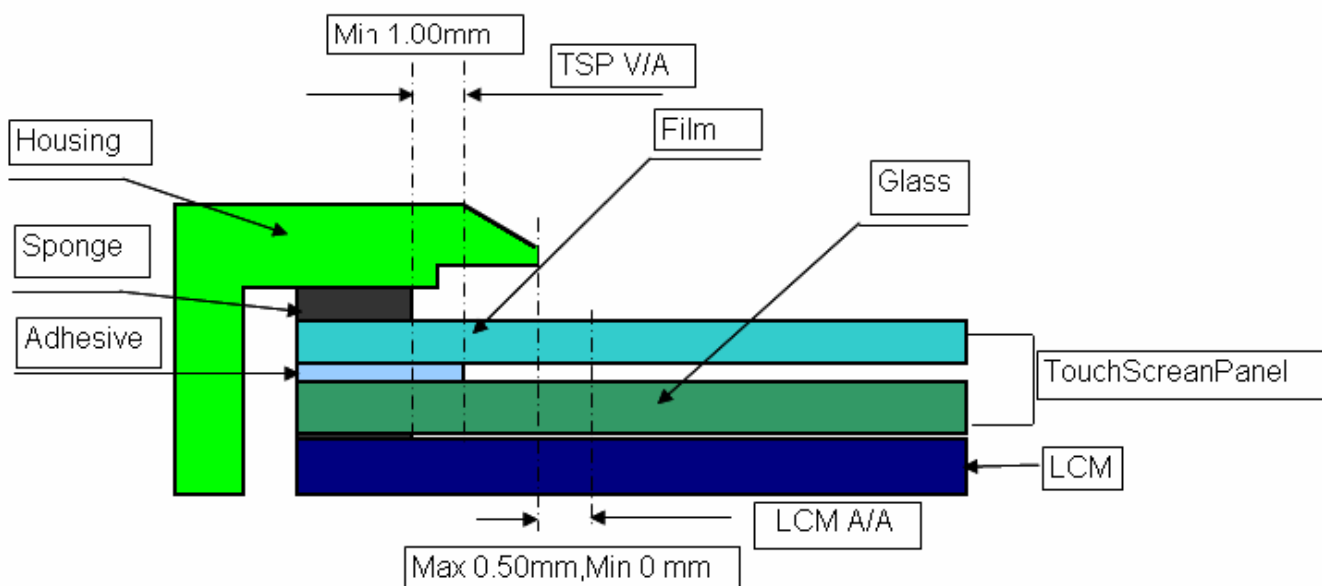
4.4. Housing design guide

Housing design follow as below

- 1) Avoid the design that housing overlap and press on the active area of the LCM
- 2) Give enough gap(over 0.5mm at compressed) between the housing and TSP to protect wrong operating.



- 3) Use a buffer material(Gasket) between the TSP and housing to protect damage and wrong operating
- 4) Avoid the design that buffer material overlap and press on the inside of TSP view area.





5. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥10)	θ_L	$\Phi=180^\circ$ (9 o'clock)	60	70	-	degree	Note 1 Note 7
	θ_R	$\Phi=0^\circ$ (3 o'clock)	60	70	-		
	θ_T	$\Phi=90^\circ$ (12 o'clock)	40	50	-		
	θ_B	$\Phi=270^\circ$ (6 o'clock)	60	70	-		
Response time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	10	20	msec	Note 3
	T_{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR		400	500	-	-	Note 4
Color chromaticity	W_X		0.26	0.31	0.36	-	Note 2
	W_Y		0.28	0.33	0.38	-	Note 5 Note 6 Note7
Luminance	L		250	400	-	cd/m2	Note 6
Luminance uniformity	Y_U		70	75	-	%	Note 8

Test Conditions:

1. $V_{DD}=3.3V$, $I_L=20mA$ (Backlight current), the ambient temperature is $25^\circ C$.
2. The test systems refer to Note 2.



Note 1: Definition of viewing angle

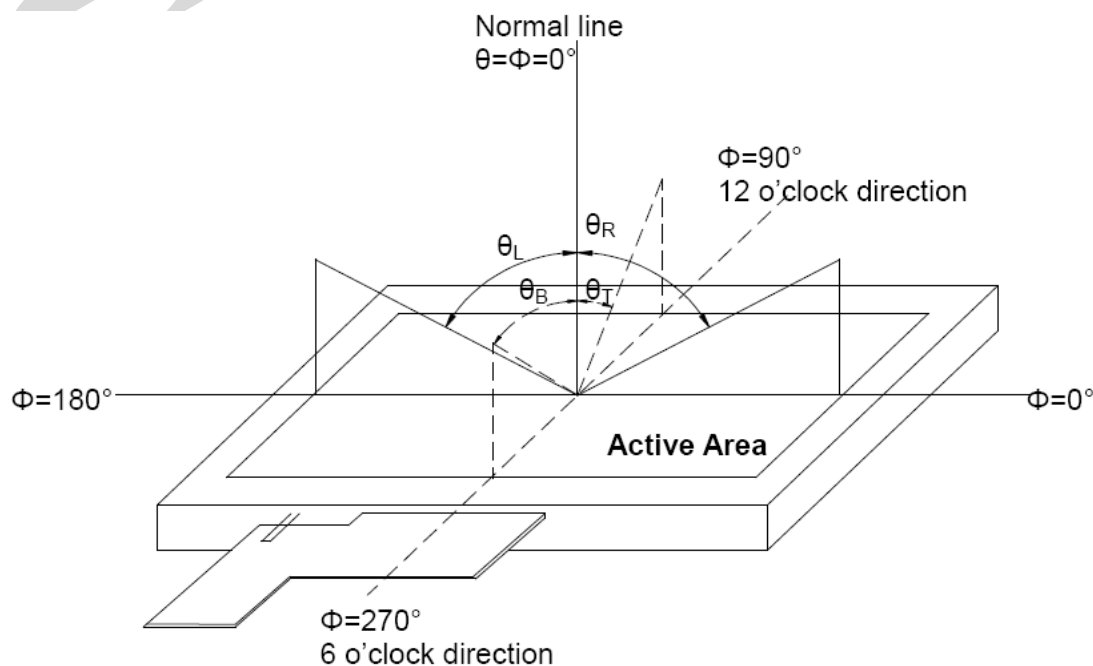


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

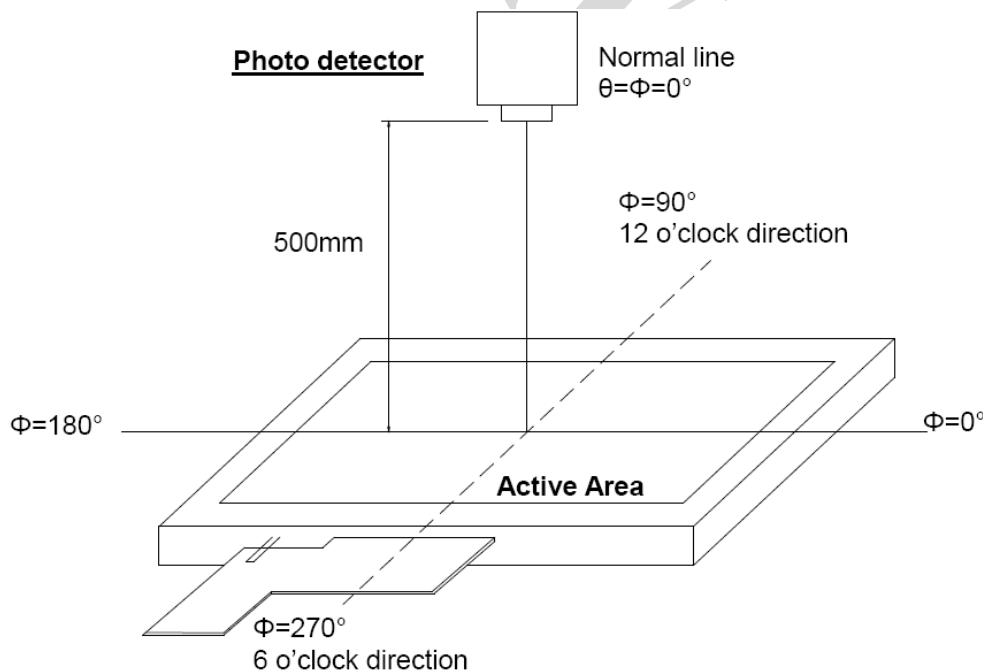


Fig. 4-2 Optical measurement system setup



Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

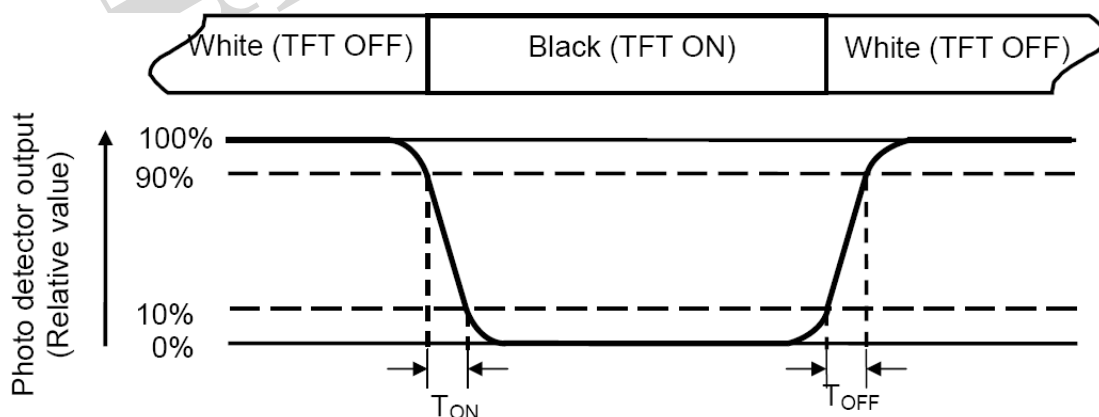


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

Luminance measured when LCD on the "White" state

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $I_L=20\text{mA}$.

Note 7: The values shall be measured without Touch Screen Panel.



Note8: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{\min}}{B_{\max}}$$

L-----Active area length

W----- Active area width

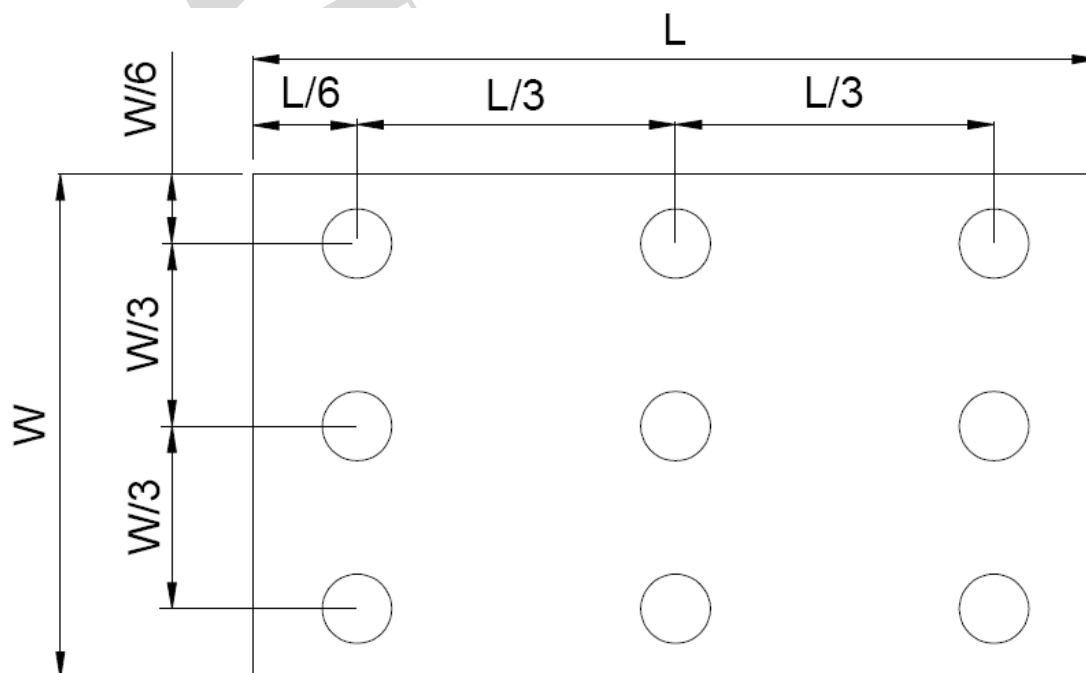


Fig. 4-4 Definition of measuring points

$B_{\max}$: The measured maximum luminance of all measurement position.

$B_{\min}$: The measured minimum luminance of all measurement position.



6. Reliability Test

Item	Test Conditions	Remark
High Temperature Storage	Ta = 80°C 240 hrs	Note 1, 4
Low Temperature Storage	Ta = -30°C 240hrs	Note 1, 4
High Temperature Operation	Ts = 70°C 240hrs	Note 2, 4
Low Temperature Operation	Ta = -20°C 240hrs	Note 1, 4
Operate at High Temperature and Humidity	+60°C, 90%RH 240 hrs	Note 5
Thermal Shock	-30°C/30 min ~ +80°C/30 min for a total 100 cycles, Start with cold temperature and end with high temperature	Note 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.

Note 4: Before cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.

Note 5: Before cosmetic and function tests, the product must have enough recovery time, at least 24 hours at room temperature.



7. General Precautions

7.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

7.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

7.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

7.4. Storage

1. Store the module in a dark room where must keep at $25\pm 10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

7.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

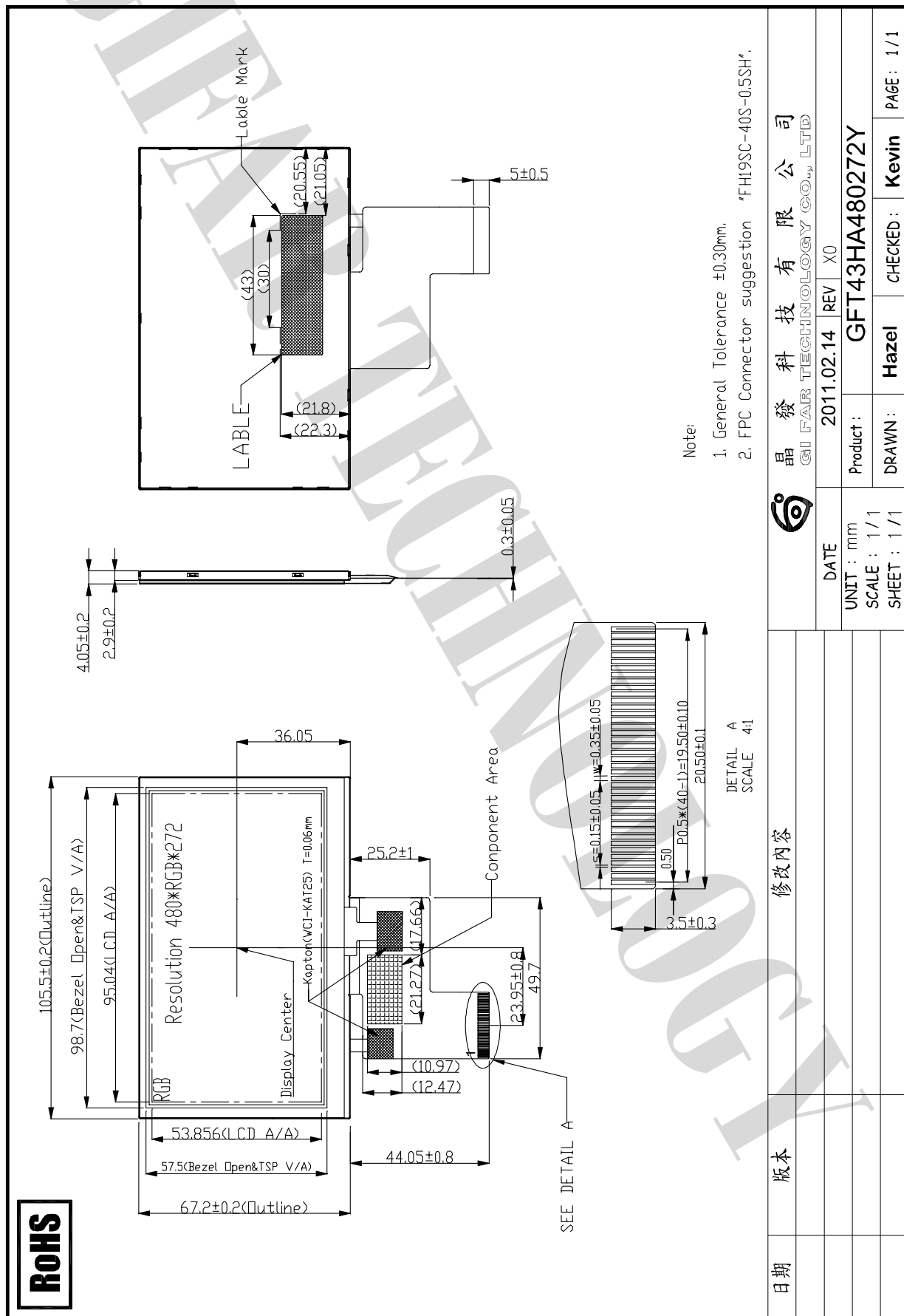


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8. Mechanical Drawing



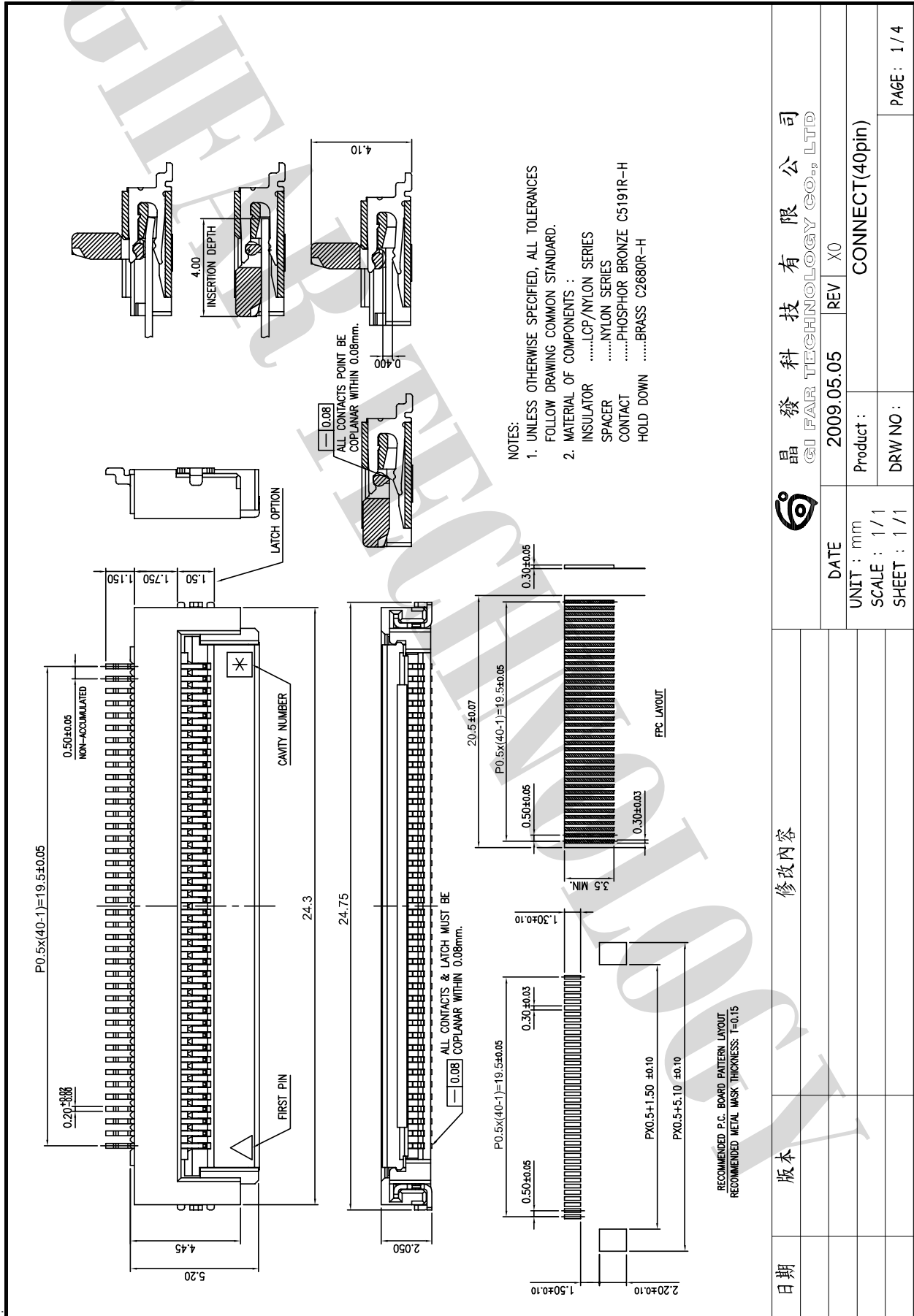


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CONNECT DRAWING



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