# onsemi

# **<u>MOSFET</u> – Power, Single N-Channel** 40 V, 1.39 mΩ, 386 A

# NVMJST1D3N04C

#### Features

- Small Footprint (5x7 mm) for Compact Design
- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Q<sub>G</sub> and Capacitance to Minimize Driver Losses
- TCPAK57 5x7 Top Cool Package
- AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted) Parameter Unit Symbol Value Drain-to-Source Voltage 40 V V<sub>DSS</sub> Gate-to-Source Voltage V<sub>GS</sub> ±20 V Continuous Drain $T_{C} = 25^{\circ}C$ 386 A $I_D$ Current $R_{\theta JC}$ $T_C = 100^{\circ}C$ 273 (Notes 1, 3) Steady State $T_C = 25^{\circ}C$ Power Dissipation $P_D$ 375 W R<sub>0JC</sub> (Note 1) $T_{\rm C} = 100^{\circ}{\rm C}$ 187 **Pulsed Drain Current** 900 A $T_A = 25^{\circ}C, t_p = 10 \ \mu s$ IDM °C Operating Junction and Storage Temperature -55 to T<sub>J</sub>, T<sub>sta</sub> Range +175 $I_{\rm S}$ Source Current (Body Diode) 312 А Single Pulse Drain-to-Source Avalanche E<sub>AS</sub> 739 mJ Energy $(I_{L(pk)} = 19 \text{ A})$ Lead Temperature for Soldering Purposes ΤL °C 260 (1/8" from case for 10 s)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL RESISTANCE MAXIMUM RATINGS

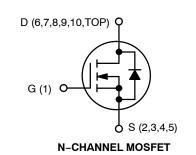
Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State	$R_{\theta JC}$	0.4	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\thetaJA}$	29.2	
Junction-to-Heatsink Top (Note 2)	$\Psi_{JH}$	1.67	
Junction-to-Drain Lead	$\Psi_{JL}$	5.4	
Junction-to-Source Lead	$\Psi_{JL}$	5.3	

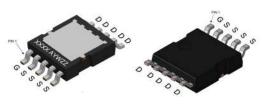
 The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

 2. 2s2p JEDEC51-7 standard PCB mounted to a 25x25x3 (mm) aluminum heatsink with a 12 w/mK TIM interface.

3. Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

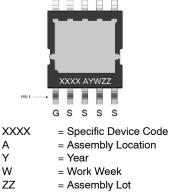
V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
40 V	1.39 m $\Omega$ @ 10 V	386 A





TCPAK57 CASE 760AG





#### **ORDERING INFORMATION**

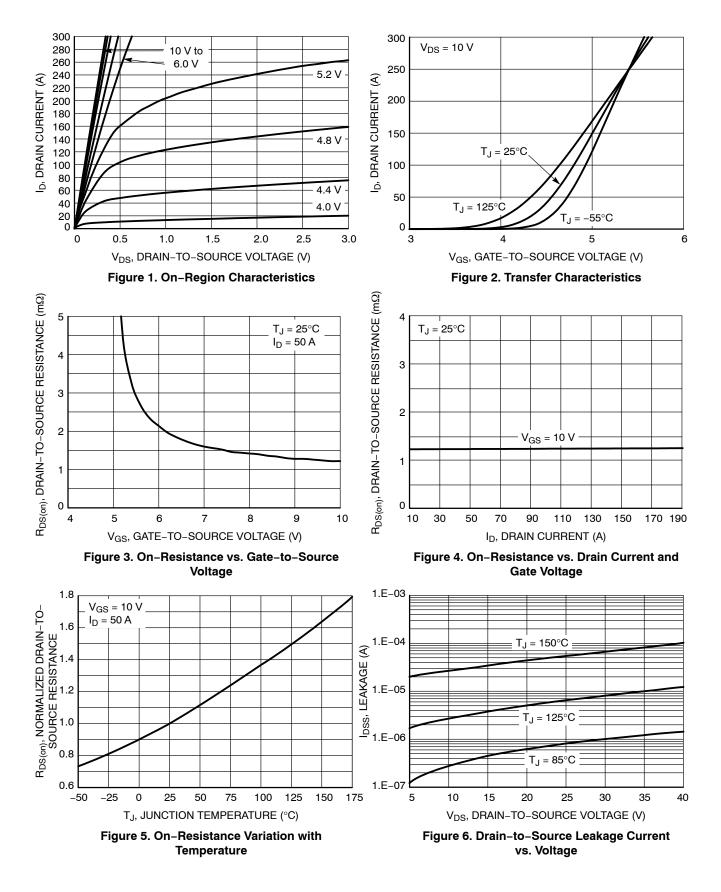
See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise specified)

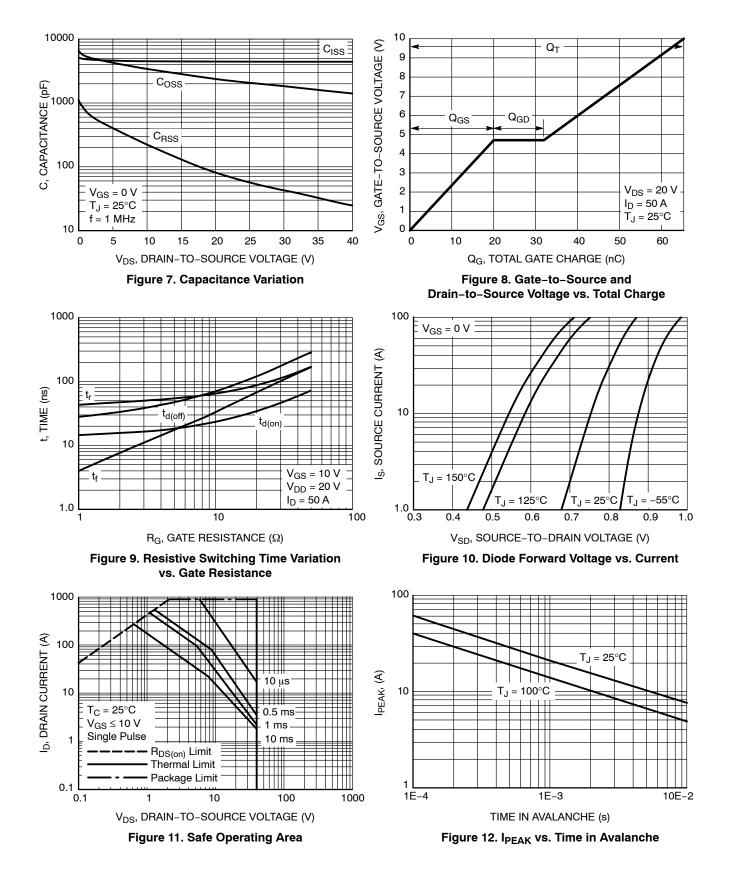
Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS				-	-	-	-
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ = 0 V, I <sub>D</sub> =	= 250 μA	40			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> / T <sub>J</sub>				9.6		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V,$	$T_J = 25^{\circ}C$			10	μA
		V <sub>DS</sub> = 40 V	T <sub>J</sub> = 125°C			100	1
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS}$	<sub>S</sub> = 20 V			100	nA
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> =	= 170 μA	2.5		3.5	V
Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>				-8.6		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 50 A		1.2	1.39	mΩ
Forward Transconductance	9 <sub>FS</sub>	V <sub>DS</sub> =15 V, I <sub>D</sub>	= 50 A		145		S
CHARGES AND CAPACITANCES					-		-
Input Capacitance	C <sub>ISS</sub>				4300		pF
Output Capacitance	C <sub>OSS</sub>	V <sub>GS</sub> = 0 V, f = 1 MH:	z, V <sub>DS</sub> = 25 V		2100		1
Reverse Transfer Capacitance	C <sub>RSS</sub>				59		
Total Gate Charge	Q <sub>G(TOT)</sub>	$V_{GS}$ = 10 V, $V_{DS}$ = 20 V; $I_{D}$ = 50 A			65		nC
Threshold Gate Charge	Q <sub>G(TH)</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 20 V; I <sub>D</sub> = 50 A			13		
Gate-to-Source Charge	Q <sub>GS</sub>				20		
Gate-to-Drain Charge	Q <sub>GD</sub>				12		
Plateau Voltage	V <sub>GP</sub>				4.7		V
SWITCHING CHARACTERISTICS (Note	5)						
Turn-On Delay Time	t <sub>d(ON)</sub>				15		ns
Rise Time	tr	Vcs = 10 V. Vn	s = 20 V.		47		
Turn-Off Delay Time	t <sub>d(OFF)</sub>	$\begin{array}{l} V_{GS} = 10 \; V, \; V_{DS} = 20 \; V, \\ I_{D} = 50 \; A, \; R_{G} = 2.5 \; \Omega \end{array}$			36		
Fall Time	t <sub>f</sub>				9.0		
DRAIN-SOURCE DIODE CHARACTERIS	TICS						
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V,	$T_J = 25^{\circ}C$		0.82	1.2	V
		$I_{\rm S} = 50 \rm{A}$	T <sub>J</sub> = 125°C		0.68		1
Reverse Recovery Time	t <sub>RR</sub>	V <sub>GS</sub> = 0 V, dIS/dt = 100 A/µs, I <sub>S</sub> = 50 A			63		ns
Charge Time	t <sub>a</sub>				34		1
Discharge Time	t <sub>b</sub>				29		1
Reverse Recovery Charge	Q <sub>RR</sub>				92		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 4. Pulse Test: pulse width  $\leq 300 \ \mu$ s, duty cycle  $\leq 2\%$ . 5. Switching characteristics are independent of operating junction temperatures.

#### **TYPICAL CHARACTERISTICS**



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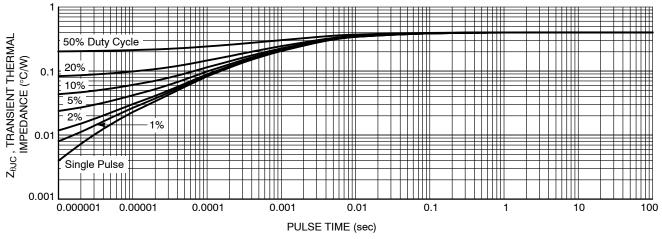


Figure 13. Thermal Characteristics

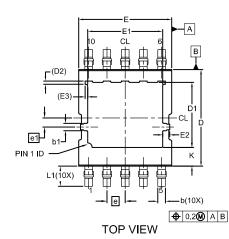
#### **DEVICE ORDERING INFORMATION**

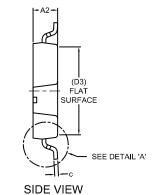
Device	Marking	Package	Shipping <sup>†</sup>
NVMJST1D3N04CTXG	1D34C	TCPAK57 Top Cool (Pb–Free)	3000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS

LFPAK10 7.5x5 CASE 760AG **ISSUE C** 





SOLDER MASK FREE AREA

NOTE 6

(5.30)

1.50(10X)

0.60(10X)

NOTES:

- DIMENSIONING AND TOLERANCING PER ASME 1. Y14.5M, 1994
- UNIT DIMENSION: MILLIMETERS 2.
- 3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.150mm PER SIDE.
- 4. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY. 5. OPTIONAL MOLD FEATURE.
- LAND PAD UNDER THE PACKAGE BODY IS FOR 6. MECHANICAL SUPPORT ONLY, SOLDER CONNECTION IS NOT REQUIRED.
- 7. DIMENSION A1 IS THE LEAD STAND-OFF FROM THE BOTTOM SURFACE OF THE PACKAGE BODY.

	(5.20)	
	10	6
	1.20 5.00	
CL	3.30	
	<sup>1</sup> 1.00 - I	<sup>5</sup>

BOTTOM VIEW

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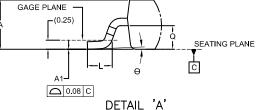
NOTE 5

CL

#### LAND PAD RECOMMENDATION

\*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

	TOM SURFACE OF THE PA			
	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	1.30	1.35	1.45	
A1	-0.05	0.00	0.05	
A2	1.30	1.35	1.40	
b	0.36	0.41	0.46	
b1	0.30	0.40	0.50	
С	0.16	0.21	0.26	
D	5.20	5.30	5.40	
D1	3.47	3.57	3.67	
D2	0.17 REF			
D3	4.82 REF			
Е	5.00	5.10	5.20	
E1	4.02	4.12	4.22	
E2	0.30	0.40	0.50	
E3		0.14 RE	F	
е	1.00 BSC			
e1	0.50 BSC			
К	0.93	1.03	1.13	
Н	7.30	7.50	7.70	
L	0.49	0.69	0.89	
11	0.90	1.10	1.30	
Q Q	0.60	0.65	0.70	
θ	0°	2.5°	5°	



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