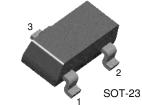


BCW61A/B/C/D

General Purpose Transistor



1. Base 2. Emitter 3. Collector

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a =25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	-32	V
V _{CEO}	Collector-Emitter Voltage	-32	V
V _{EBO}	Emitter-Base Voltage	-5.0	V
I _C	Collector Current	-100	mA
P _C	Collector Power Dissipation	350	mW
T _{STG}	Storage Temperature	-55 ~ 150	°C

Refer to KST5086 for graphs

$\textbf{Electrical Characteristics} \ \, \mathbf{T_{a}} \mathbf{=} 25 ^{\circ} \mathbf{C} \ \, \mathbf{unless \ otherwise \ \, noted}$

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = -2mA$, $I_B = 0$	-32		
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{E}=-1\mu A, I_{C}=0$	-5		
I _{CES}	Collector Cut-off Current	V _{CB} = -32V, V _{BE} =0		-20	
h _{FE}	DC Current Gain				
	: BCW61B	$V_{CE} = -5V, I_{C} = -10\mu A$	20		V
	: BCW61C		40		
	: BCW61D		100		
	: BCW61A	V_{CE} = -5V, I_{C} = -2mA	120	220	V
	: BCW61B		140	310	
	: BCW61C		250	460	
	: BCW61D		380	630	
	: BCW61A	V_{CE} = -5V, I_{C} = -50mA	60		nA
	: BCW61B		80		
	: BCW61C		100		
	: BCW61D		100		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I_{C} = -50mA, I_{B} = -1.25mA		-0.55	V
-		I_{C} = -10mA, I_{B} = -0.25mA		-0.25	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I_{C} = -50mA, I_{B} = -1.25mA	0.68	1.05	V
		$I_{C} = -10 \text{mA}, I_{B} = -0.25 \text{mA}$	0.6	0.85	V
V _{BE} (on)	Base-Emitter On Voltage	V _{CE} = -5V, I _C = -2mA	0.6	0.75	V
C _{ob}	Output Capacitance	V _{CB} = -10V, I _E =0 f=1MHz		6	pF
NF	Noise Figure	I_C = -0.2mA, V_{CE} = -5V R_G =20KΩ, f=1KHz		6	dB
t _{ON}	Turn On Time	I _C = -10mA, I _{B1} = -1mA		150	ns
t _{OFF}	Turn Off Time	V_{BB} = -3.6V, B22= -1mA R1=R2=5.0K Ω , R _L =990 Ω		800	ns

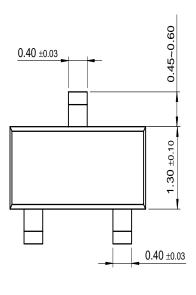
Marking Code

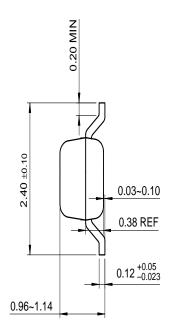
Туре	BCW61A	BCW61B	BCW61C	BCW61D
Mark.	BA	BB	BC	BD

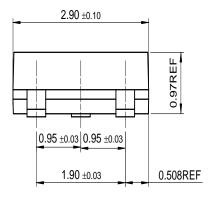


Package Demensions

SOT-23







Dimensions in Millimeters

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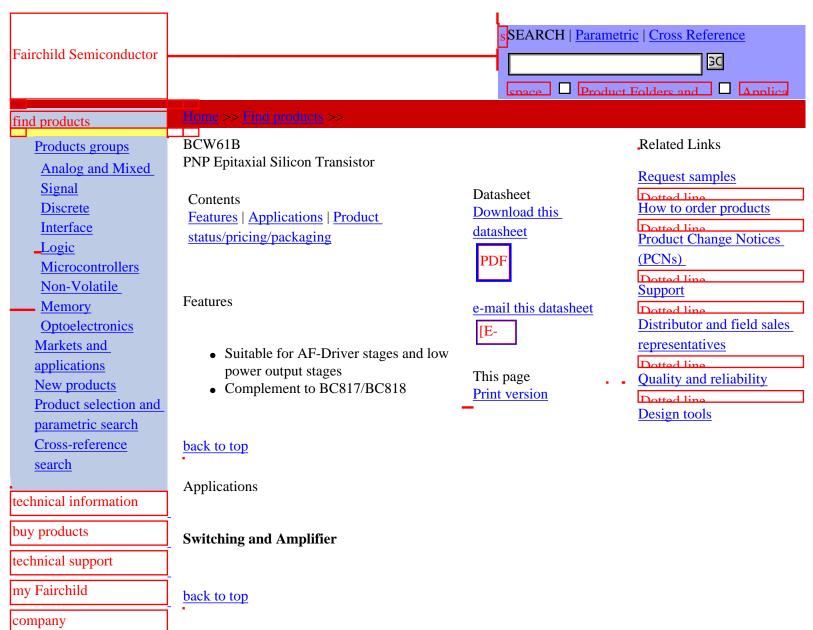
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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Package marking	Packing method
BCW61B	Full Production	\$0.0475	SOT-23	3	BB USE BINARY CODE	TAPE REEL
BCW61BMTF	Full Production	\$0.045	<u>SOT-23</u>	3	N/A	TAPE REEL

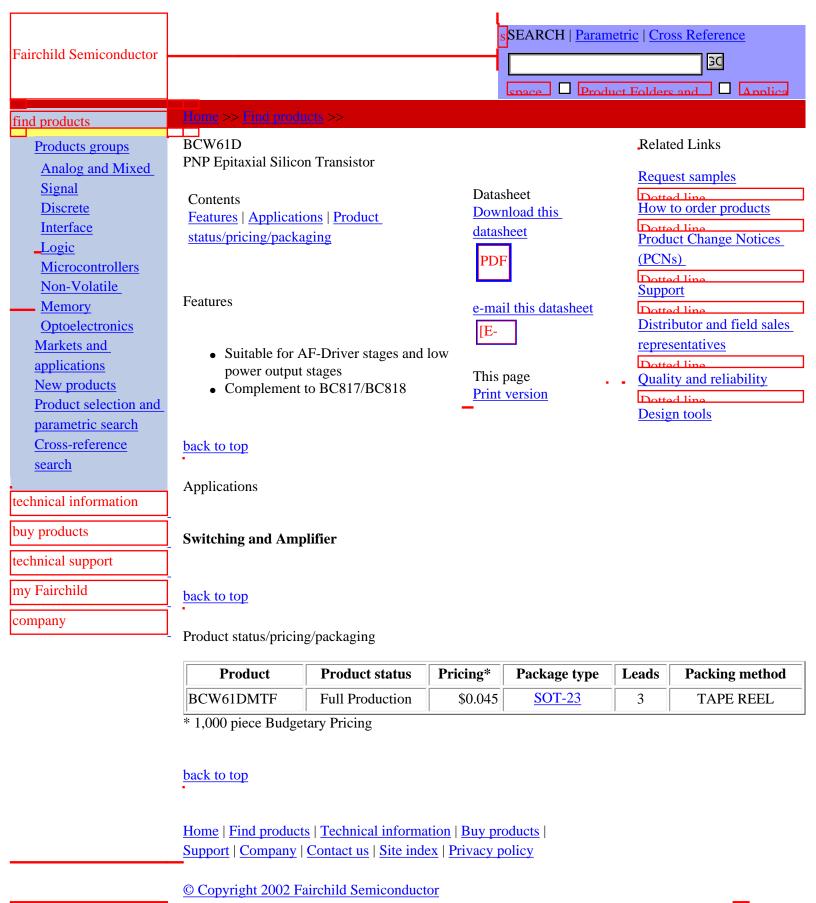
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