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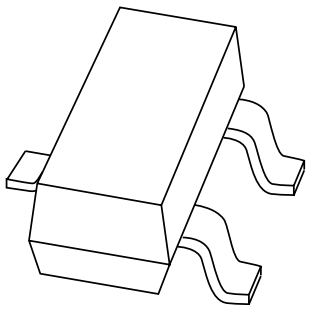
Team Nexperia

DATA SHEET



PMBD7100
High-speed double diode

Product data sheet

2003 Nov 07

NXP founded by Philips

High-speed double diode PMBD7100

FEATURES

- Small plastic SMD package
- High switching speed: max. 4 ns
- Continuous reverse voltage: max. 100 V
- Repetitive peak reverse voltage: max. 100 V
- Repetitive peak forward current: max. 450 mA.

APPLICATIONS

- High-speed switching in thick and thin-film circuits.

DESCRIPTION

The PMBD7100 consists of two high-speed switching diodes with common cathodes, fabricated in planar technology, and encapsulated in the small SOT23 SMD plastic package.

MARKING

TYPE NUMBER	MARKING CODE(1)
PMBD7100	*3A

Note

1. * = p: made in Hong Kong.
   * = t: made in Malaysia.
   * = W: made in China.

PINNING

PIN	DESCRIPTION
1	anode (a1)
2	anode (a2)
3	common connection

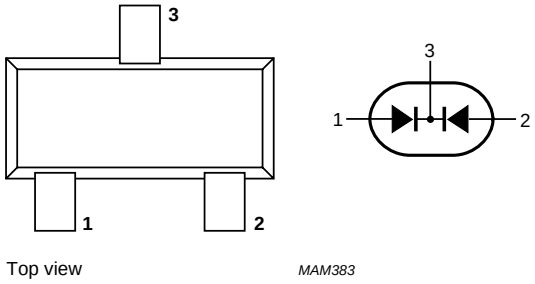


Fig.1 Simplified outline (SOT23) and symbol.

ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
PMBD7100	–	plastic surface mounted package; 3 leads	SOT23

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per diode					
V_{RRM}	repetitive peak reverse voltage		–	100	V
V_R	continuous reverse voltage		–	100	V
I_F	continuous forward current	single diode loaded; see Fig.2; note 1	–	215	mA
		double diode loaded; see Fig.2; note 1	–	125	mA
I_{FRM}	repetitive peak forward current		–	450	mA
I_{FSM}	non-repetitive peak forward current	square wave; T_j = 25 °C prior to surge; see Fig.4			
		t_p = 1 μs	–	4	A
		t_p = 1 ms	–	1	A
		t_p = 1 s	–	0.5	A
P_{tot}	total power dissipation	T_{amb} = 25 °C; note 1	–	250	mW
T_{stg}	storage temperature		−65	+150	°C
T_j	junction temperature		–	150	°C

Note

1. Device mounted on an FR4 printed-circuit board.

ELECTRICAL CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
Per diode				
V_F	forward voltage	see Fig.3		
		I_F = 1 mA	715	mV
		I_F = 10 mA	855	mV
		I_F = 50 mA	1	V
		I_F = 150 mA	1.25	V
I_R	reverse current	see Fig.5		
		V_R = 25 V	30	nA
		V_R = 100 V	2.5	μA
		V_R = 25 V; T_j = 150 °C	60	μA
		V_R = 100 V; T_j = 150 °C	100	μA
C_d	diode capacitance	V_R = 0 V; f = 1 MHz; see Fig.6	1.5	pF
t_{rr}	reverse recovery time	when switched from I_F = 10 mA to I_R = 10 mA; R_L = 100 Ω; measured at I_R = 1 mA; see Fig.7	4	ns
V_{fr}	forward recovery voltage	when switched from I_F = 10 mA to t_r = 20 nA; see Fig.8	1.75	V

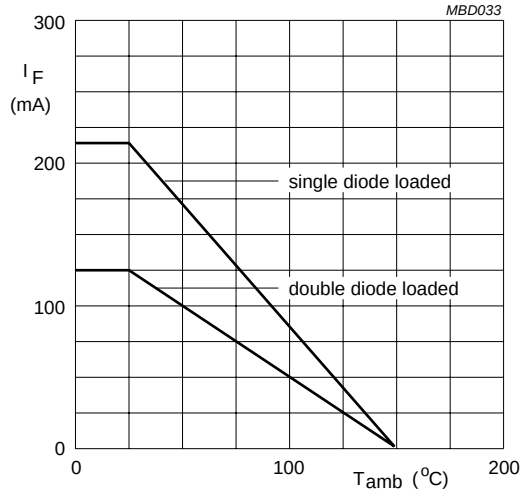
THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}tp}$	thermal resistance from junction to tie-point		360	K/W
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

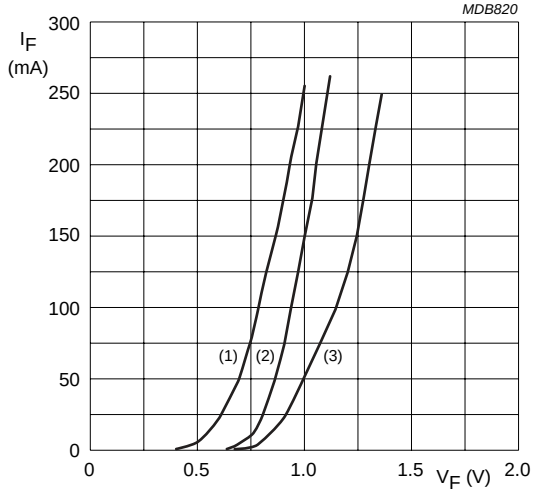
1. Device mounted on an FR4 printed-circuit board.

GRAPHICAL DATA



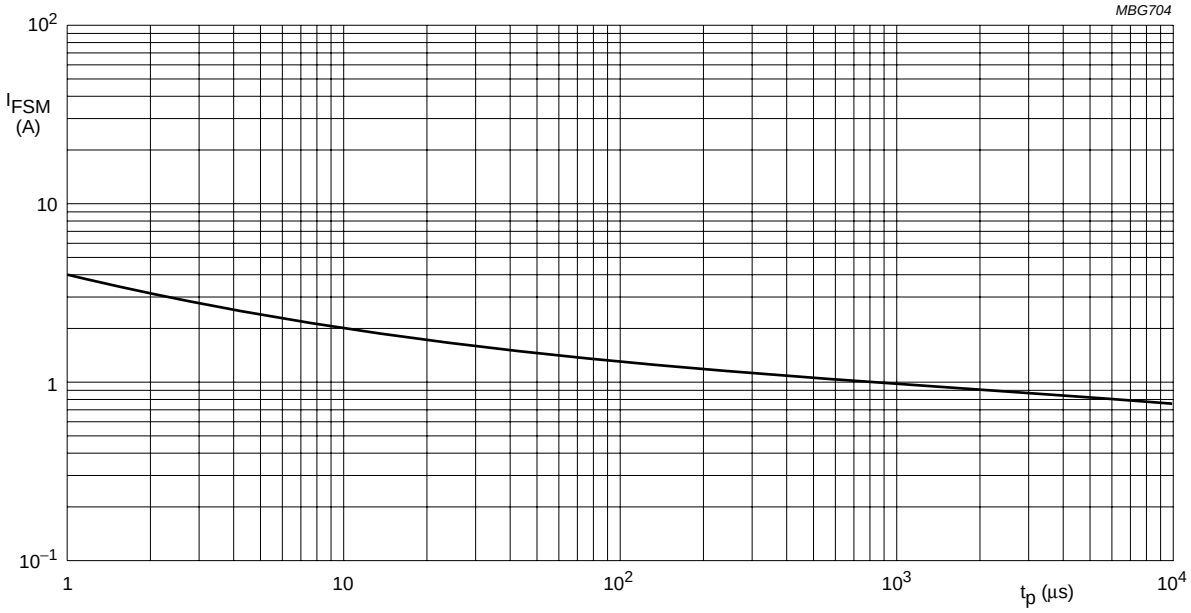
Device mounted on an FR4 printed-circuit board.

Fig.2 Maximum permissible continuous forward current as a function of ambient temperature.



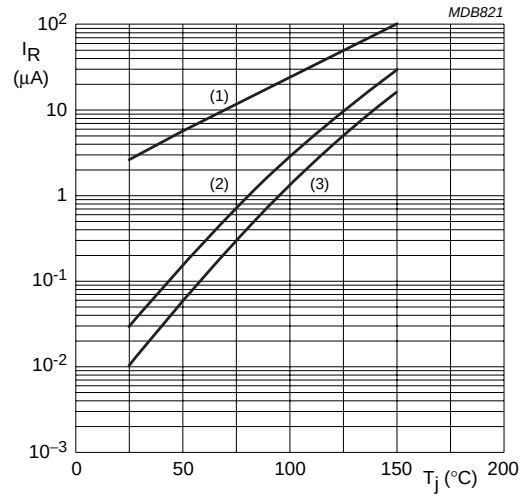
(1) T_j = 150 °C; typical values.
(2) T_j = 25 °C; typical values.
(3) T_j = 25 °C; maximum values.

Fig.3 Forward current as a function of forward voltage.



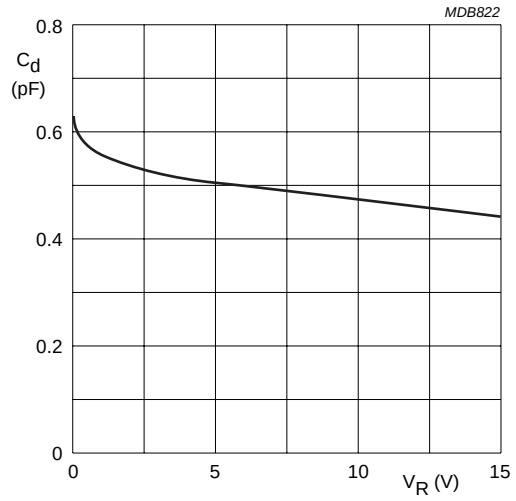
Based on square wave currents.
T_j = 25 °C prior to surge.

Fig.4 Maximum permissible non-repetitive peak forward current as a function of pulse duration.



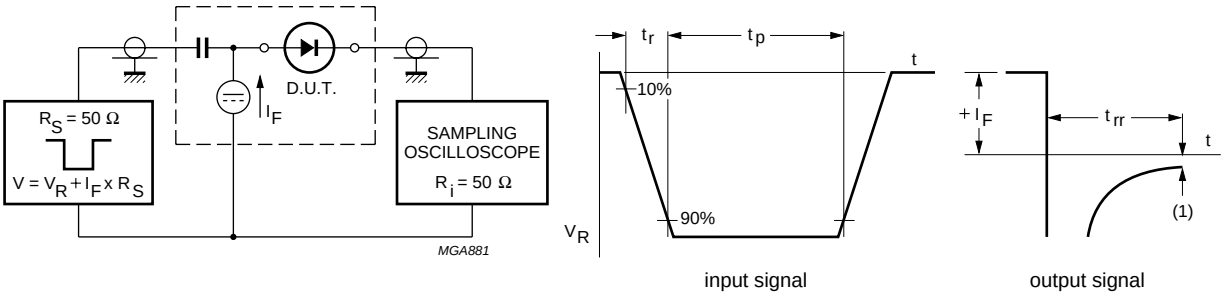
(1) V_R = 100 °C; maximum values.
(2) V_R = 100 °C; typical values.
(3) V_R = 25 °C; typical values.

Fig.5 Reverse current as a function of junction temperature.



f = 1 MHz; T_j = 25 °C.

Fig.6 Diode capacitance as a function of reverse voltage; typical values.



(1) I_R = 1 mA.

Fig.7 Reverse recovery voltage test circuit and waveforms.

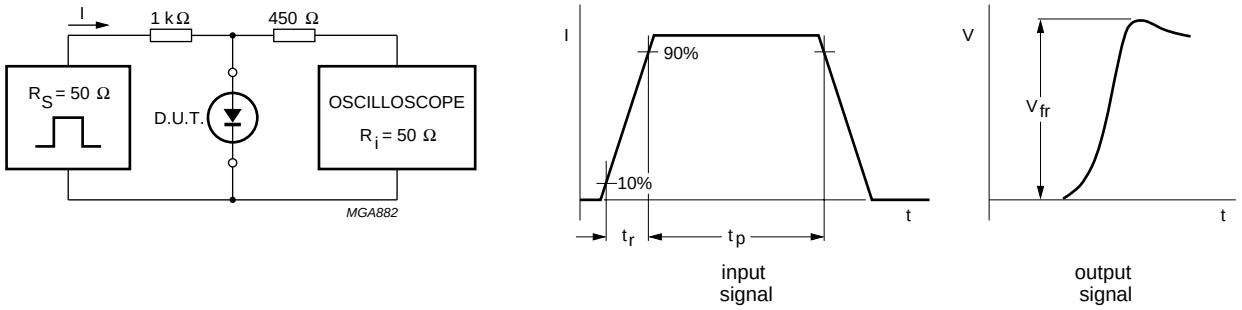
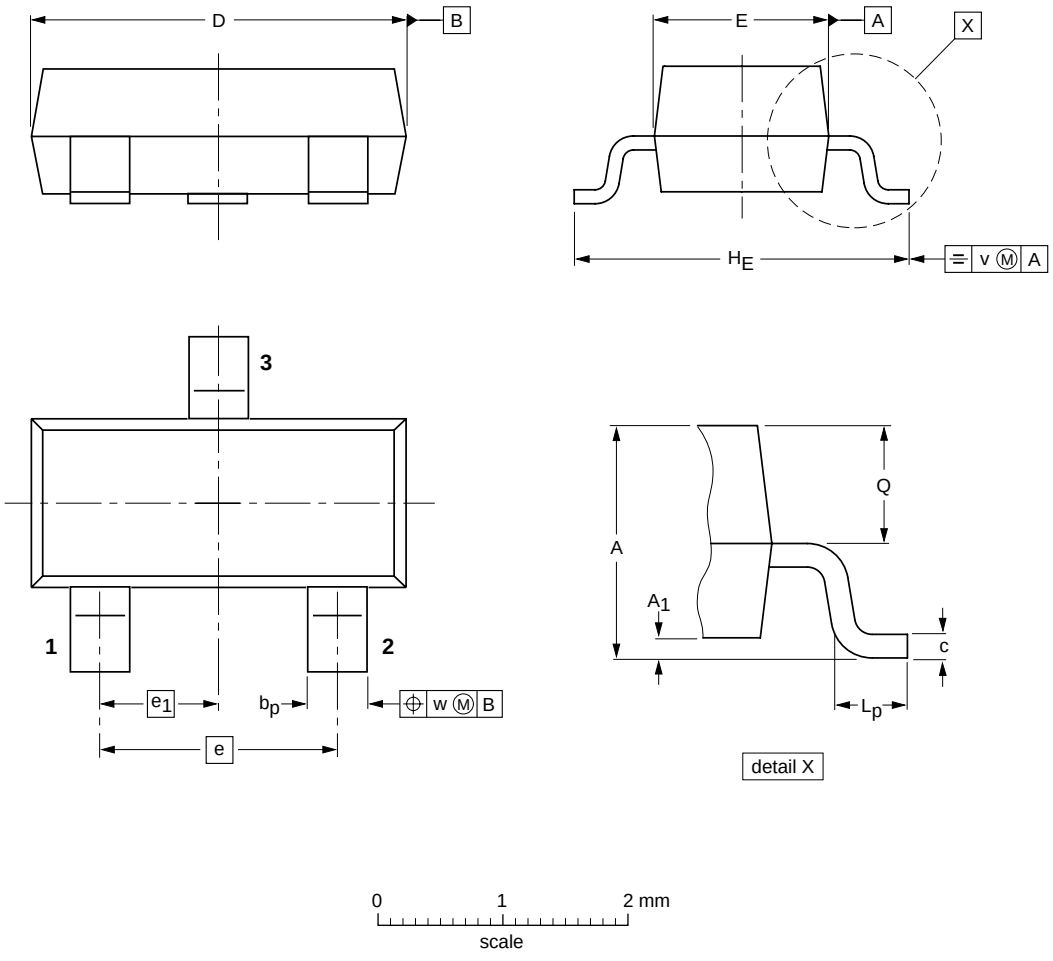


Fig.8 Forward recovery voltage test circuit and waveforms.

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads **SOT23**



DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1 max.	b_p	c	D	E	e	e_1	H_E	L_p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23		TO-236AB				~~97-02-28~~ 99-09-13

DATA SHEET STATUS

DOCUMENT STATUS[1]	PRODUCT STATUS[2]	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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NXP Semiconductors

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