

## Features

- $BV_{CEO} > -100V$
- Small Form Factor Thermally Efficient Package. Enables Higher Density End Products
- $I_C = -5A$  Continuous Collector Current
- $I_{CM} = -10A$  Peak Pulse Current
- Low Saturation Voltage  $V_{CE(sat)} < -100mV @ -1A$
- $R_{SAT} = 60m\Omega$  for a Low equivalent On-Resistance
- $h_{FE}$  Specified up to -10A for a High Gain Hold-Up
- Complementary NPN Type: DXTN03100CFG
- Rated to +175°C – Ideal For High Temperature Environment
- Wettable Flank For Improved Optical Inspection
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

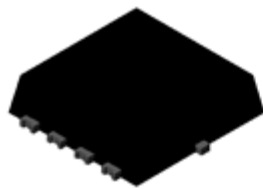
## Mechanical Data

- Case: PowerDI<sup>®</sup> 3333-8
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads Solderable per MIL-STD-202, Method 208 <sup>Ⓔ</sup>
- Weight: 0.03 grams (Approximate)

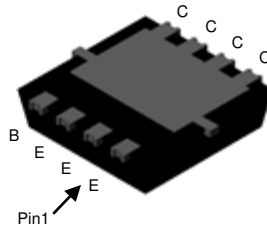
## Applications

- Motor Driving
- Line Switching
- High Side Switches

PowerDI3333-8 (SWP) (Type UX)

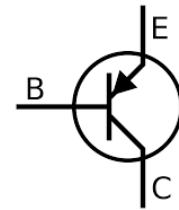


Top View



Bottom View

Equivalent Circuit



Device Symbol

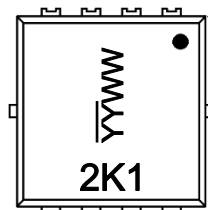
## Ordering Information (Note 4)

Part Number	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DXTP03100CFG-7	2K1	7	12	2,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information

PowerDI3333-8 (SWP) (Type UX)



- 2K1 = Product Type Marking Code
- YYWW = Date Code Marking
- YY = Last Two Digits of Year (ex: 19 = 2019)
- WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-110	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-100	V
Emitter-Base Voltage	V <sub>EBO</sub>	-7	V
Continuous Collector Current	I <sub>C</sub>	-5	A
Peak Pulse Current	I <sub>CM</sub>	-10	A

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

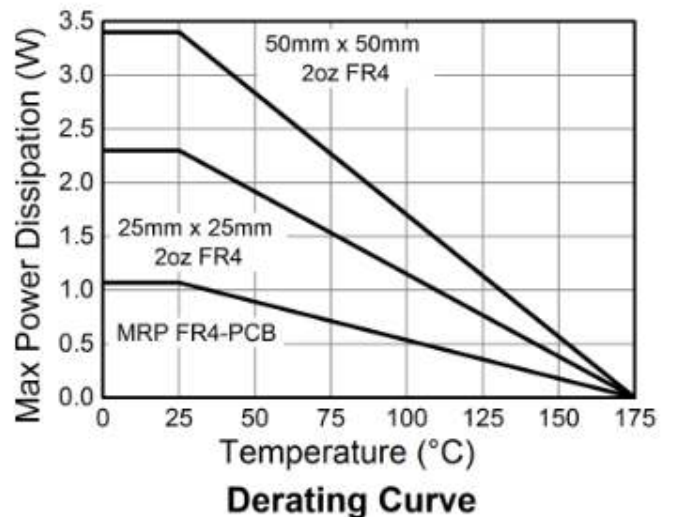
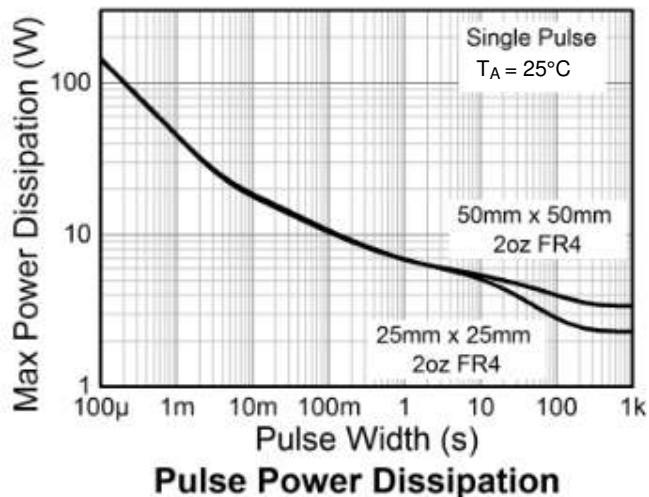
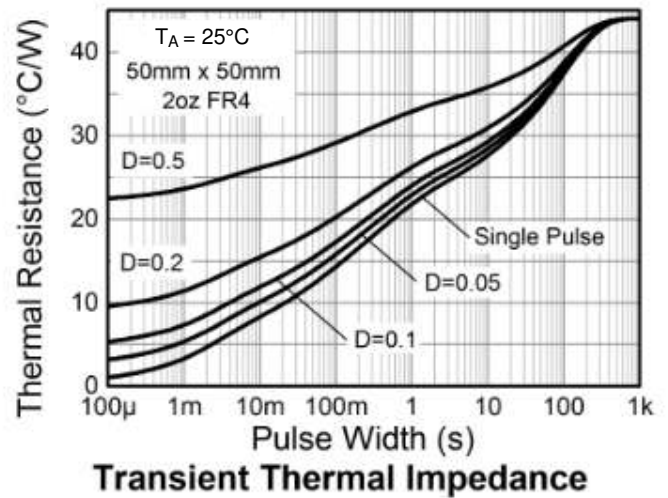
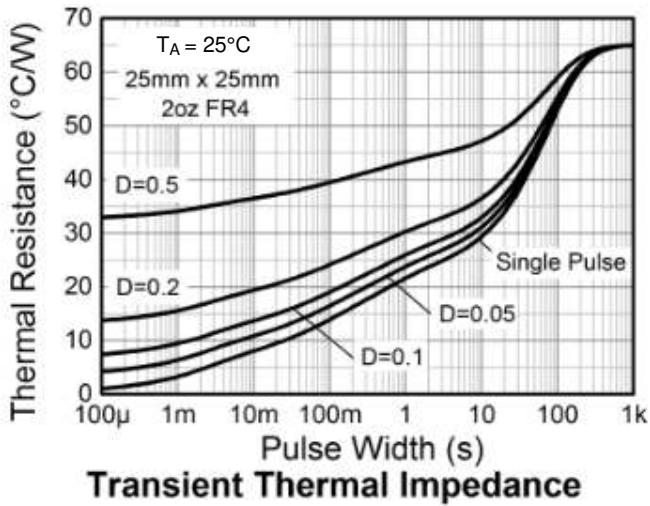
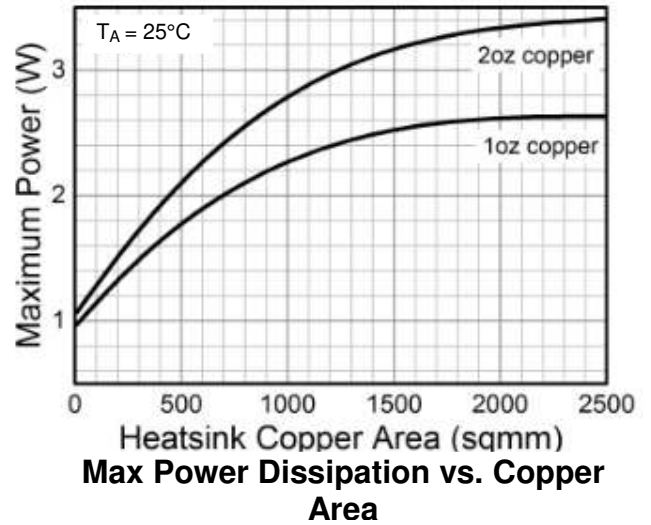
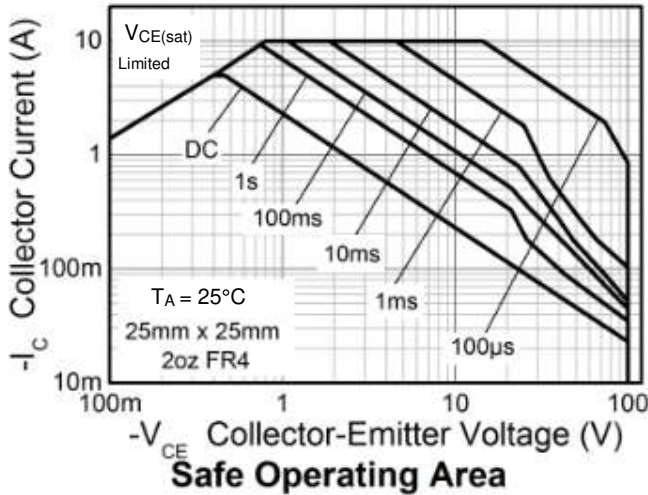
Characteristic	Symbol	Value	Unit	
Power Dissipation	P <sub>D</sub>	(Note 5)	1.07	W
		(Note 6)	2.3	W
		(Note 7)	3.4	W
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	(Note 5)	140	°C/W
		(Note 6)	65	°C/W
		(Note 7)	44	°C/W
Thermal Resistance, Junction to Leads (Note 8)	R <sub>θJL</sub>	6	°C/W	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C	

**ESD Ratings** (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	≥ 400	V	C

- Notes:
5. For a device mounted with the collector tab on MRP FR-4 PCB; device is measured under still air conditions whilst operating in a steady-state.
  6. Same as Note 5, except the device is mounted on 25mm x 25mm 2oz copper.
  7. Same as Note 5, except the device is mounted on 50mm x 50mm 2oz copper.
  8. Thermal resistance from junction to solder-point (at the collector tab).
  9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

**Thermal Characteristics and Derating Information**

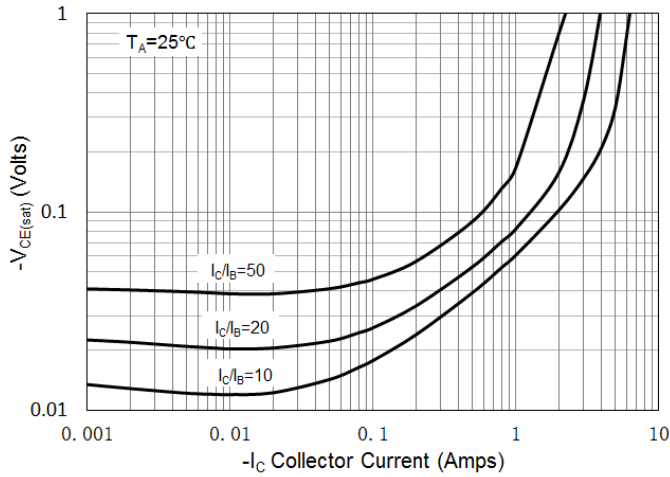


**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

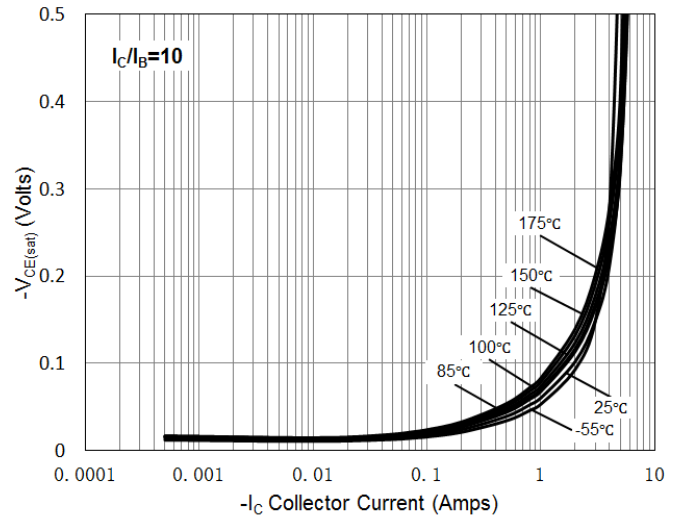
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-110	-138	—	V	I <sub>C</sub> = -100μA
Collector-Emitter Breakdown Voltage (Note 10)	BV <sub>CEO</sub>	-100	-116	—	V	I <sub>C</sub> = -10mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-7	-8.8	—	V	I <sub>E</sub> = -100μA
Collector-Base Cut-Off Current	I <sub>CBO</sub>	—	-1	-50	nA	V <sub>CB</sub> = -100V
		—	-0.07	10	μA	V <sub>CB</sub> = -100V, T <sub>A</sub> = +125°C
Collector-Emitter Cut-Off Current (R ≤ 1kΩ)	I <sub>CER</sub>	—	-2	-50	nA	V <sub>CB</sub> = -100V
		—	-2.3	50	μA	V <sub>CB</sub> = -100V, T <sub>A</sub> = +125°C
Emitter Cut-Off Current	I <sub>EBO</sub>	—	-1	-20	nA	V <sub>EB</sub> = -6V
Static Forward Current Transfer Ratio (Note 10)	h <sub>FE</sub>	250	421	—	—	I <sub>C</sub> = -10mA, V <sub>CE</sub> = -2V
		200	338	800	—	I <sub>C</sub> = -1A, V <sub>CE</sub> = -2V
		180	323	—	—	I <sub>C</sub> = -1.2A, V <sub>CE</sub> = -2V
		150	295	—	—	I <sub>C</sub> = -1.5A, V <sub>CE</sub> = -2V
		35	57	—	—	I <sub>C</sub> = -3A, V <sub>CE</sub> = -2V
		20	29	—	—	I <sub>C</sub> = -4A, V <sub>CE</sub> = -2V
Collector-Emitter Saturation Voltage (Note 10)	V <sub>CE(sat)</sub>	—	-17.5	-40	mV	I <sub>C</sub> = -100mA, I <sub>B</sub> = -10mA
		—	-60	-100	mV	I <sub>C</sub> = -1A, I <sub>B</sub> = -100mA
		—	-102	-180	mV	I <sub>C</sub> = -2A, I <sub>B</sub> = -200mA
		—	-212	-380	mV	I <sub>C</sub> = -4A, I <sub>B</sub> = -400mA
Base-Emitter Saturation Voltage (Note 10)	V <sub>BE(sat)</sub>	—	-963	-1.1	mV	I <sub>C</sub> = -4A, I <sub>B</sub> = -400mA
Base-Emitter Turn-On Voltage (Note 10)	V <sub>BE(on)</sub>	—	-888	-1	mV	I <sub>C</sub> = -4A, V <sub>CE</sub> = -2V
Output Capacitance	C <sub>obo</sub>	—	42	—	pF	V <sub>CB</sub> = -10V, f = 1MHz
Transition Frequency	f <sub>T</sub>	—	125	—	MHz	V <sub>CE</sub> = -10V, I <sub>C</sub> = -100mA f = 50MHz
Switching Characteristics	t <sub>delay</sub>	—	6.8	—	ns	V <sub>CC</sub> = -10V, I <sub>C</sub> = -1A I <sub>B1</sub> = -I <sub>B2</sub> = -100mA
	t <sub>rise</sub>	—	259	—	ns	
	t <sub>storage</sub>	—	1718	—	ns	
	t <sub>fall</sub>	—	185	—	ns	

Note: 10. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

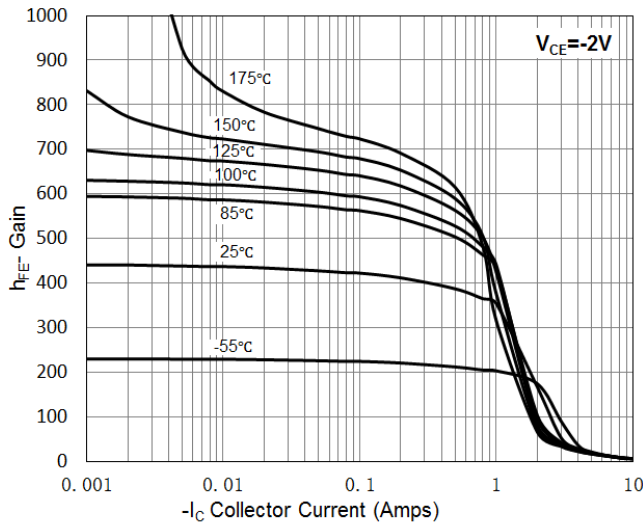
**Typical Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)



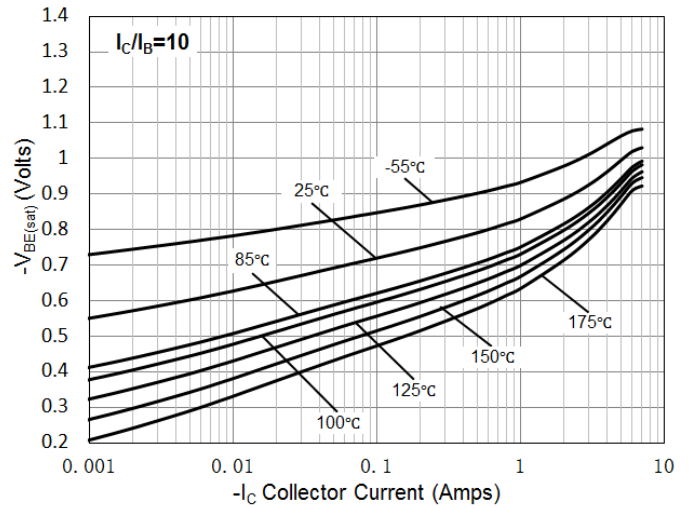
**$V_{CE(sat)}$  vs  $I_C$**



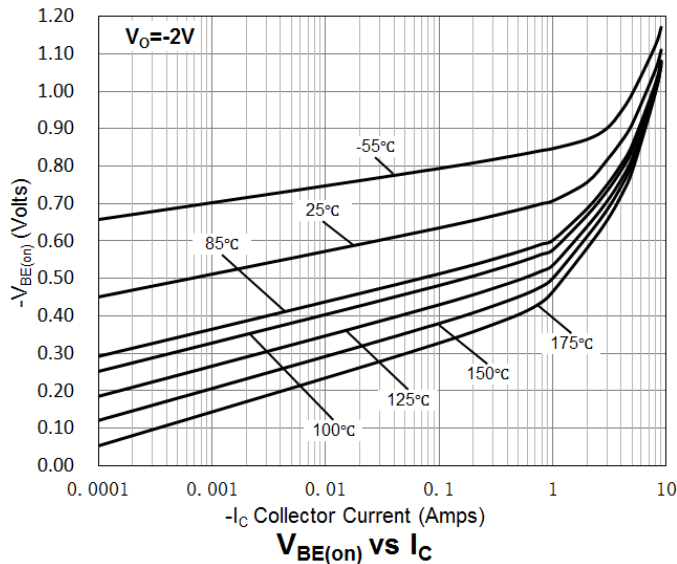
**$V_{CE(sat)}$  vs  $I_C$**



**$h_{FE}$  vs  $I_C$**



**$V_{BE(sat)}$  vs  $I_C$**

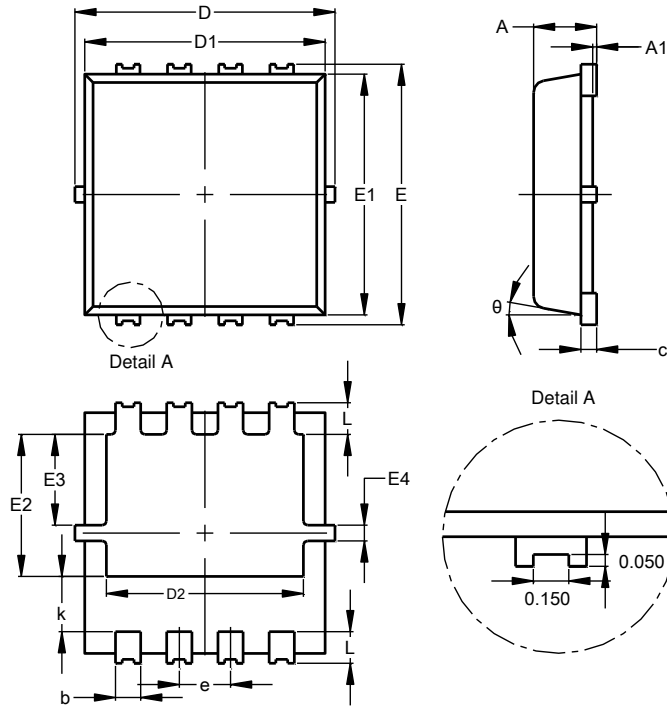


**$V_{BE(on)}$  vs  $I_C$**

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI3333-8 (SWP) (Type UX)**

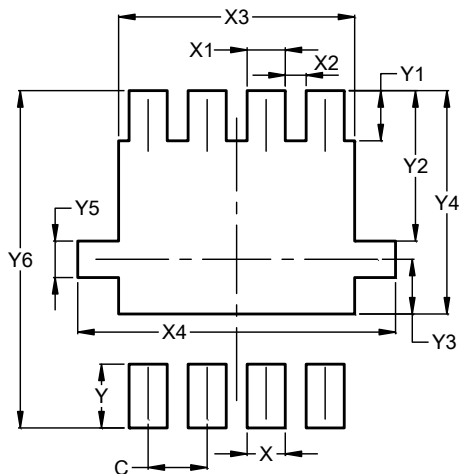


PowerDI3333-8 (SWP) (Type UX)			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	--
b	0.25	0.40	0.32
c	0.10	0.25	0.15
D	3.20	3.40	3.30
D1	2.95	3.15	3.05
D2	2.30	2.70	2.50
E	3.20	3.40	3.30
E1	2.95	3.15	3.05
E2	1.60	2.00	1.80
E3	0.95	1.35	1.15
E4	0.10	0.30	0.20
e	--	--	0.65
k	0.50	0.90	0.70
L	0.30	0.50	0.40
θ	0°	12°	10°
<b>All Dimensions in mm</b>			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI3333-8 (SWP) (Type UX)**



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
X3	2.600
X4	3.500
Y	0.700
Y1	0.550
Y2	1.650
Y3	0.600
Y4	2.450
Y5	0.400
Y6	3.700

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.

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