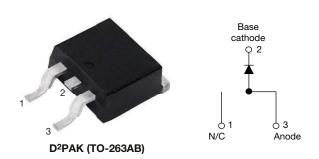


COMPLIANT

HALOGEN

FREE

High Performance Schottky Rectifier, 20 A



PRIMARY CHARACTERISTICS								
I _{F(AV)}	20 A							
V _R	15 V							
V _F at I _F	0.33 V							
I _{RM} max.	600 mA at 100 °C							
T _J max.	125 °C							
E _{AS}	10 mJ							
Package	D ² PAK (TO-263AB)							
Circuit configuration	Single							

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- · Center tap module
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	20	А						
V _{RRM}		15	V						
I _{FSM}	t _p = 5 μs sine	700	Α						
V _F	19 A _{pk} , T _J = 125 °C (typical)	0.25	V						
T _J	Range	-55 to +125	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VS-STPS20L15G-M3	UNITS					
Maximum DC reverse voltage	T _{.1} = 100 °C	15	V						
Maximum working peak reverse voltage	V_{RWM}	1J=100 C	15	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	I _{F(AV)} 50 % duty cycle at T _C = 85 °C, rectangular waveform						
Maximum peak one cycle	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load	700	Α			
non-repetitive surge current See fig. 7		10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	330				
Non-repetitive avalanche energy	E _{AS}	$T_J = 25$ °C, $I_{AS} = 2$ A, $L = 6$ mH	10	mJ				
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in Frequency limited by T _J maximum	2	Α				

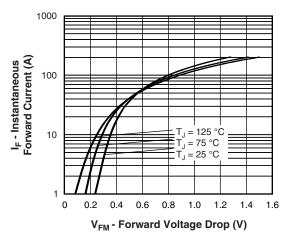


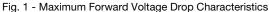
ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS			
Forward voltage drop See fig. 1		19 A	T _{.1} = 25 °C	-	0.41	V		
	V _{FM} ⁽¹⁾	40 A	1J=25 C	ı	0.52			
	VFM ('')	19 A	T _{.1} = 125 °C	0.25	0.33	ľ		
		40 A	1j = 125 C	0.37	0.50			
Reverse leakage current	ı (1)	T _J = 25 °C	V Dated V	-	10	mA		
See fig. 2	I _{RM} ⁽¹⁾	T _J = 100 °C	V _R = Rated V _R	-	600	IIIA		
Threshold voltage	V _{F (TO)}	T T mayimum						
Forward slope resistance	r _t	$T_J = T_J$ maximum	7.6		mW			
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	-	2000	pF			
Typical series inductance	L _S	Measured lead to lead 5 r	8	-	nΗ			
Maximum voltage rate of change	dV/dt	Rated V _R	000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction tempera	ature range	TJ		-55 to +125	°C			
Maximum storage tempera	ture range	T _{Stg}		-55 to +150	-0			
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	1.5				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased (for TO-220)	0.50	°C/W			
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation (for D ² PAK)	40				
Approximate weight				2	g			
Approximate weight				0.07	OZ.			
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf · cm			
wounting torque	maximum			12 (10)	(lbf·in)			
Marking device			Case style D ² PAK (TO-263AB)	STPS2	DL15G			





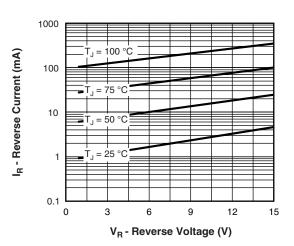


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

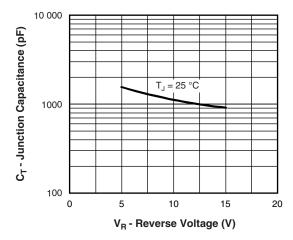


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

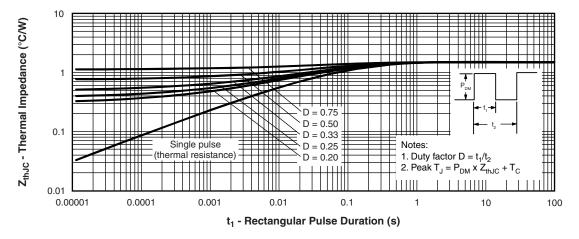


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

Allowable Case Temperature (°C)

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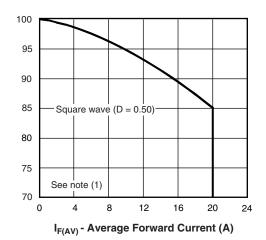


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

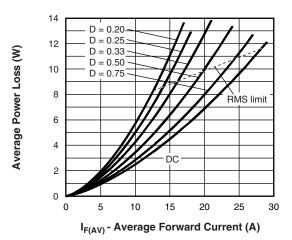


Fig. 6 - Forward Power Loss Characteristics

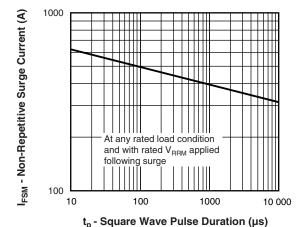


Fig. 7 - Maximum Non-Repetitive Surge Current

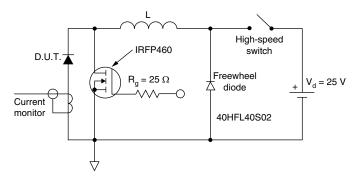


Fig. 8 - Unclamped Inductive Test Circuit

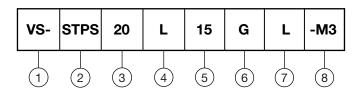
Note

 $^{(1)}$ Formula used: $T_C = T_J$ - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = $I_{F(AV)}$ x V $_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = V $_{R1}$ x I $_{R}$ (1 - D); I $_{R}$ at V $_{R1}$ = 80 % rated V $_{R1}$



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Essential part number

Current rating (20 = 20 A)

4 - Low voltage

Voltage rating (15 = 15 V)

6 - G = D²PAK package

7 - • None = tube

• L = tape and reel (left oriented)

• R = tape and reel (right oriented)

8 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

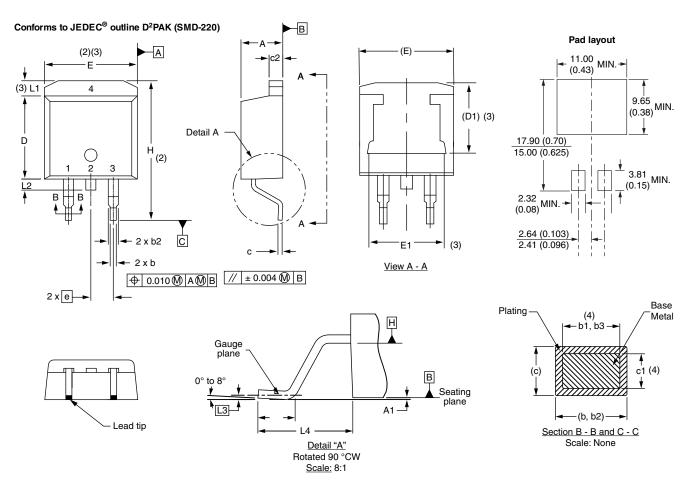
ORDERING INFORMATION (Example)								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-STPS20L15GL-M3	800	13" diameter plastic tape and reel						
VS-STPS20L15G-M3	50	Antistatic plastic tubes						
VS-STPS20L15GR-M3	800	13" diameter plastic tape and reel						

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96164				
Part marking information	www.vishay.com/doc?95444				
Packaging information	www.vishay.com/doc?96424				



D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES		SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inches
- (7) Outline conforms to JEDEC® outline TO-263AB

Revision: 13-Jul-17 Document Number: 96164



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