# DISCRETE SEMICONDUCTORS

# DATA SHEET

# **BFS17**NPN 1 GHz wideband transistor

**Product specification** 

September 1995



**BFS17** 

# **DESCRIPTION**

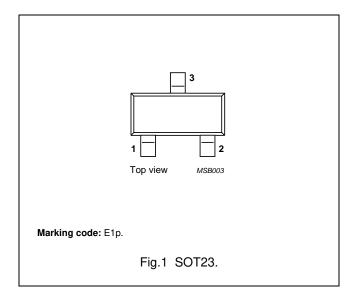
NPN transistor in a plastic SOT23 package.

#### **APPLICATIONS**

- A wide range of RF applications such as:
  - Mixers and oscillators in TV tuners
  - RF communications equipment.

# **PINNING**

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



# **QUICK REFERENCED DATA**

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	_	25	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	15	V
I <sub>C</sub>	DC collector current		_	25	mA
P <sub>tot</sub>	total power dissipation	up to T <sub>s</sub> = 70 °C; note 1	_	300	mW
f <sub>T</sub>	transition frequency	$I_C = 25 \text{ mA}; V_{CE} = 5 \text{ V}; f = 500 \text{ MHz}; T_j = 25 °C$	1	_	GHz
F	noise figure	$I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}; R_S = 50 \Omega; f = 500 \text{ MHz};$ $T_i = 25 ^{\circ}\text{C}$	4.5	_	dB

# **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	25	V
$V_{CEO}$	collector-emitter voltage	open base	-	15	V
$V_{EBO}$	emitter-base voltage	open collector	-	2.5	V
I <sub>C</sub>	DC collector current		-	25	mA
I <sub>CM</sub>	peak collector current		-	50	mA
P <sub>tot</sub>	total power dissipation	up to $T_s = 70  ^{\circ}C$ ; note 1	-	300	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C

# Note to the Quick reference data and the Limiting values

1.  $T_s$  is the temperature at the soldering point of the collector pin.

NXP Semiconductors Product specification

# NPN 1 GHz wideband transistor

BFS17

# THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-s</sub>	thermal resistance from junction to soldering point	up to $T_s = 70 ^{\circ}C$ ; note 1	260	K/W

# Note

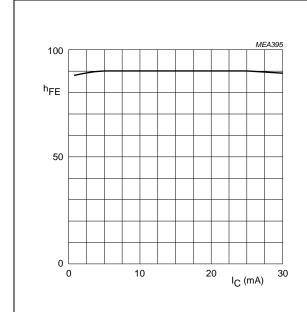
1.  $T_s$  is the temperature at the soldering point of the collector pin.

# **CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise specified.

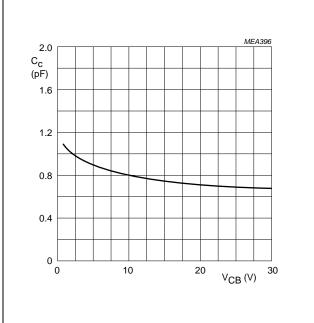
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 10 V	_	_	10	nA
h <sub>FE</sub>	DC current gain	$I_C = 2 \text{ mA}; V_{CE} = 1 \text{ V}$	25	90	_	
		$I_C = 25 \text{ mA}; V_{CE} = 1 \text{ V}$	25	90	_	
f <sub>T</sub>	transition frequency	$I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}; f = 500 \text{ MHz}$	-	1	_	GHz
		$I_C = 25 \text{ mA}; V_{CE} = 5 \text{ V}; f = 500 \text{ MHz}$	_	1.6	_	GHz
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	0.8	1.5	pF
Ce	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = 0.5 \text{ V}$ ; $f = 1 \text{ MHz}$	-	_	2	pF
C <sub>re</sub>	feedback capacitance	I <sub>C</sub> = 1 mA; V <sub>CE</sub> = 5 V; f = 1 MHz	-	0.65	_	pF
F	noise figure	$I_C$ = 2 mA; $V_{CE}$ = 5 V; $R_S$ = 50 $\Omega$ ; $f$ = 500 MHz	_	4.5	_	dB

**BFS17** 



 $V_{CE}=1~V;\,T_{j}=25~^{\circ}C.$ 

Fig.2 DC current gain as a function of collector current.



 $I_E=i_e=0;\,f=1\,\,MHz;\,T_j=25\,\,^{\circ}C.$ 

Fig.3 Collector capacitance as a function of collector-base voltage.

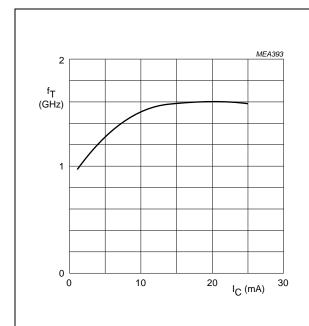
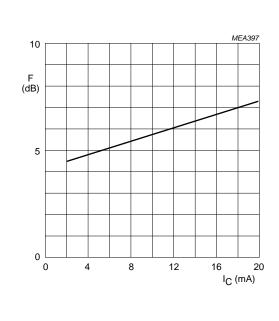


Fig.4 Transition frequency as a function of collector current.



 $V_{CE}$  = 5 V;  $R_{S}$  = 50  $\Omega;$  f = 500 MHz;  $T_{j}$  = 25 °C.

Fig.5 Minimum noise figure as a function of collector current.

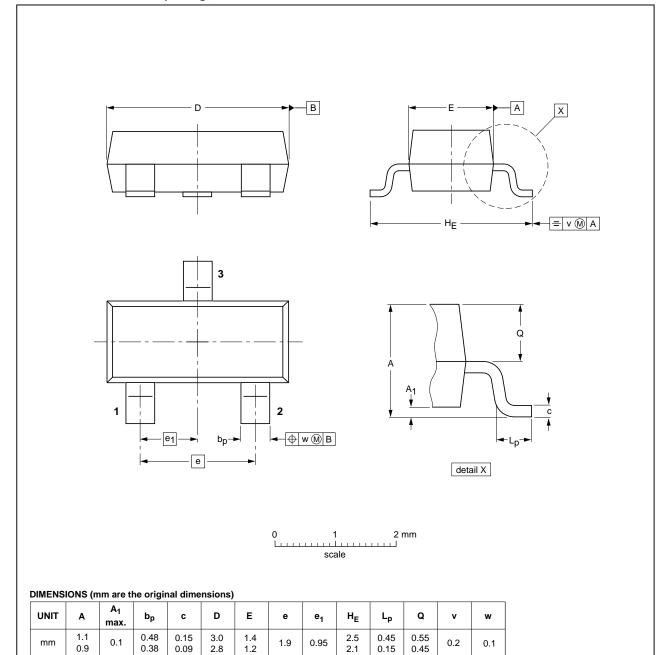
 $V_{CE} = 5 \text{ V}$ ; f = 500 MHz;  $T_j = 25 \,^{\circ}\text{C}$ .

BFS17

# **PACKAGE OUTLINE**

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE REFERENCES			EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION ISSUE DA	
SOT23		TO-236AB				<del>-04-11-04-</del> 06-03-16

BFS17

#### **DATA SHEET STATUS**

DOCUMENT STATUS(1)	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

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Printed in The Netherlands R77/02/pp8 Date of release: September 1995